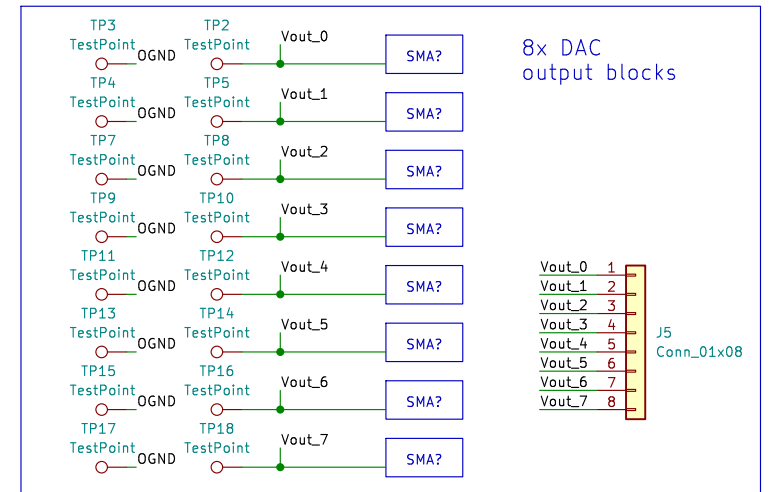
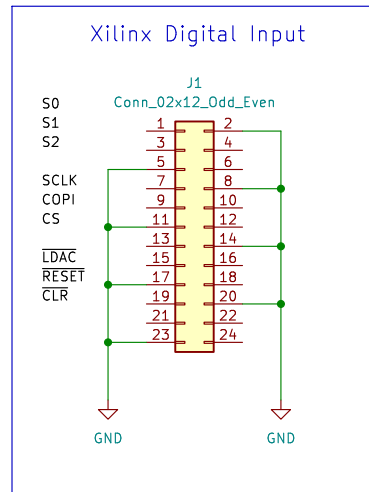
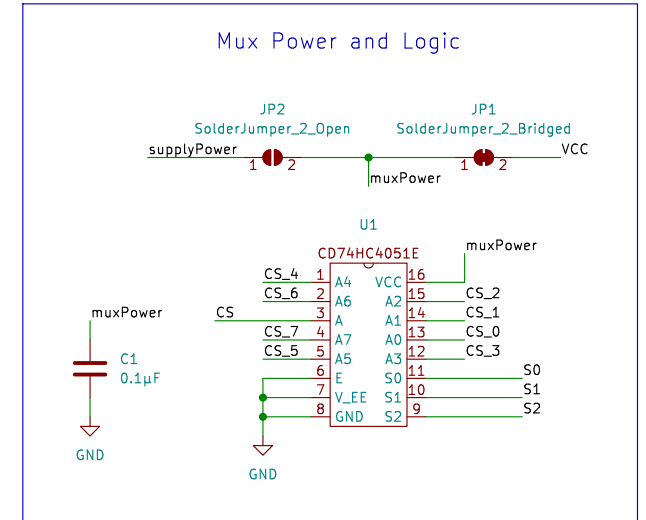
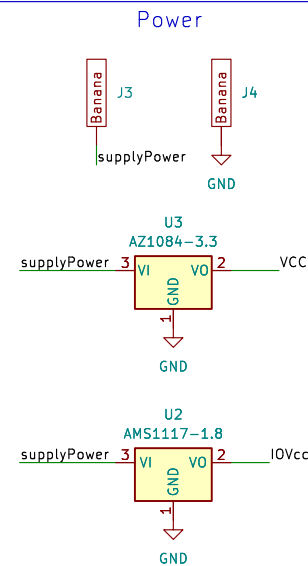
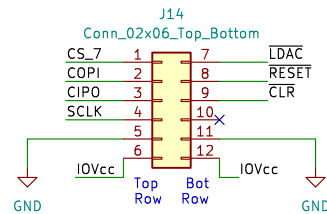
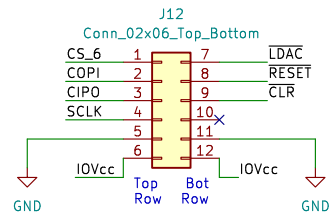
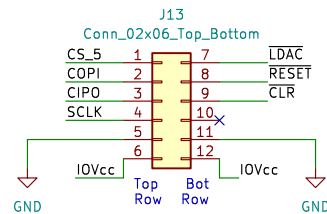
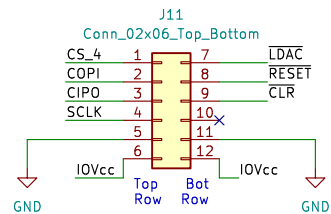
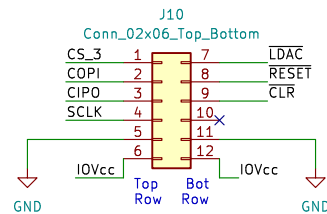
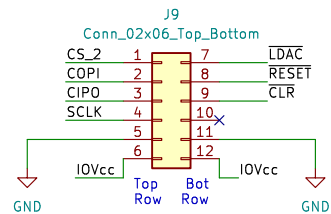
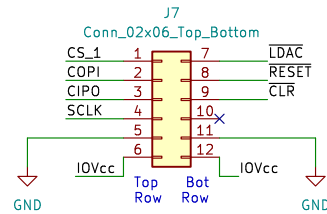
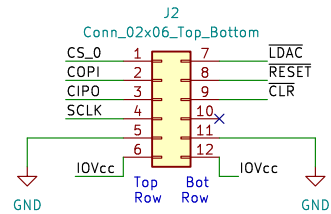
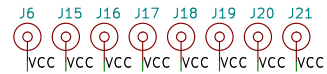
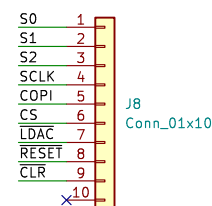


### VCC Ports for Side



### Additional Testpoints



PCBv1.1 HRL Labs Harvey Mudd Clinic Team

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