# H. Race Hunter

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## Objective

Self-motivated computer engineering student experienced with enterprise development tools and project management. Interested in large-scale, mission-critical systems. Seeking full-time entry-level engineering position starting in January 2021.

### Skills

**Languages** – C/C++, Java, JavaScript/React.JS/JSON, Python

Tools – git, Docker, Jenkins, Trello/Asana/Jira, EAGLE PCB, Quartus Prime, IntelliJ, VS code

Hardware – Verilog, digital logic, analog circuit analysis

# **Professional Experience**

Intern, Tesla Inc.

Sep 2020 – Dec 2020

**CS Engineering Intern**, Sun Engineering & Technology International

June 2020 - Sep 2020

• Lead a team of 4 to design, code, and deploy a .NET automation tool spanning an hour-long workflow for over 400 files per project, with emphasis on adaptability for future projects

**Software Engineering Intern, Fitbit, Inc.** 

June 2019 - Dec 2019

Full time internship (June – September)

- Created React.JS web app, increasing productivity by replacing CLI tools that required developer involvement, saving several hours each week for a team of 20
- Automated update distribution (CI/CD) and testing, increasing productivity and reducing debugging time Part time internship (September – December)
  - Created Java microservice to automate EOL for consumer firmware, saving 2 hours each week and reducing consumer update time

Business Owner, Race to the Top

June 2014 - June 2015

Sold and installed industrial surveillance systems to fund a full-time religious mission for 2 years

# Notable Coursework - Computer Engineering at UC San Diego (December 2020)

### Advanced Software Engineering (CSE 112)

Spring 2020

 Lead a group of 11 to design, document, and build a Chrome extension to boost remote developer productivity

#### Advanced Digital Design Project (ECE 111)

Spring 2020

Conduct a group of 3 to design and synthesize a bitcoin miner using SystemVerilog

#### Intro to Computer Architecture (CSE 141)

Winter 2020

- Optimized a simplified version of AlexNet for a speed up of 4.8x using a Skylake processor
- Identified hot functions using gprof and utilized pipelining, loop tiling, loop reordering, and multithreading

#### Quadcopter Class (CSE 176E)

Spring 2019

- Worked in a team of 2 to design, manufacture, and assemble a custom PCB using EAGLE software
- Developed various safety mechanisms in firmware to reduce operator injury, including safe startup, auto timeout, and interference rejection

### Other Coursework

Health Care Robotics Digital Logic Design + Lab Ethics at Work Advanced Data Structures Intro to Data Science Enterprise Finance Theory of Computability Algorithms Principles of Accounting