

BLG 354E Homework - 4

Yunus Güngör

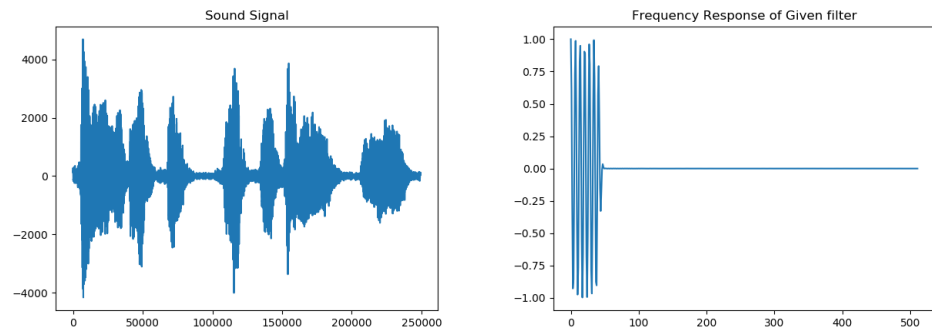
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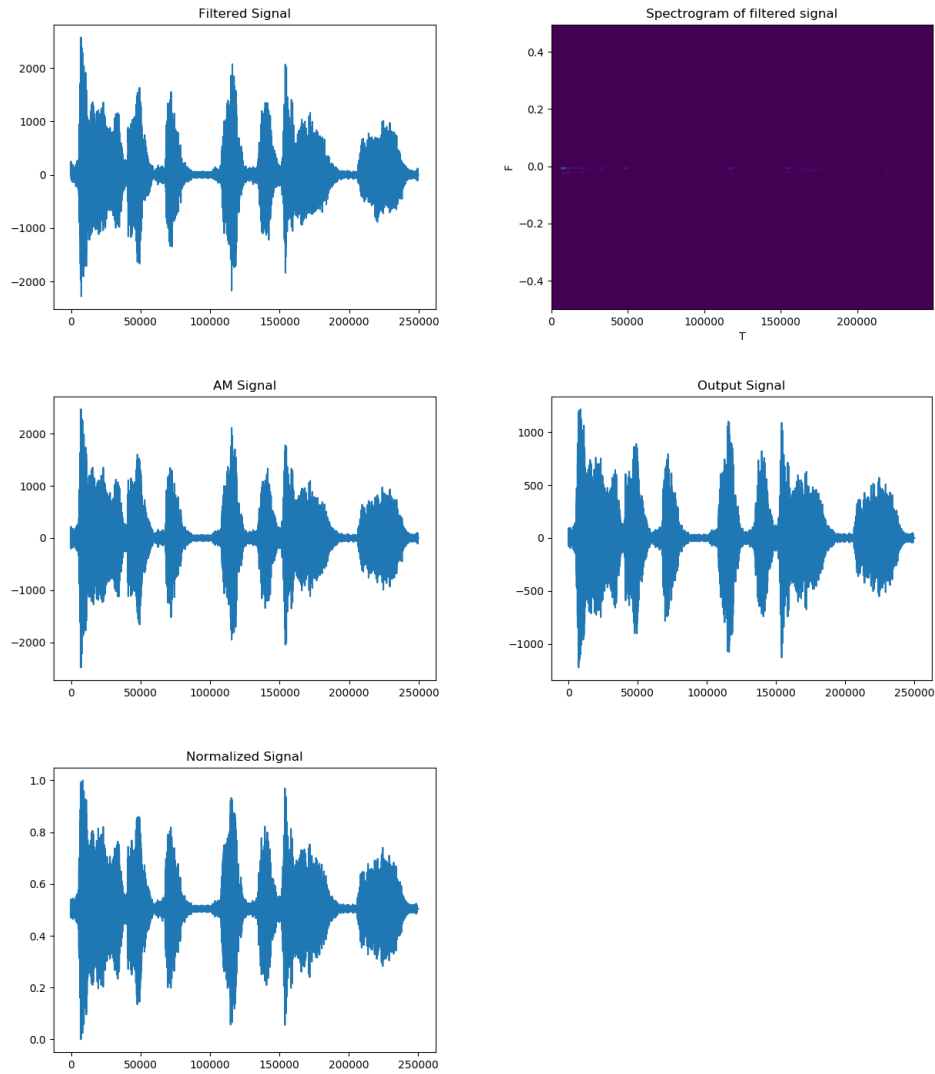
1 Answers

This homework only includes answers to given questions

1)

Required codes can be found at attached file. Results gotten:





Demodulator output increases, decreases and gets corrupted according to phase, and its zero for $\pi/2$.

When demodulator frequency has been set 10Hz higher than the modulator, signal becomes corrupted