CSE 321a

Computer Organization (1) تنظيم الحاسبات (1)



3rd year, Computer Engineering Fall 2017



Lecture #8

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Credits to Dr. Ahmed Abdul-Monem Ahmed for the slides

Administrivia

- Assignment #2:
 - —Due: **Thursday, Nov. 16, 2017**
- Midterm:
 - —New Date: **Sunday, Nov. 19, 2017**
 - —New Time: **12:30pm 1:45pm**
 - —Location: TBA!
 - —Coverage: lecture #1 → lecture #6

Website: http://hshehata.github.io/courses/zu/cse321a/

Office hours: Sunday 1:00pm – 2:00pm

Chapter 12. Instruction Sets: Characteristics and Functions (*cont.*)

Outline

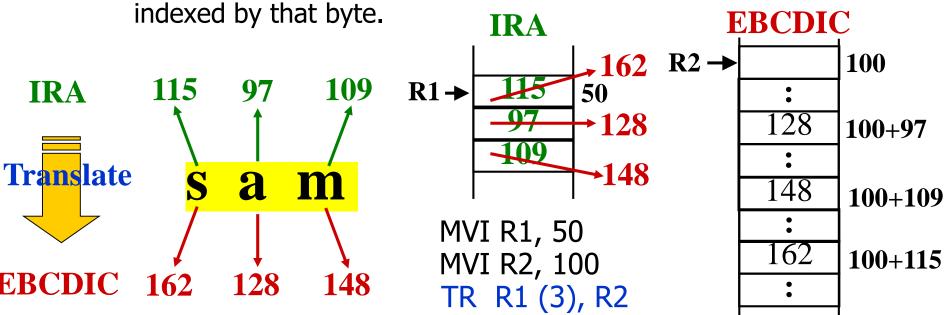
- Machine Instruction Characteristics
 - Elements of machine instructions, instruction representation, instruction types, number of addresses, instruction set design issues.
- Types of Operands
 - —Addresses, numbers, characters, logical data
 - —Example: x86 data types
- Types of Operations
 - —Data transfer, arithmetic, logical, conversion, input/output, system control, transfer of control.

4. Conversion

- Instructions that change the format of data.
 - e. g., convert from binary to BCD.
- Example: Translate (TR) instruction in IBM System 390.

— R1: starting address of source; L: number of bytes to be translated; R2: starting address of a table of 8-bit codes.

Each byte of the source bytes is replaced by a table entry indexed by that byte.



5. Input/Output

- May be done using data movement instructions
 —memory-mapped i/o
- May be using specific i/o instructions
 - —isolated i/o
- May be done by a separate controller
 - —DMA

5. Input/Output – Common Operations

Operation Name	Description	
Input (read)	Transfer data from specified I/O port or device to destination (e.g., main memory or processor register)	
Output (write)	Transfer data from specified source to I/O port or device	
Test I/O	Transfer status information from I/O system to specified destination	

6. System Control

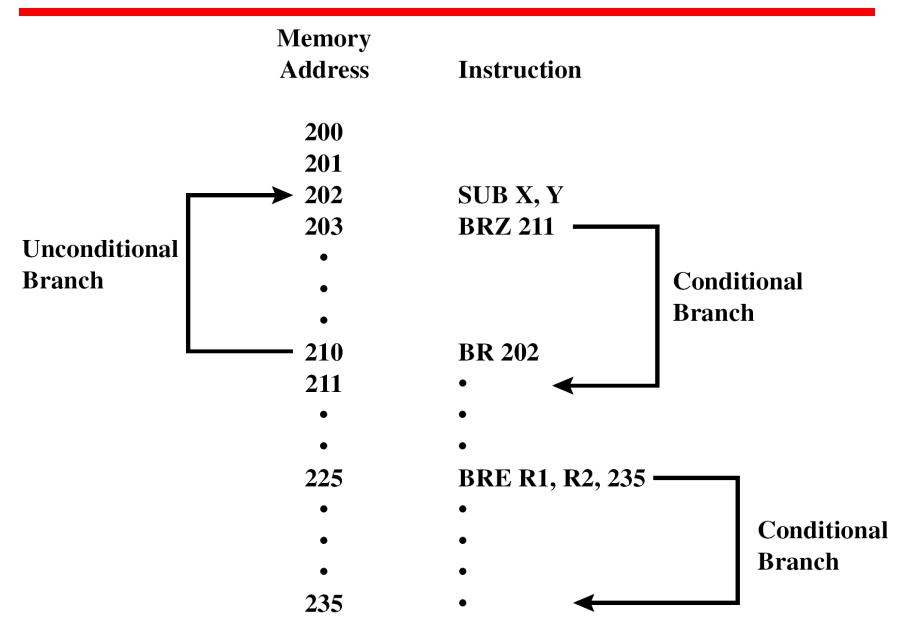
- Privileged instructions.
- Executed only when the processor is in a certain privileged state
 - —e.g., ring 0, supervisor mode, kernel mode, ... etc.
- Reserved for operating systems use.
- Example Instructions:
 - —Modify storage protection keys.
 - —Access process control blocks (ID, address space, priority, ... etc).

—,,,

7. Transfer of Control

- Most common transfer-of-control operations:
 - —branch, skip, and procedure call/return.
- Branch (jump):
 - —Types: unconditional or conditional.
 - —Unconditional: always take the branch (update PC to equal address specified in operand).
 - Ex.: BR X → branch to location X
 - —Conditional: take the branch if a condition is met, otherwise continue.
 - Condition: test a **flag** set as a result of a recent operation, or perform a **comparison** (need three-address instruction)
 - + Ex.: BRP X → Branch to location X if result is positive.
 - + Ex.: BRE R1, R2, X \rightarrow Branch to location X if [R1] equals [R2].
 - —Branches can be either forward or backward.

7. Transfer of Control – Branch Instructions



7. Transfer of Control

- Skip:
 - —Types: unconditional or conditional.
 - —Unconditional: always skip following instn. (update PC to equal address of following instn. plus Δ).
 - —Conditional: skip following instruction if condition is met, otherwise continue.
 - Typical Example: increment-and-skip-if-zero (ISZ)

```
+ 301 ...

+ ... ...

+ 309 ISZ R1

+ 310 BR 301

+ 311 ...
```

7. Transfer of Control

- Procedure call/return:
 - —Call: save address of following instruction (*i.e.*, return address) and branch to target procedure.
 - Return address can be saved: in special register, at start of called procedure, or on top of stack.
 - Ex.: Call $X \rightarrow$ call the procedure at location X

Register	Procedure Start	Stack Top
$RN \leftarrow [PC]$	$X \leftarrow [PC]$	Push [PC] to stack
$PC \leftarrow X$	PC $\leftarrow X+1$	PC ← X

- —Return: Return to calling procedure.
 - Load PC with the return address
 - Ex.: Ret → return to where you came from!

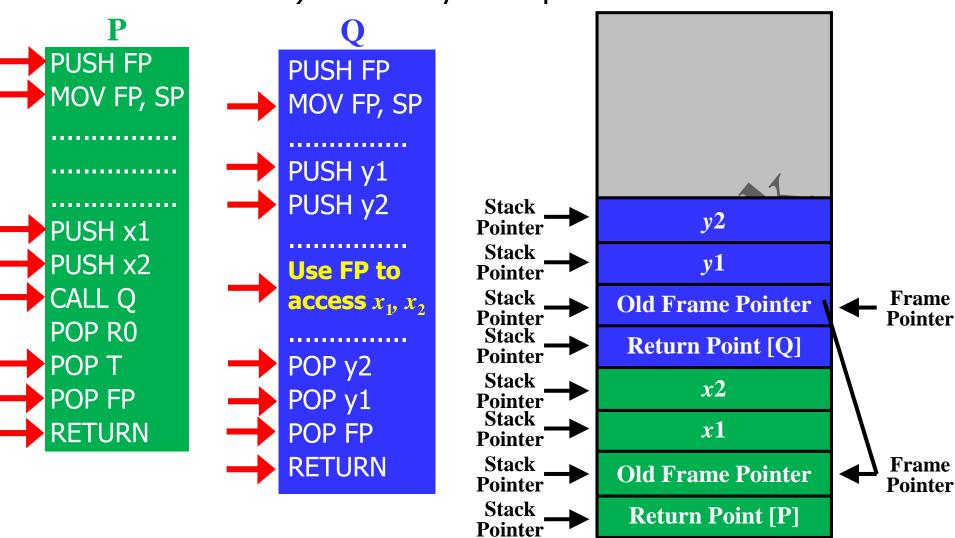
Register	Procedure Start	Stack Top
PC ← [RN]	$PC \leftarrow [X]$	Pop [PC] from stack

—c.f. interrupt call/return

Addresses Main Memory 4000 7. Transfer of Main **Control – Nested** 4100 **CALL Proc1 Program** 4101 **Procedure Calls** 4500 **CALL Proc2** 4600 4601 **Procedure** Proc1 4650 **CALL Proc2** 4651 **RETURN** 4800 **Procedure** Proc2 **RETURN** 4601 4651 4101 4101 4101 4101 4101 (g) After (a) Initial stack (b) After (c) Initial (d) After (e) After (f) After **CALL Proc1 CALL Proc2 RETURN CALL Proc2 RETURN RETURN** contents

Stack Frame Growth – Parameter Passing

 Stack frame: set of (parameters + local variables + return address) necessary for a procedure call.



7. Transfer of Control – Common Op's

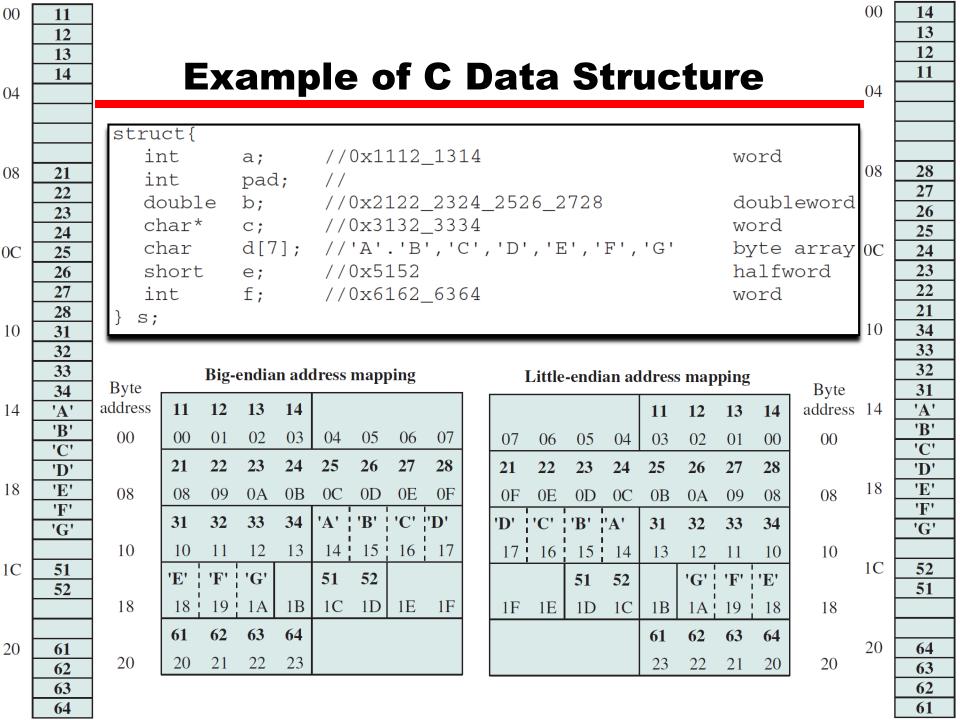
Operation Name	Description	
Jump (branch)	Unconditional transfer; load PC with specified address	
Jump Conditional	Test specified condition; either load PC with specified address or do nothing, based on condition	
Jump to Subroutine	Place current program control information in known location; jump to specified address	
Return	Replace contents of PC and other register from known location	
Execute	Fetch operand from specified location and execute as instruction; do not modify PC	
Skip	Increment PC to skip next instruction	
Skip Conditional	Test specified condition; either skip or do nothing based on condition	
Halt	Stop program execution	
Wait (hold)	Stop program execution; test specified condition repeatedly; resume execution when condition is satisfied	
No operation	No operation is performed, but program execution is continued	

Byte Order

 In what order do we read numbers that occupy more than one byte? e.g. (numbers in hex) <u>12345678</u> can be stored in 4x8-bit locations as follows:

Address	Value (1)	Value (2)
184	12	78
185	oig endian	ettle endian
186	Big56	Tittle 4
187	78	12

- The problem is called Endian.
- Highest significant byte in lowest address:
 - ➢ Big-endian.
- Least significant byte in lowest address:
 - > Little-endian.



Standard...What Standard?

- X86 and VAX are little-endian.
- IBM System 370/390, Sun SPARC, and most RISC are big-endian.
- Big-endian is favored for:
 - —<u>Character-string sorting</u>: multiple bytes can be compared in parallel.
 - —<u>Decimal/ASCII dumps</u>: values can be printed left to right without causing confusion.
 - —<u>Consistent order</u>: integers and character strings are stored in the same order.
- Little-endian is favored for:
 - —32-to-16-bit integer conversion: no need to increment address to access least-significant 2 bytes.
 - —<u>Higher-precision arithmetic</u>: no need to find the leastsignificant byte and move backward.

Reading Material

- Stallings, Chapter 12:
 - —Pages 425 431
 - -Pages 447 450