

# References

- R1. Rajeev Alur and Thomas A. Henzinger. Computer- Aided Verification. An Introduction to Model Building and Model Checking for Concurrent Systems. 1998. Draft: <http://wwwcad.eecs.berkeley.edu/tah/CavBook/>.
- R2. V. S. Alagar and K. Periyasamy. Specification of Software Systems. Springer, 1998.
- R3. Randal E. Bryant and Yirng-An Chen. Verification of arithmetic functions with binary moment diagrams. Technical Report CMU-CS-94-160, School of Computer Science, Carnegie Mellon University, June 1994.
- R4. J. R. Burch, E.M. Clarke, K.L. McMillan, D.L. Dill, and J. Hwang. Symbolic model checking: 1020 states and beyond. In Proceedings of the Fifth Annual Symposium on Logic in Computer Science, June 1990.

# References

- R6. J. R. Burch, E. M. Clarke, K. L. McMillan, D. L. Dill, and L. J. Hwang. Symbolic model checking: 1020 states and beyond. *Information and Computation*, 98:142170, June 1992.
- R7. Randal E. Bryant. Graph-based algorithms for Boolean function manipulation. *IEEE Transactions on Computers*, C-35(8):677691, August 1986.
- R8. R. E. Bryant. Symbolic boolean manipulation with ordered binary decision diagrams. *ACM Computing Surveys*, 24(3):293318, September 1992. Preprint version published as CMU Technical Report CMU-CS-92-160.
- R9. E. M. Clarke and E.A. Emerson. Synthesis of synchronization skeletons for branching time temporal logic. In *Logic of Programs: Workshop*, volume 131 of *Lecture Notes in Computer Science*. Springer- Verlag, May 1981.

# References

- R10. E.M. Clarke, E.A. Emerson, and A.P. Sistla. Automatic verification of finite-state concurrent systems using temporal logic. *ACM Transactions on Programming Languages and Systems*, 8(2):244-263, April 1986.
- R11. Edmund Clarke Jr., Orna Grumberg, and Doron A. Peled. *Model Checking*. MIT Press, 1999.
- R12. F. Corella, Z. Zhou, X. Song, M. Langevin, and E. Cerny. Multiway decision graphs for automated hardware verification. Technical Report RC19676, IBM, 1994. Also *Formal Methods in Systems Design*, 10(1) , pages 7-46 , 1997.
- R13. Matthew B. Dwyer, George S. Avrunin, and James C. Corbett. Property specification patterns for finite-state verification. In Mark Ardis, editor, *Proceedings of FMSP98. The Second Workshop on Formal Methods in Software Practice*, pages 7-15. ACM Press, March 1998.

# References

- R14. Carlo Ghezzi, Mehdi Jazayeri, and Dino Mandrioli. Fundamentals of Software Engineering. Prentice Hall, Englewood Cliffs, NJ, 1991.
- R15. Ralph P. Grimaldi. Discrete and Combinatorial Mathematics. Addison-Wesley, Reading, Massachusetts, 1985.
- R16. Gerard J. Holzmann. The Spin Model Checker: Primer and Reference Manual. Addison-Wesley.
- R17. Michael R. A. Huth and Mark D. Ryan. Logic in Computer Science. Cambridge University Press, Cambridge, 2000. DC Library: QA76.9.L63 H88 2000.

# References

- R18. Michael R. A. Huth and Mark D. Ryan. Logic in Computer Science. Cambridge University Press, Cambridge, 2004. Second Edition.
- R19. Thomas Kropf. Introduction to Formal Hardware Verification. Springer, 1999.
- R20. R. P. Kurshan. Computer-Aided Verification of Coordinating Processes: The Automata-Theoretic Approach. Princeton University Press, 1994.
- R21. Udi Manber. Introduction to Algorithms: A Creative Approach. Addison-Wesley Publishing Company, Reading , Massachusetts, 1989.
- R22. John Matthews, Byron Cook, and John Launchbury. Microprocessor specification in Hawk. In International Conference on Computer Languages, 1998.

# References

- R23. Kenneth L. McMillan. Symbolic Model Checking. PhD thesis, Carnegie Mellon University, May 1992.
- R24. Robin Milner. Communication and Concurrency. Prentice Hall, New York, 1989.
- R25. Doron A. Peled. Software Reliability Methods. Springer, New York, 2001.
- R26. A. Pnueli. A temporal logic of programs. In 18th IEEE Symposium on Foundations of Computer Science, pages 4657, 1977.
- R27. J. P. Quielle and J. Sifakis. Specification and verification of concurrent systems in cesar. In Proceedings of the Fifth International Symposium on Programming, volume 137 of Lecture Notes In Computer Science, pages 337350, 1981.
- R28. C.-J. H. Seger and R. E. Bryant. Formal verification by symbolic evaluation of partially-ordered trajectories. Formal Methods in System Design, 6:147 189, March 1995. 225-