Tutorial #3

External problem

Suppose an 8-bit data word stored in memory is 11011100.

- i. Determine no. of check bits
- ii. Using the Hamming algorithm, determine what check bits would be stored in memory with the data word. Show how you got your answer.
- iii. Suppose error occur in data bit D3 where data word read from memory is 11011000. Show how hamming algorithm can be used to detect and correct error.
- i. Need K check bits such that $2K 1 \ge 1024 + K$. Using try and error the minimum value of K that satisfies this condition is 4.

ii. Code word will organized as shown

Position	12	11	10	9	8	7	6	5	4	3	2	1
Bits	D8	D7	D6	D5	C8	D4	D3	D2	C4	D1	C2	C1
Data	1	1	0	1		1	1	0		0		

C1=0
$$\bigoplus$$
1 \bigoplus 1 \bigoplus 1 \bigoplus 0=1
C2=1 \bigoplus 0 \bigoplus 1 \bigoplus 1 \bigoplus 0=1
C4=1 \bigoplus 1 \bigoplus 1 \bigoplus 0=1
C8=1 \bigoplus 1 \bigoplus 0 \bigoplus 1=1

So, hamming word will be: 110111101011

iii. When data word read from memory is 11011000, new check bits will be calculated to be

Position	12	11	10	9	8	7	6	5	4	3	2	1
Bits	D8	D7	D6	D5	C8	D4	D3	D2	C4	D1	C2	C1
Hamming word	1	1	0	1	1	1	0	0	0	0	0	1

Doing an XOR of 1001 and 1111 yields 0110 \Rightarrow error in bit position 6 (six) of the Hamming word. Thus, the correct data word was 11011 $\frac{1}{2}$ 00

Q2-2 Final W2014

An SDRAM module (DIMM) is made out of eight 64M x 8 SDRAM chips that receive the same address and command. Each chip contributes 8 bits to the overall data word handled by the module. Suppose the module is connected to a bus clocked at 100MHz.

- (a) Represent the size of the module in the form $m \times n$.
- (b) What is the maximum data transfer rate of the module (measured in MB/s)?
- a) Size of the module is 64M x 64 bits
- b) Max rate is achieved by producing 64 bits every cycle at 100 MHz speed so, Max rate = 64 b/cycle * 100 M cycle/s = 6400 Mb/s = 800 MB/s

[8 points] Consider a single-platter disk with the following parameters: rotation speed: 3600 rpm; number of tracks on one side of platter: 15,000; number of sectors per track: 300; seek time: 2 ms for every hundred tracks traversed. Let the disk receive a request to read 5 consecutive sectors starting at a random sector on a random track and assume the disk head starts at track 0.

- (a) What is the average seek time?
- (b) What is the average rotational latency?
- (c) What is the transfer time for one sector?
- (d) What is the total average time to satisfy the request?

Solution

2. (a) 14,999/2 tracks are traversed on average

(b) Av. seek time =
$$\frac{14,999}{2} * \frac{2 \text{ nS}}{100} = 149.99 \text{ mS}$$

(b) Av. rotan delay = $\frac{1}{2 \text{ v}} = \frac{60 * 10^3}{2 * 3600} = 8.339 \text{ mS}$

(c) Transfer time for a sector = $\frac{1}{300} = \frac{1}{300} =$

(d) Time to satisfy request =

149.99 + 8.33 + 5 + 0.056

= 158.603