

Memory

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graph TD; Memory[Memory] <--> CU[Control Unit]; Memory <--> ALU[Arithmetic Logic Unit]; subgraph CPU; CU <--> ALU; end; Input[Input] --> ALU; ALU --> Output[Output];
```

The diagram illustrates the basic components of a computer system and their interactions. At the top is a large box labeled **Memory**. Below it is a large box labeled **CPU** on its right side, which contains two smaller boxes: the **Control Unit** on the left and the **Arithmetic Logic Unit** on the right. These two units are connected by a horizontal double-headed arrow. Above the CPU box, two vertical double-headed arrows connect the **Memory** box to the **Control Unit** and the **Arithmetic Logic Unit** respectively. At the bottom, there are two boxes: **Input** on the left and **Output** on the right. An arrow points from the **Input** box to the **Arithmetic Logic Unit**, and another arrow points from the **Arithmetic Logic Unit** to the **Output** box.

**Control
Unit**

**Arithmetic
Logic
Unit**

CPU

Input

Output