

Memory

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graph TD; Memory[Memory] <--> CU[Control Unit]; Memory <--> ALU[Arithmetic Logic Unit]; subgraph CPU; CU <--> ALU; end; Input[Input] --> CPU; CPU --> Output[Output];
```

The diagram illustrates the basic components of a computer system and their interactions. At the top is a large box labeled **Memory**. Below it is a large box labeled **CPU** on its right side. Inside the CPU box are two smaller boxes: **Control Unit** on the left and **Arithmetic Logic Unit** on the right. These two units are connected by a double-headed horizontal arrow. Above the CPU box, there are two double-headed vertical arrows, one connecting **Memory** to the **Control Unit** and another connecting **Memory** to the **Arithmetic Logic Unit**. Below the CPU box, there are two boxes: **Input** on the left and **Output** on the right. An arrow points from the **Input** box to the CPU box, and another arrow points from the CPU box to the **Output** box.

**Control
Unit**

**Arithmetic
Logic
Unit**

CPU

Input

Output