

# Functionally Reduced And-Inverter Graph (FRAIG) **Implementation by C++**

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# Summary

Circuit Structure Design

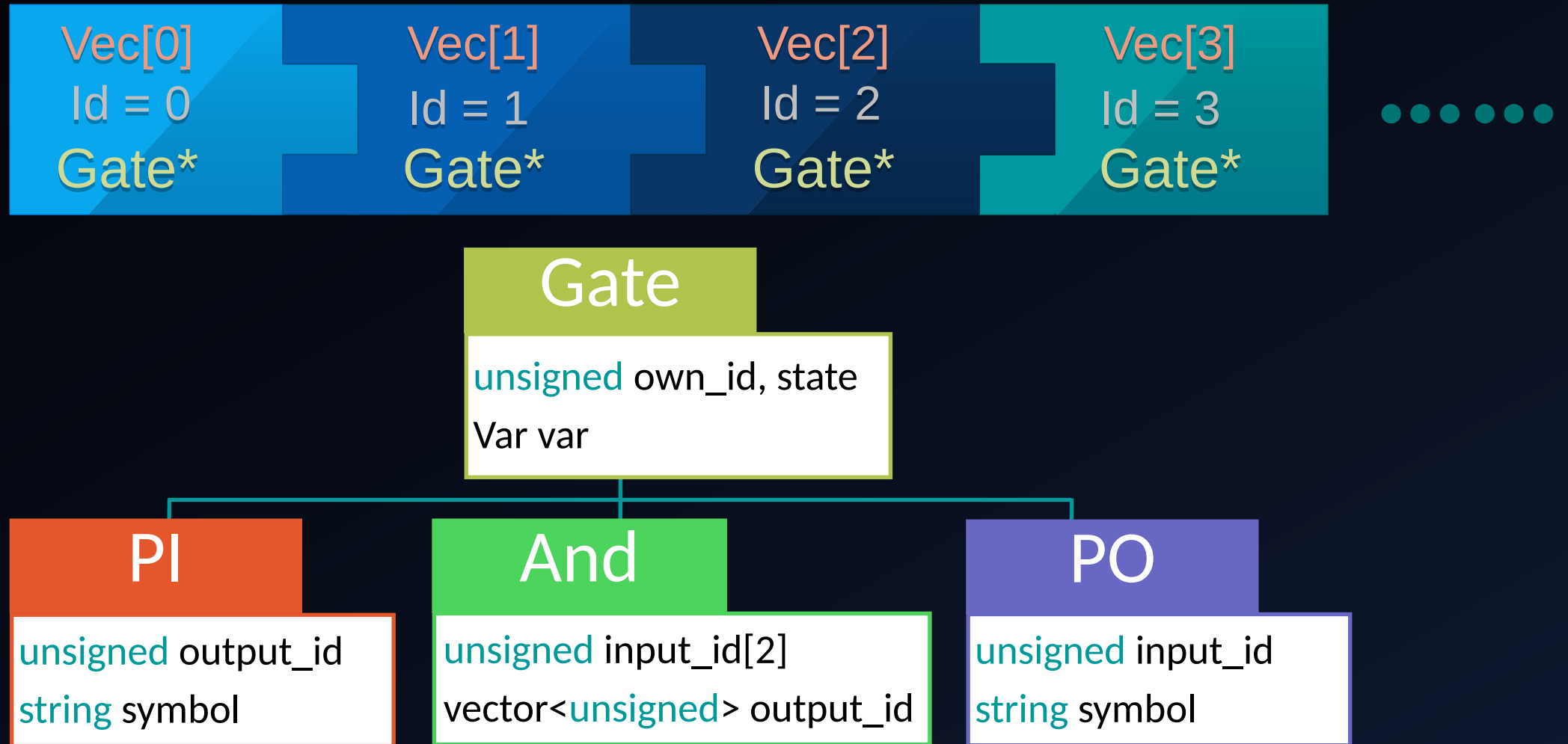
Structural Hash

Simulation

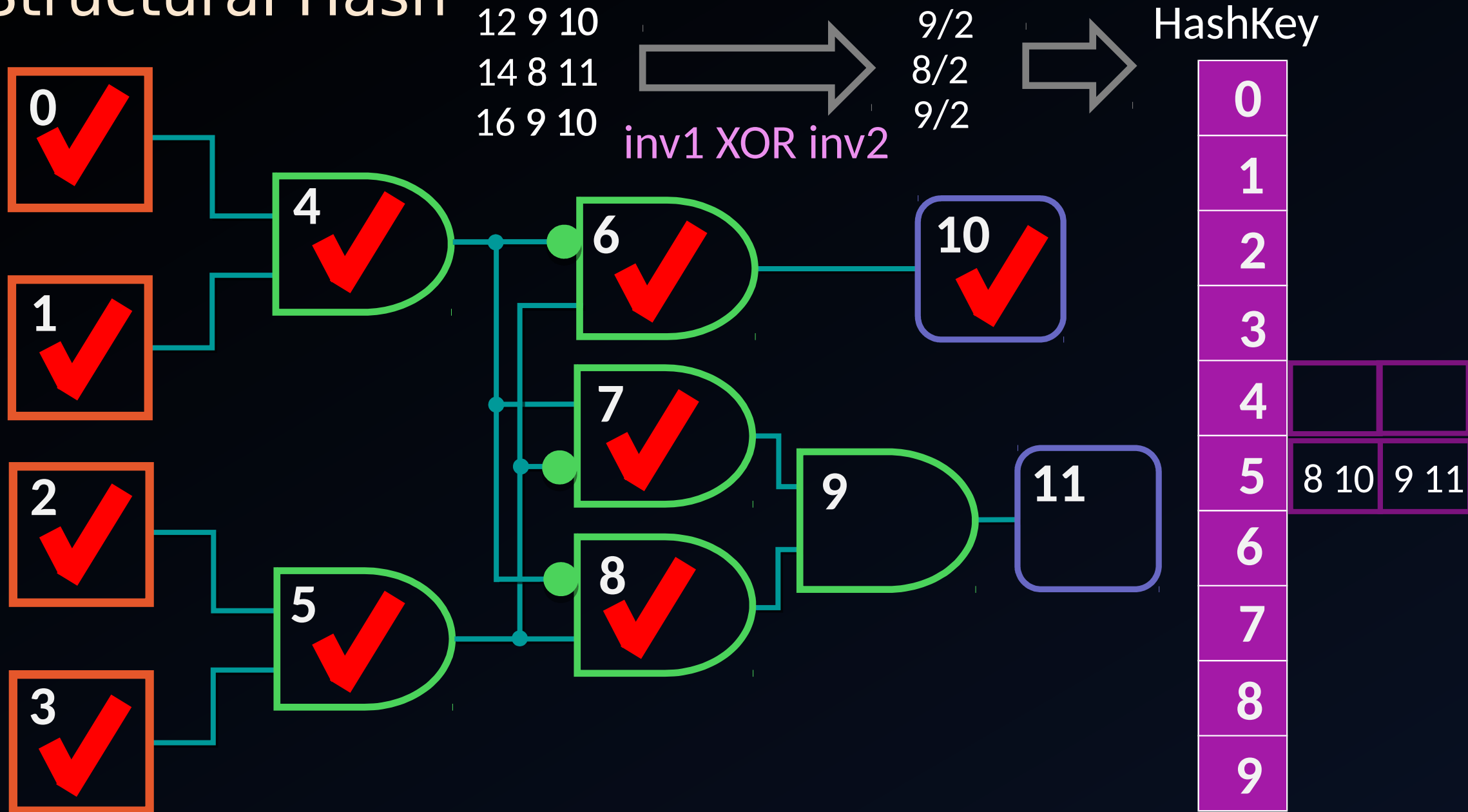
Fraig

# Circuit Structure Design

This is a vector<Gate\*> :



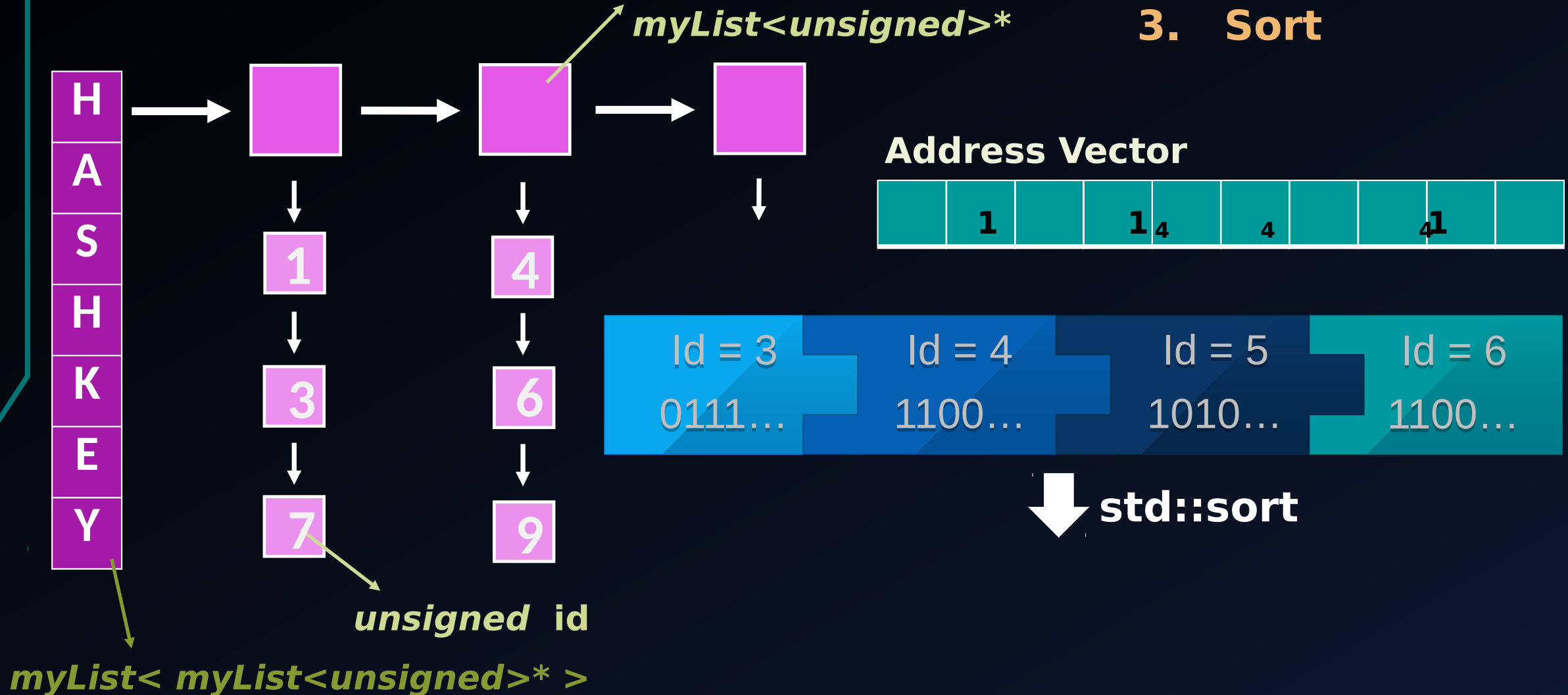
# Structural Hash



# Simulation

How I arrange FEC groups ?

1. **Singly-linked list**
2. **Address**
3. **Sort**



# Fraig

- If list size  $> 4$ , from list to vector
- If patterns collected  $> 5$ , re-simulate and sort
- Throw the vector back into hash table





Thanks for  
listening !