, # <b>`\$</b>		Revisions			216381	
LAL	Rev.	Description	Chk	Date	Approv	ed
:	A	ENGRG RELEASE	bol	3-8-76	/	
	В	REVISED; ITEM 5 QTY WAS 1	U BN	7-28	m	*** **
	С	ADDED BOARD SILKSCREEN DWG.	BN	8-96	Bo	/
	D	M/L ITEM 38 P/N WAS 514-AG10D, ITEM 39 P/N WAS 516-A ITEM 40 P/N WAS 524-AG10D.	G10D	4-13	m	,



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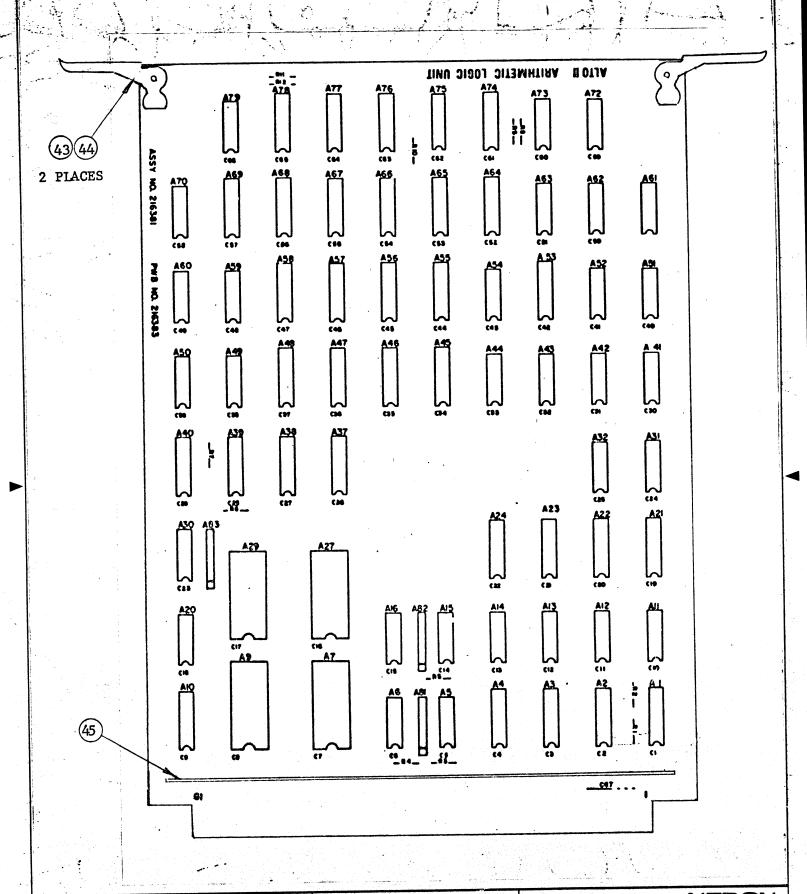
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1. ASSEMBLE PER ALTO II MODULE ASSEMBLY SPECIFICATION, DWG NO. 216207.

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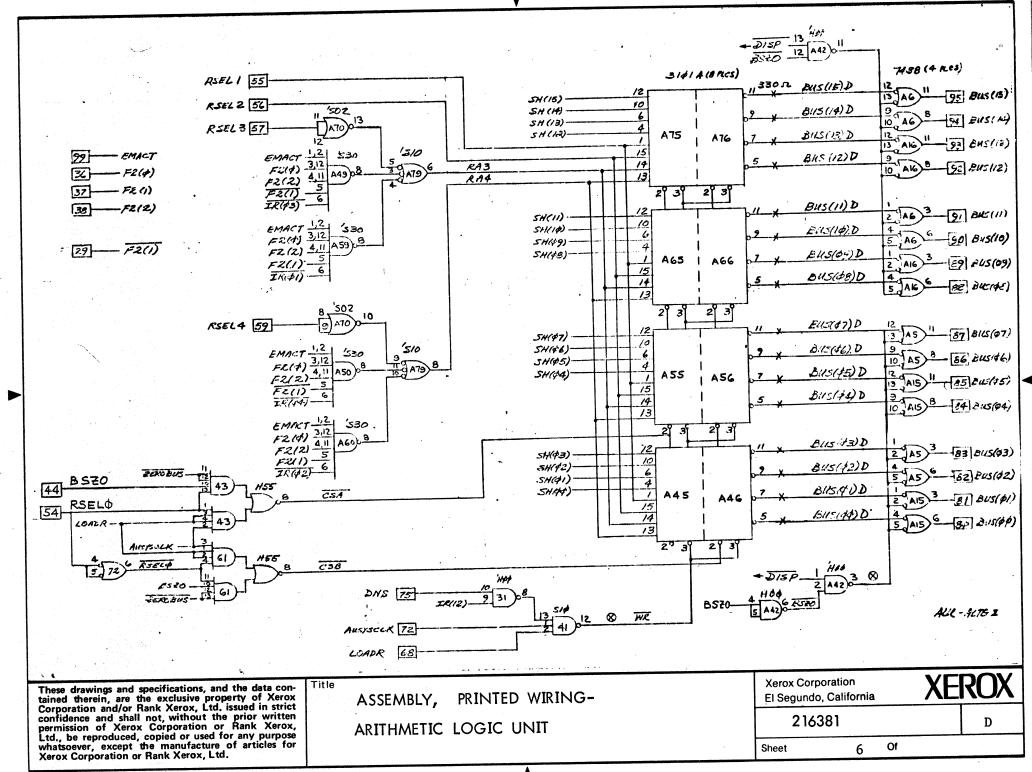
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4		ARITHMETIC LOGIC UNIT	Model No. EWO7YF92	Model No. Date She		
216381	Item No.	Drawing Title	Drawing No.	No. Req.	3/5/76 4 Of Remarks	
	1	Board, P. W ALU	216383	1		
	2	Procedure, Test	216382	REF		
Z	3	Spec, Module Assembly	216207	REF		
	4	Microcircuit, 74H00		4	A31,42,52,72	
-	5	74H55		2	A43, 61	
	6	74Н01		2	A44,54	
	7	74S02		2	A51,70	
	8	74H04		2	A20,30	
	9	74S08		4	A11,12,13,14	
	10	74S10		2	A41,79	
	11	74\$30		4	A49,50,59,60	
	12	7438		4	A5,6,15,16	
	13	74H50		1	A63	
	14	74H51	•	1	A32	
	15	74H74		$\frac{1}{1}$	A73	
	16	7486		1	A62	
	17	748133		1	A69	
	18	748174		3	A37,38,39	
	19	748175		4	A1,2,3,4	
	20	74S181		4	A7,9,17,19	
	21	74S182		1	A40	
	22	74298		4	A21,22,23,24	
	23					
	24				A45,46,55,56,65,66,	
	25	I3101A Intel		8	A45,46,55,56,65,66, 75,76 A47,48,53,57,58,67, 68,77,78	
	26	F9309 Fairchild		9		
	27	82S23 Signetics	3	1	A10	
	28	Microcircuit, 82S34 Signetics		2	A64,74	
	<b>2</b> 9					
	30					
	31	Capacitor, .05 uF, 10V		66	Centralab #UK10-503	
	32				C1 thru C66	
	34	Capacitor, 22uF, 15V, Tantalum	187720-005	1	C67	

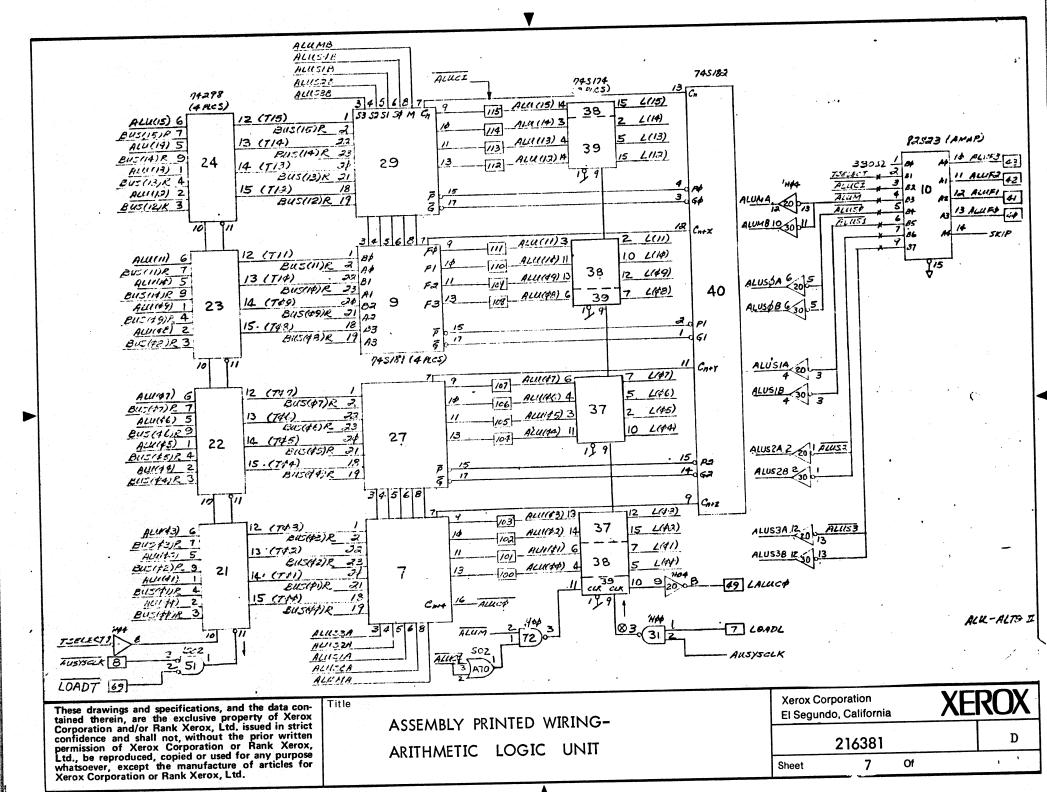
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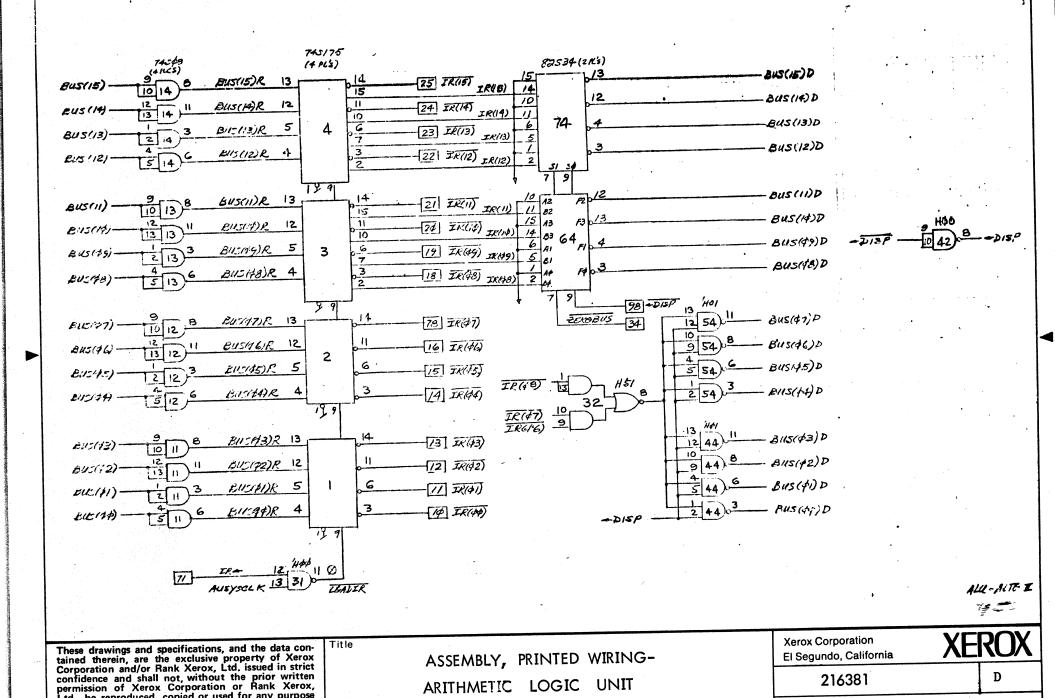
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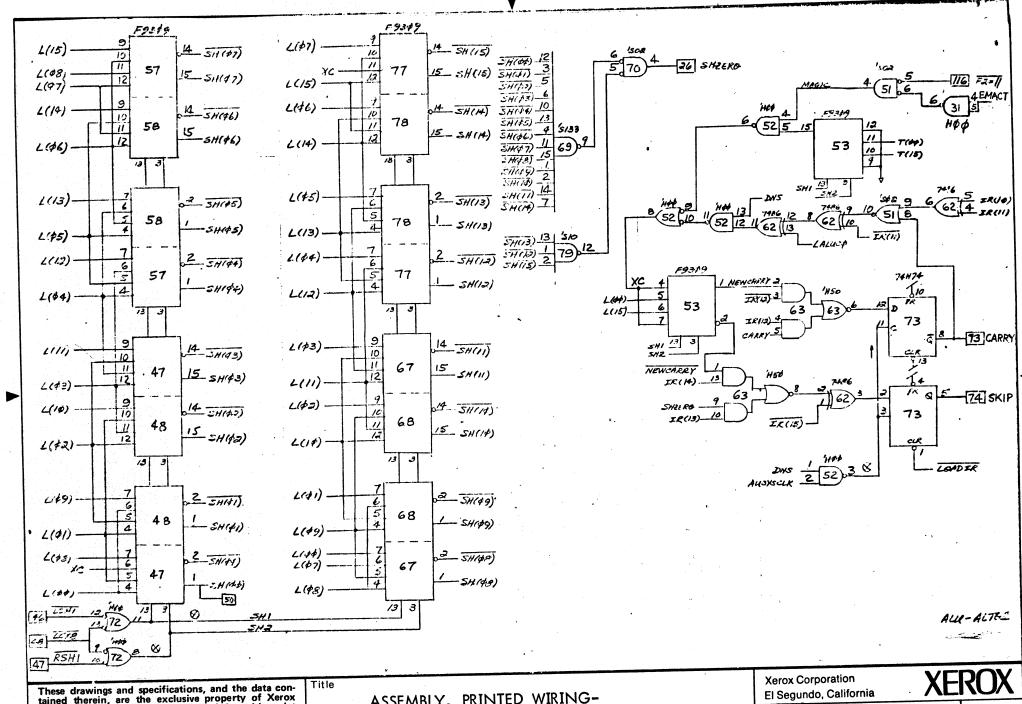
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112(3/73)