

參考文獻

- 1 Sam C.S. Pan on IEEE EDS Taipei Chapter
- 2 J.R. Hwang VLSI symposium 2003
- 3 S.Lai, "Flash memory: where we were and where we are going", IEDM Tech Dig, p. 971, 1998
- 4 "Flash Memory", edited by Cappelletti P., Zanoni E Kluwer Academic Publishers, 1999
- 5 R. Bez et al., "Introduction to Flash memory", Proceedings of the IEEE, vol.91 no. 4, 003
- 6 F.Masuoka et al., New ultra high density EPROM and Flash with NAND structure cell, IEDM Tech. Dig., p.552, 1987
- 7 T.Cho et al., "A 3.3V 1Gb Multilevel NAND Flash memory with non-uniform threshold voltage distribution", ISSCC Tech. Gig., 2003
- 8 Y.Shirota, "Future trends in NAND Flash Memory", Ext. Abstract SSDM conference, Tokyo, Japan, 2004
- 9 Y.Sasago et al., "90nm ML AG-AND type Flash with cell of true 2F2/bit and progr. Throughput of 10MB/s" IEDM Tech. Dig., 2003
- 10 B. Eitan et al., NROM: a novel localized trapping 2-bit nonvolatile memory cell, IEEE El. Dev. Lett., v.21, p.543, 2000