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A Ruby on Rails full-stack web developer at i.am+ www.iamplus.com. Graduated from UCLA with a BS Electrical Engineering degree with 7 years of integrated circuit layout design experience. Co-founded www.apmjewelry.com. A jewelry wholesale startup which went from 5 digits revenue to \$1 million in annual sales. Excited to bring my enthusiasm and expertise to grow an energetic development team.

Professional Experience

i.am+ - Los Angeles (February 2015 - present)

Full stack web development with a team of 10 software engineers. Develop web apps based on Ruby on Rails for Dial, i.am+ latest smart watch. Projects include User Identity, Developer Portal, App-Review and many other micro services to support the team and product needs. I am a strong believer in test driven development.

Projects:

- <http://dashboard.i.am/>
- <https://auth.i.am/login/>
- <https://developer.i.am/>
- <https://app-review.i.am/>
- <https://jobs.i.am/>

Technology: Ruby on Rails, Rack, Rspec, Capybara, HTML, CSS, Javascript, jQuery, Postgres, MongoDB, Github, Heroku.

General Assembly - Santa Monica, CA (September 2014 - December 2014)

Web Development Immersive program: Developed skills to become a full-stack web developer.

Built several projects which are listed on my personal website.

Technology: Ruby on Rails, HTML, CSS, Bootstrap, Javascript, jQuery, Ruby, Angular, Node, Git...

APM Jewelry - Los Angeles, CA (May 2005 - December 2013)

Co-owner and Director: Co-founded an E-commerce online jewelry store. Grew company from 2 employees to 6 and annual sales grew from 5 figure to over \$1 million. Built and maintained the company website. Duty included inventory control, customer service, sales, product planning and company roadmap, trouble shoot computers and network problems, photo editing, tax and other paperwork filing. Proficient with HTML, CSS, Photoshop, product photography, Paypal and payment gateway integration.

Vitesse Semiconductor - Camarillo, CA (May 1998 – March 2005)

Circuit and Layout Designer

PLL and high speed circuit design used in SATA and Fibre Channel devices. Proficient at CAD tools used for digital/analog designs such as Cadence layout, schematic capture, HSpice, Dracular, Calibre.

Hardware Projects:

- 10-Bit Serializer/Deserializer. First CMOS 1 GB/s SerDes for the market.
- Hex 2 Gbps Fibre Channel PBC with Dual FibreTimers. The best selling Fiber Channel JBOD chip. Industry's first IC with 6 Port Bypass Circuits (PBC).
- 6-Port Serial Attached SCSI Edge Expander at 1.5 and 3.0 Gbps.
- Other chips for high speed circuits including PLL, CMU, CRU, serializer, deserializer, clock tree distribution.

Skills and Software: UNIX, Emacs, Spice, Verilog, VHDL, VERA, PERL, C/C++, HTML, MS Office.

Education: University of California, Los Angeles. B.S. Electrical Engineering, March 1998.

Language: English and Cantonese.

Citizenship: U.S. Citizen.