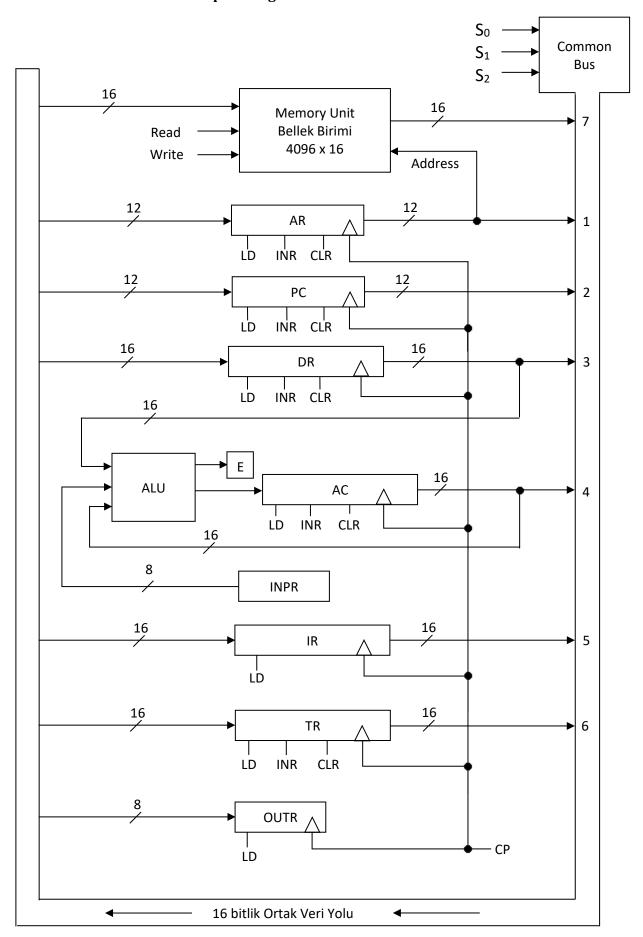
Temel Bilgisayar Yazaçlarının bir Ortak Veri Yoluna Bağlanması Basic computer registers connected to a common bus



Temel Bilgisayar için Denetim Fonksiyonları ve Mikroişlemler Control functions and microoperations for the basic computer

```
R'T_0: AR \leftarrow PC
    Fetch - Al Getir
                                         R' T_1: IR \leftarrow M[AR], PC \leftarrow PC + 1
  Decode - Kod Çöz
                                         R'T_2: I \leftarrow IR(15), D_7 ... D_0 \leftarrow Decode IR (14-12), AR \leftarrow IR(11-0)
   Indirect - Dolaylı
                                      D_7' I T_3: AR \leftarrow M[AR]
                             (T_0 + T_1 + T_2)' \cdot IEN \cdot (FGI + FGO) : R \rightarrow 1
Interrupt – Kesme
                        RT_0: AR \leftarrow 0, TR \leftarrow PC
                        RT_1: M[AR] \leftarrow TR, PC \leftarrow 0
                        RT_2: PC \leftarrow PC + 1, IEN \leftarrow 0, R \leftarrow 0, SC \leftarrow 0
Memory Reference Instructions - Bellek Adreslemeli Buyruklar
                                         D_0T_4: DR \leftarrow M[AR]
                        AND
                                         D_0T_5: AC \leftarrow AC \wedge DR, SC \leftarrow 0
                                         D_1T_4: DR \leftarrow M[AR]
                        ADD
                                         D_1T_5: AC \leftarrow AC + DR , E \leftarrow C<sub>out</sub> , SC \leftarrow 0
                        LDA
                                         D_2T_4: DR \leftarrow M[AR]
                                         D_2T_5: AC \leftarrow DR, SC \leftarrow 0
                        STA
                                         D_3T_4: M[AR] \leftarrow AC, SC \leftarrow 0
                        BUN
                                         D_4T_4: PC \leftarrow AR, SC \leftarrow 0
                                         D_5T_4: M[AR] \leftarrow PC, AR \leftarrow AR + 1
                        BSA
                                         D_5T_5: PC \leftarrow AR, SC \leftarrow 0
                                         D_6T_4: DR \leftarrow M[AR]
                          ISZ
                                         D_6T_5: DR \leftarrow DR + 1
                                         D_6T_6: M[AR] \leftarrow DR, If DR = 0 then PC \leftarrow PC + 1, SC \leftarrow 0
Register Reference Instructions - Yazaç Adreslemeli Buyruklar
D_7 I' T_3 = r,
                      IR(i) = B_i (i = 0, 1, 2, ..., 11)
                                             r: SC \leftarrow 0
                         CLA
                                          rB_{11}: AC \leftarrow 0
                         CLE
                                          rB_{10}: E \leftarrow 0
                                           rB_9: AC \leftarrow AC'
                       CMA
                                           rB_8: E \leftarrow E'
                        CME
                         CIR
                                           rB_7: AC \leftarrow shr AC , AC(15) \leftarrow E , E \leftarrow AC(0)
                                           rB_6: AC \leftarrow shl AC, AC(0) \leftarrow E, E \leftarrow AC(15)
                          CIL
                                           rB_5: AC \leftarrow AC + 1
                         INC
                        SPA
                                           rB_4: If AC(15) = 0 then PC \leftarrow PC + 1
                        SNA
                                           rB_3: If AC(15) = 1 then PC \leftarrow PC + 1
                         SZA
                                           rB_2: If AC = 0 then PC \leftarrow PC + 1
                         SZE
                                           rB_1: If E = 0 then PC \leftarrow PC + 1
                                           rB_0: S \leftarrow 0
                         HLT
Input Output Instructions – Giriş Çıkış Buyrukları
D_7 I T_3 = p,
                      IR(i) = B_i (i = 6, 7, 8, 9, 10, 11)
                                             p: SC \leftarrow 0
                         INP
                                         pB_{11}: AC (7-0) \leftarrow INPR, FGI \leftarrow 0
                        OUT
                                         pB_{10}: OUTR \leftarrow AC (7-0), FG0 \leftarrow 0
                                          pB_9: If FGI = 1 then PC \leftarrow PC + 1
                          SKI
                        SKO
                                          pB_8: If FGO = 1 then PC \leftarrow PC + 1
                         ION
                                          pB_7: IEN \leftarrow 1
                         IOF
                                          pB_6: IEN \leftarrow 0
```