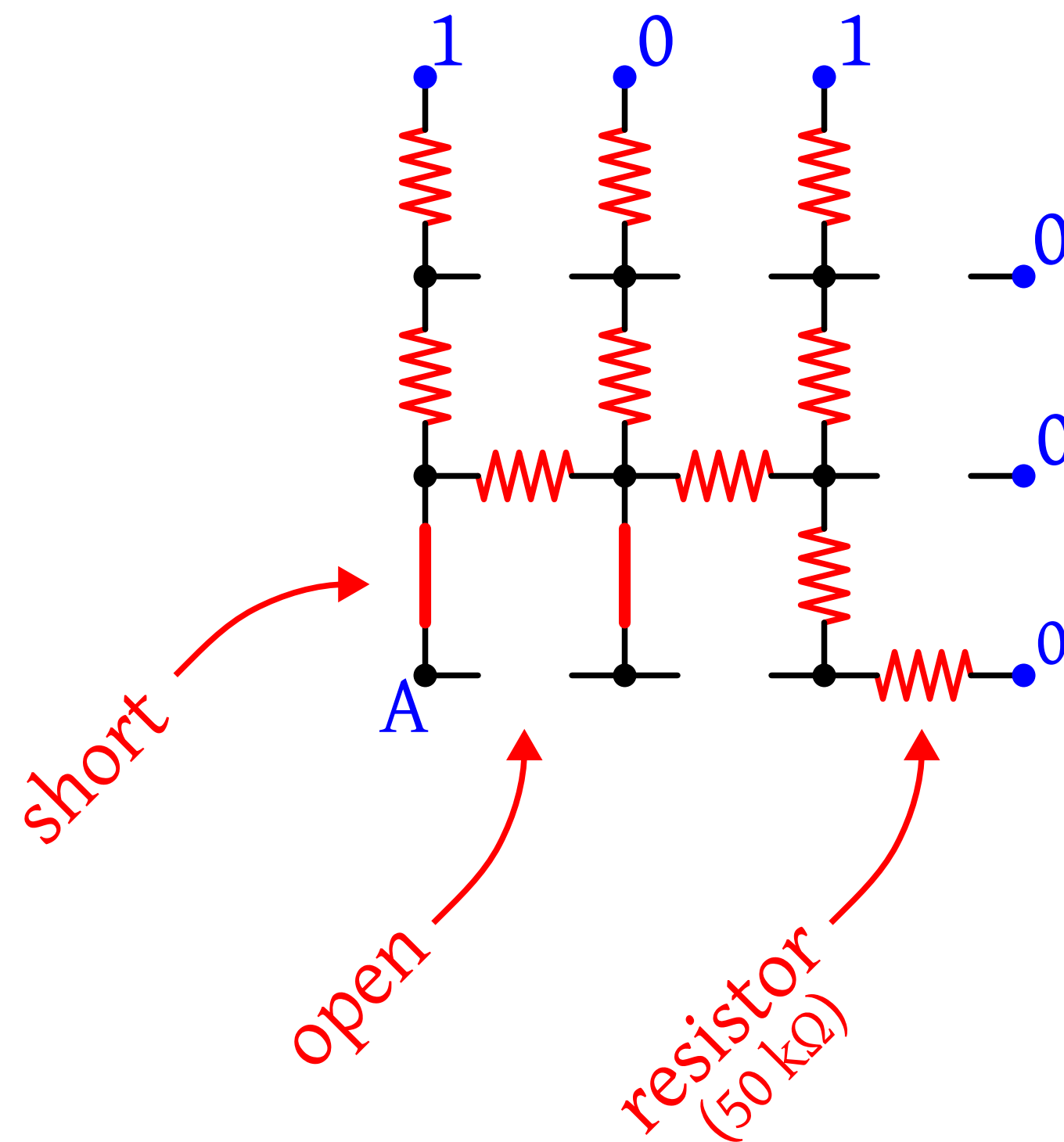


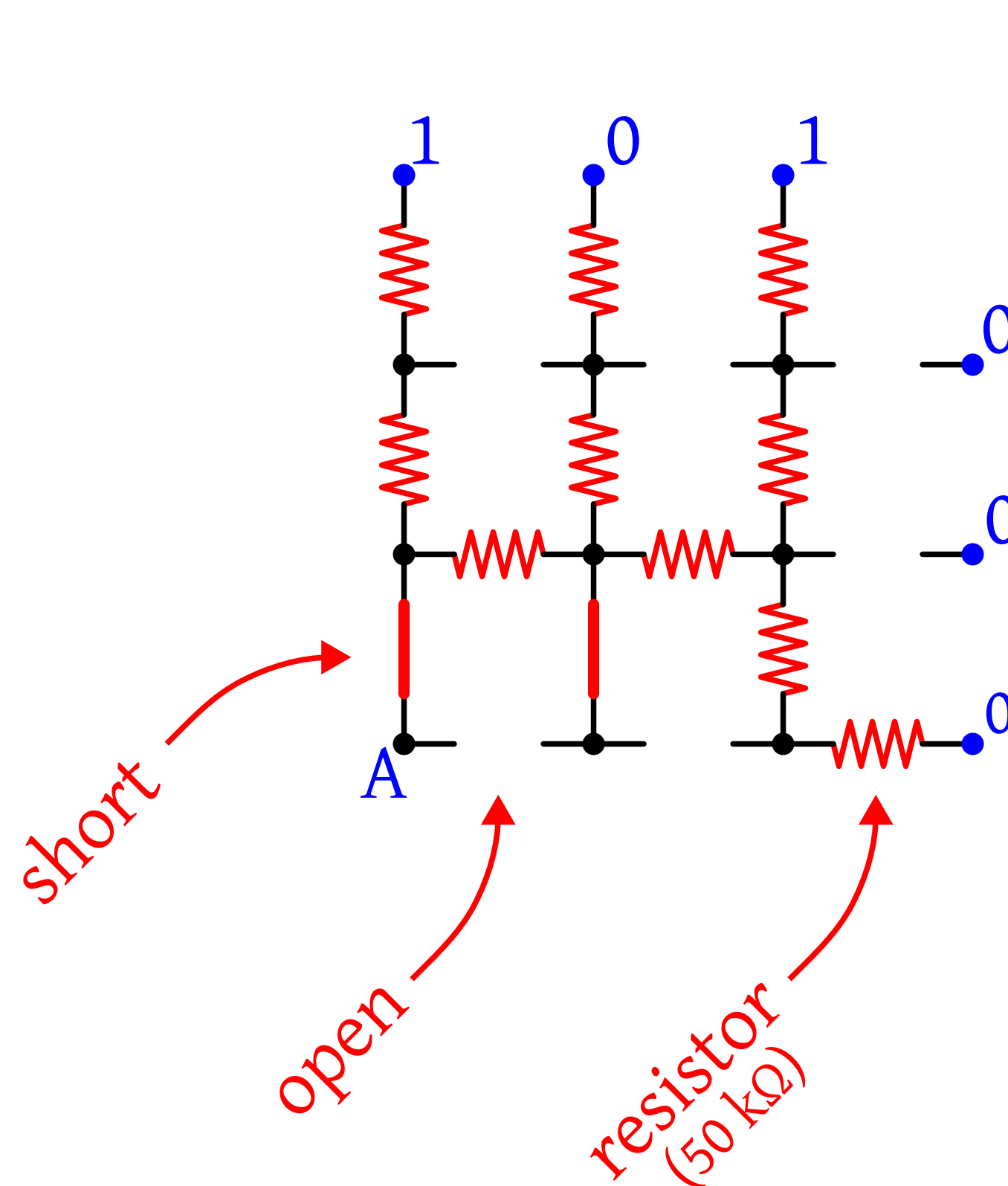
Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>



Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>



danger level
medium

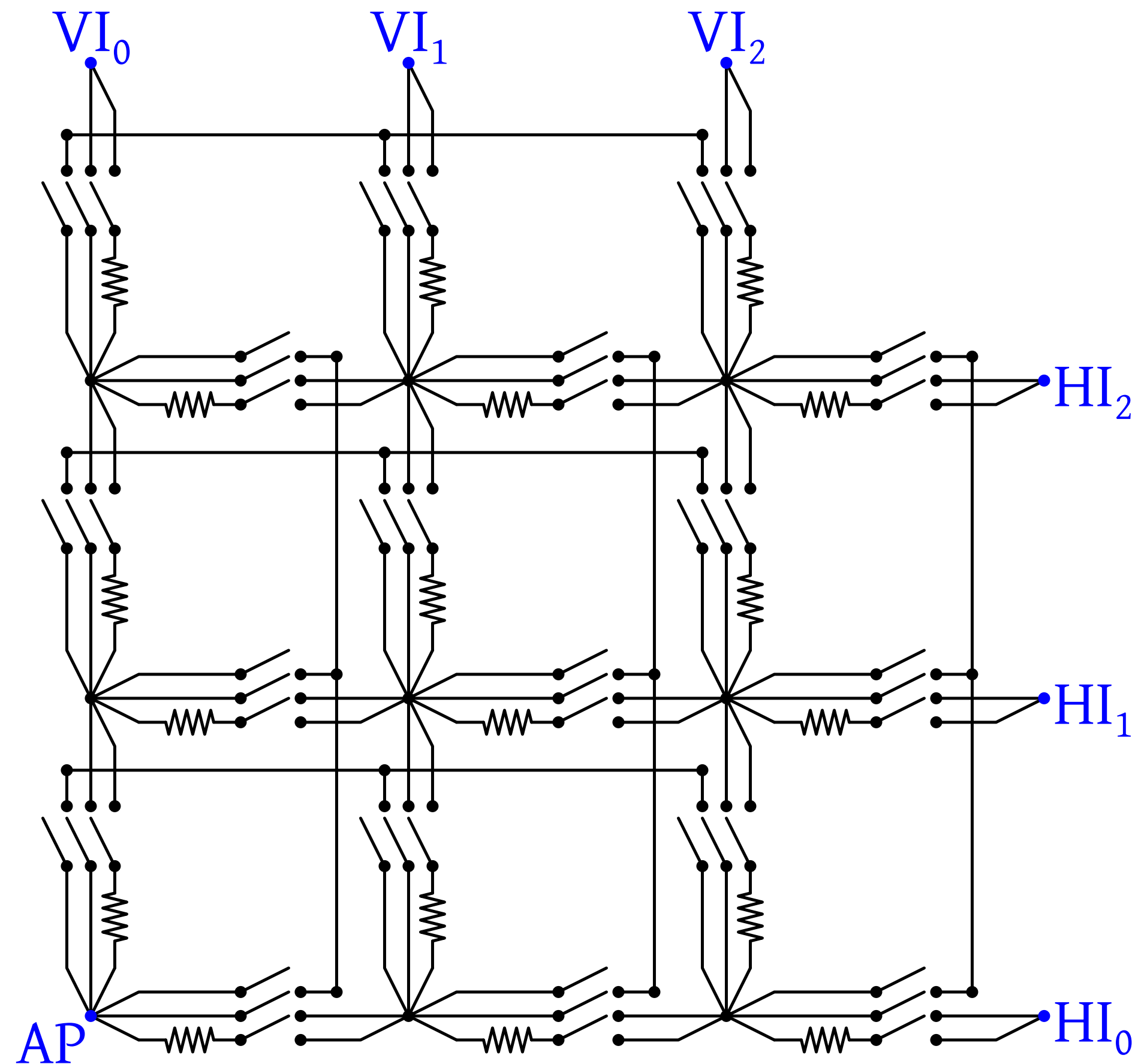
=

some
configurations
short digital
input pins
+

powers up
with an
unknown
configuration

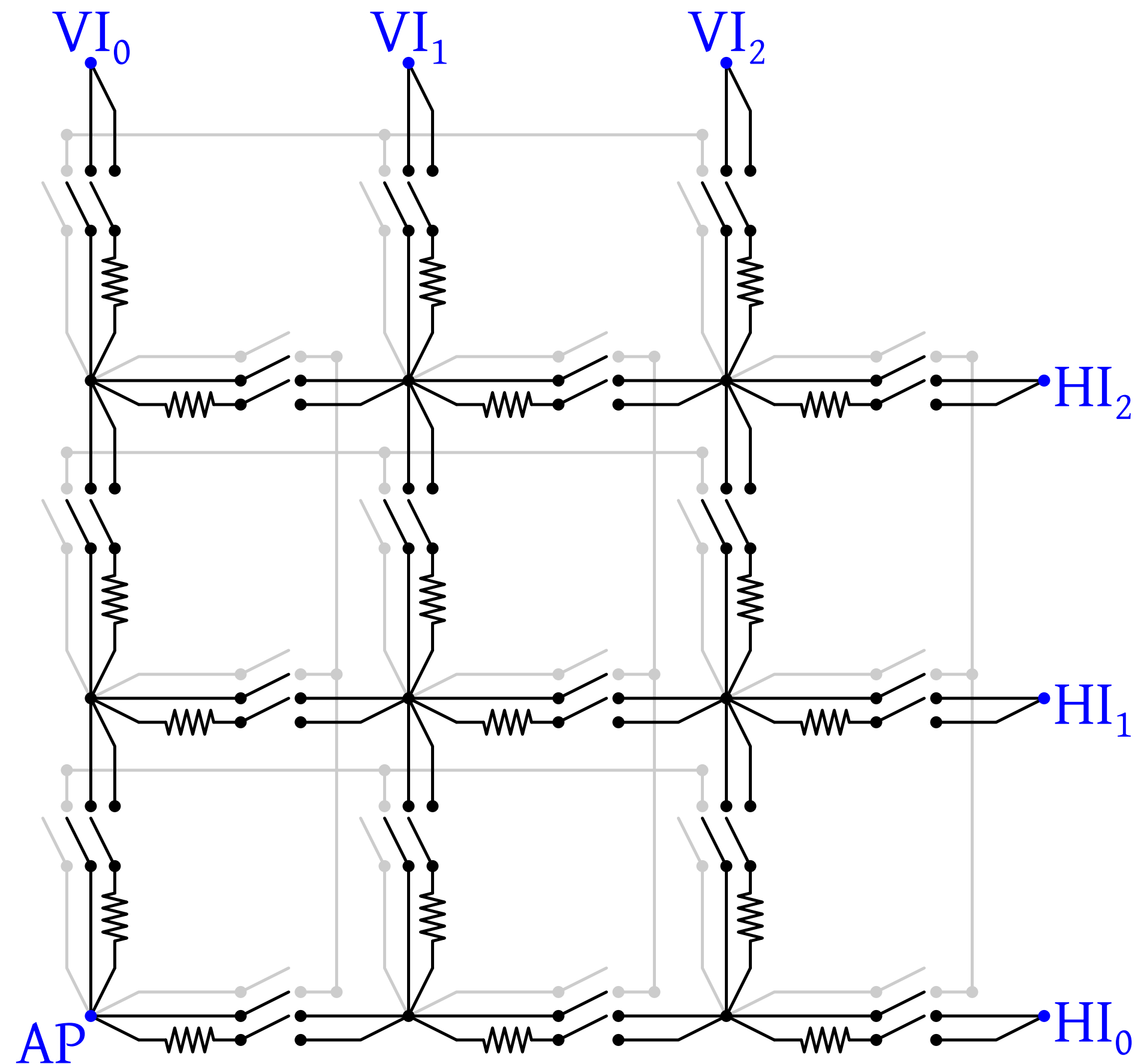
Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>



Field Programmable Resistor Network

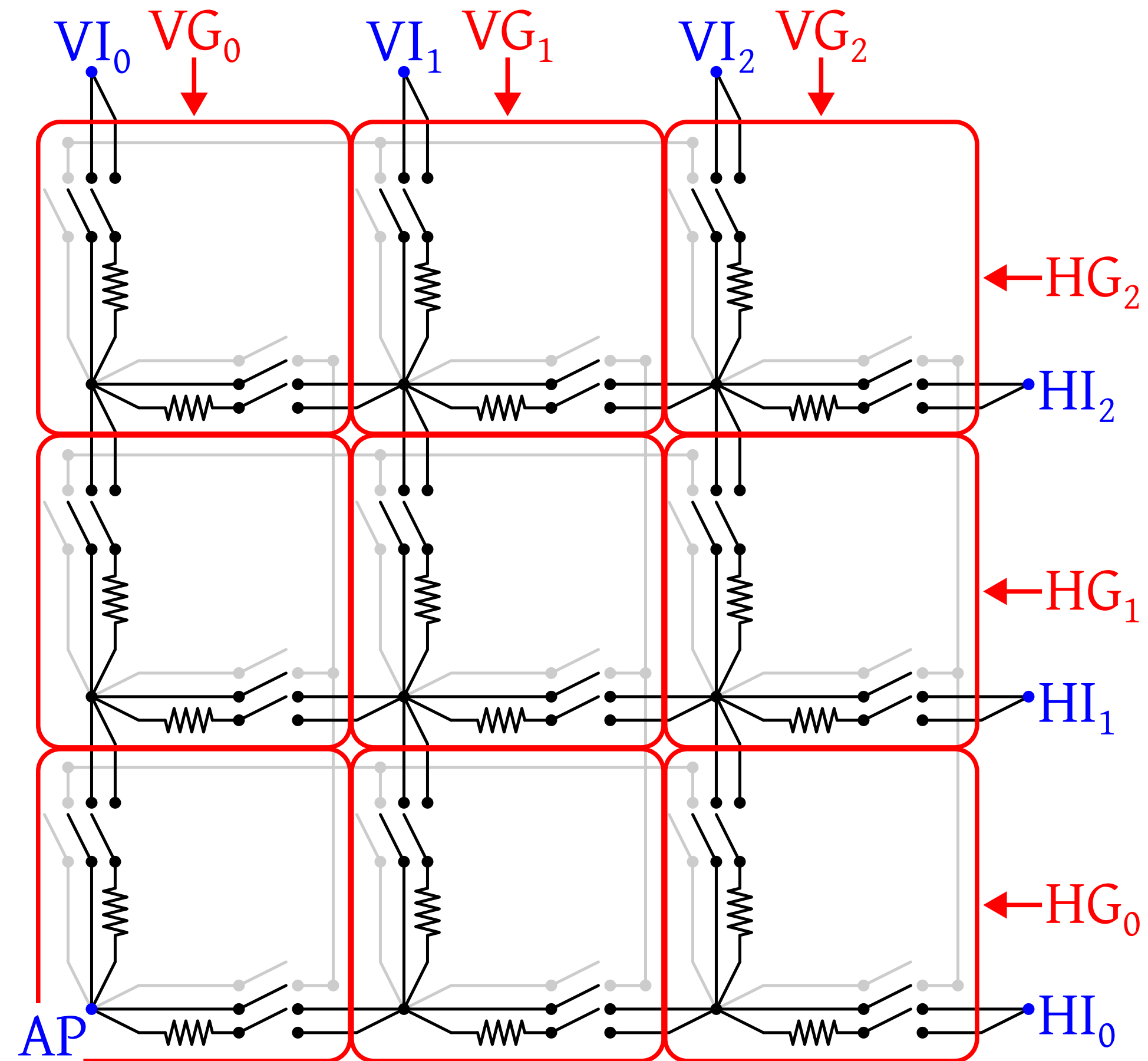
<https://github.com/htfab/tt07-fprn>



Field Programmable Resistor Network

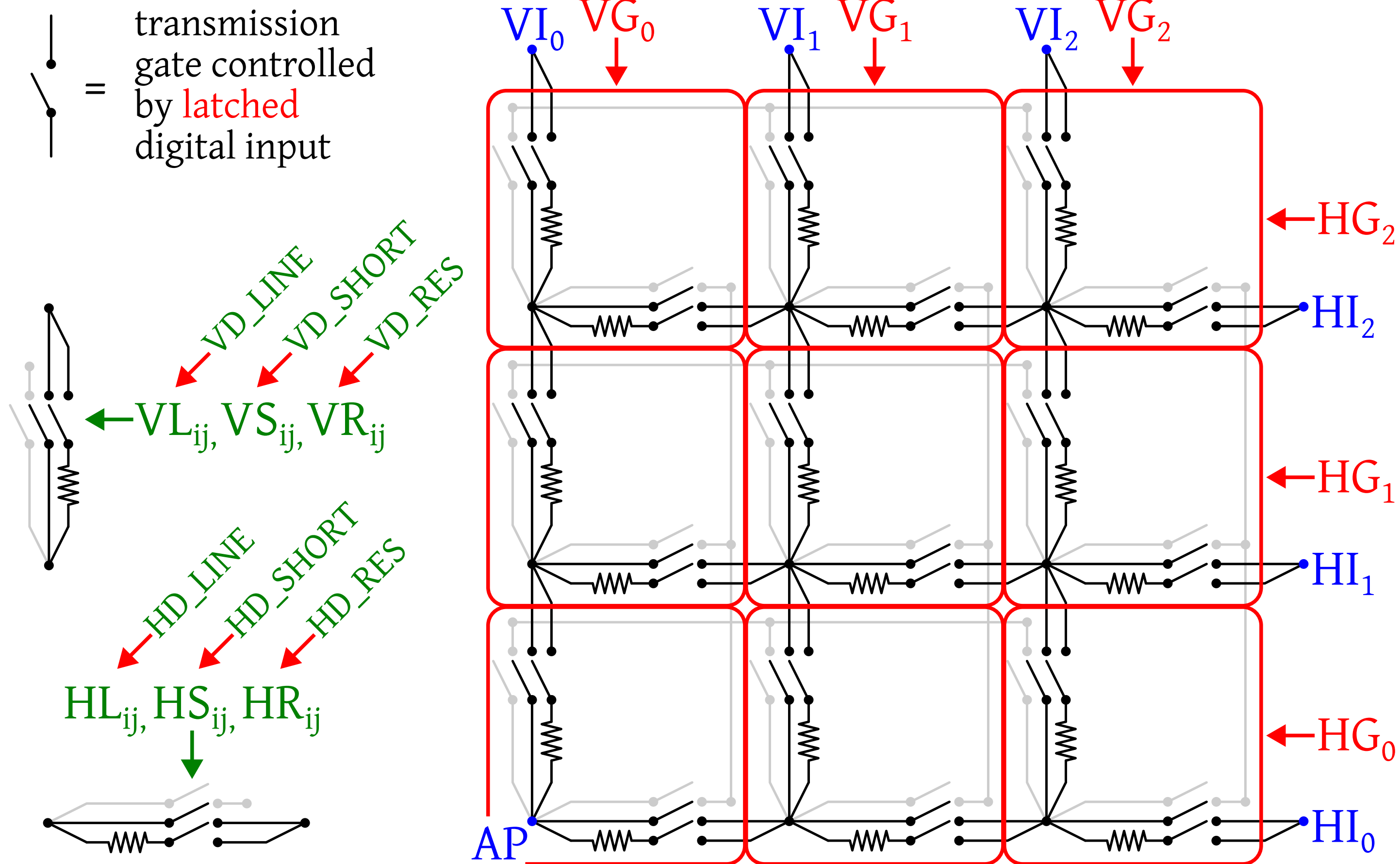
<https://github.com/htfab/tt07-fprn>

 = transmission gate controlled by **latched** digital input



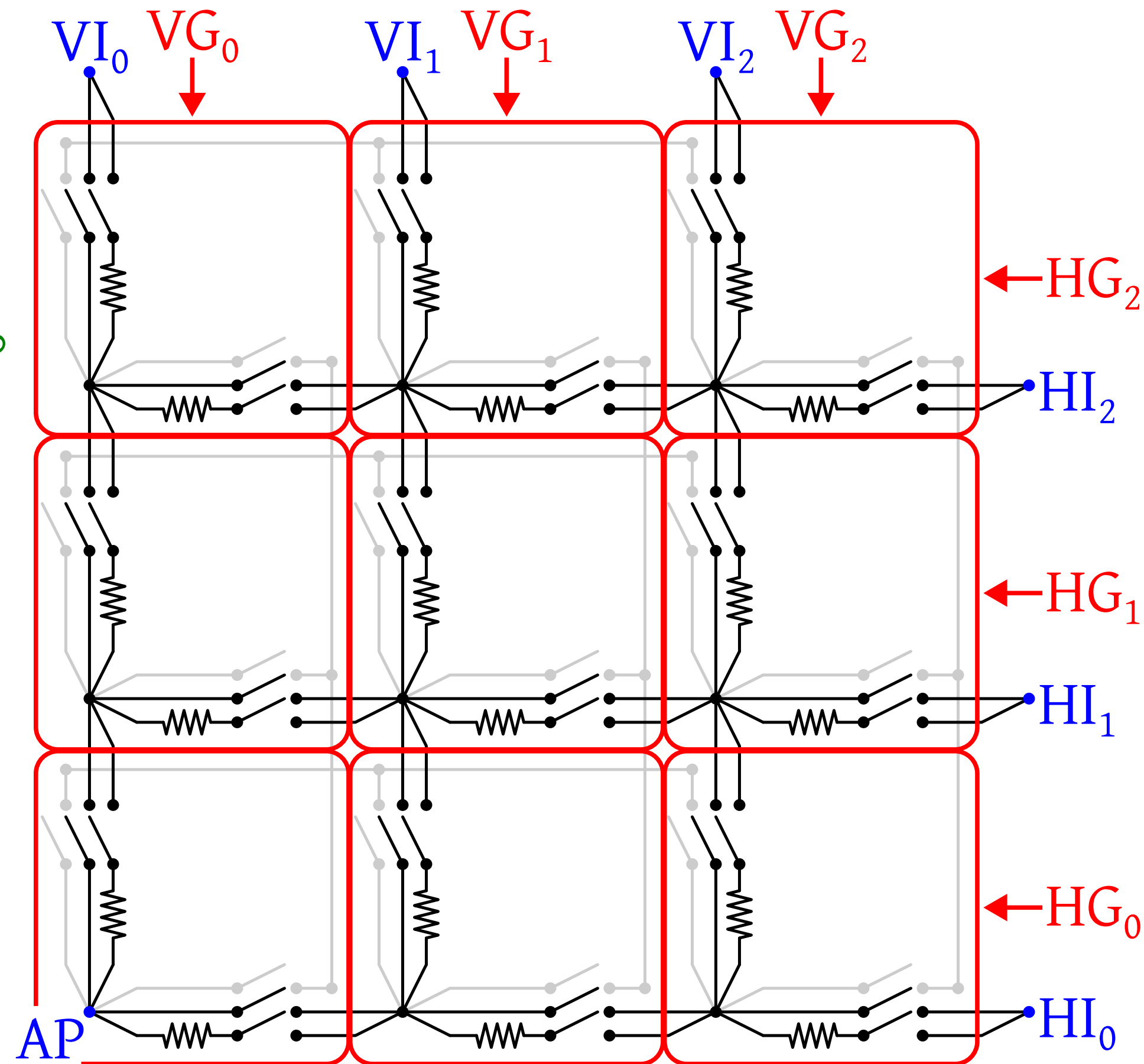
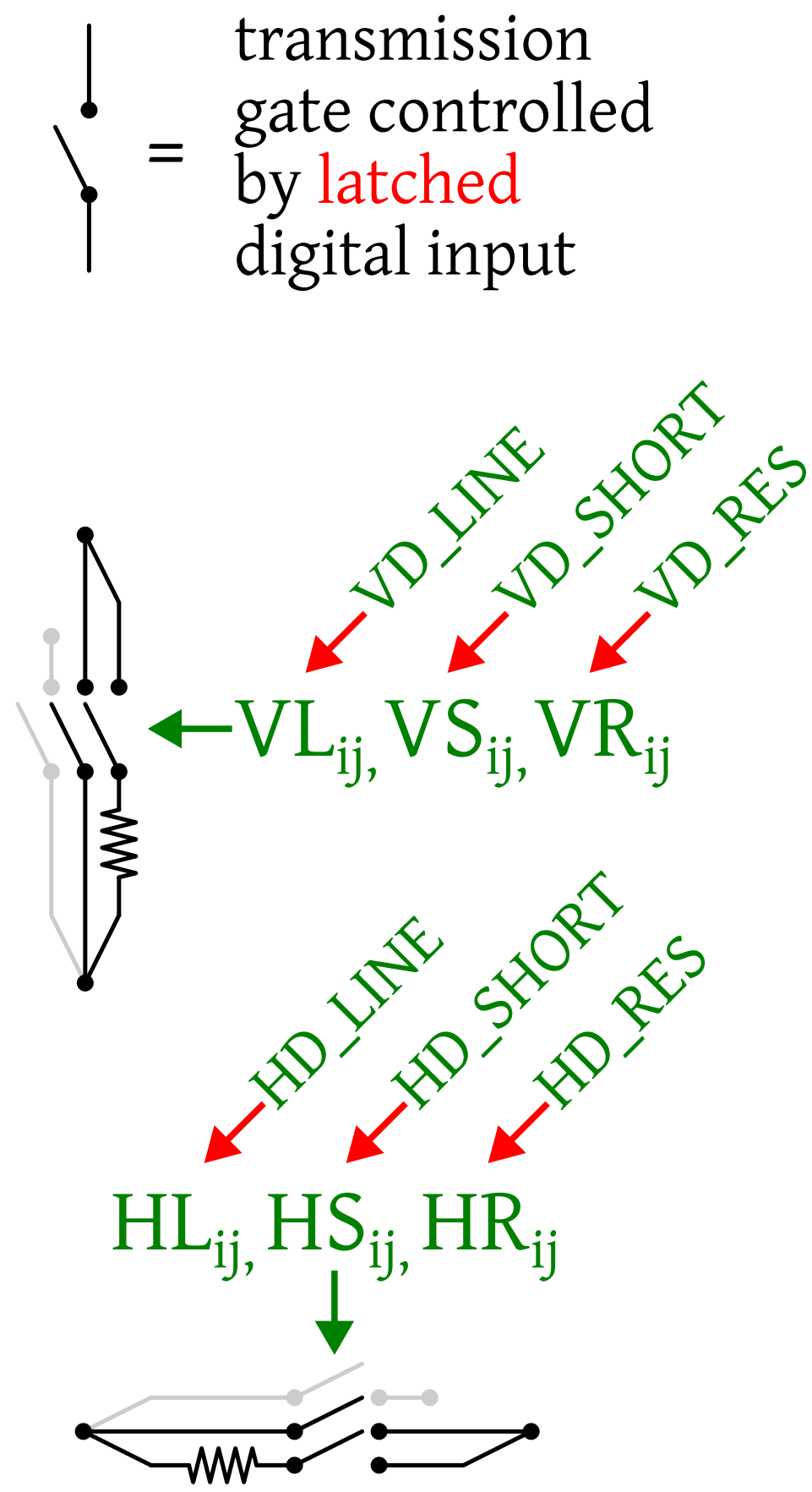
Field Programmable Resistor Network

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Field Programmable Resistor Network

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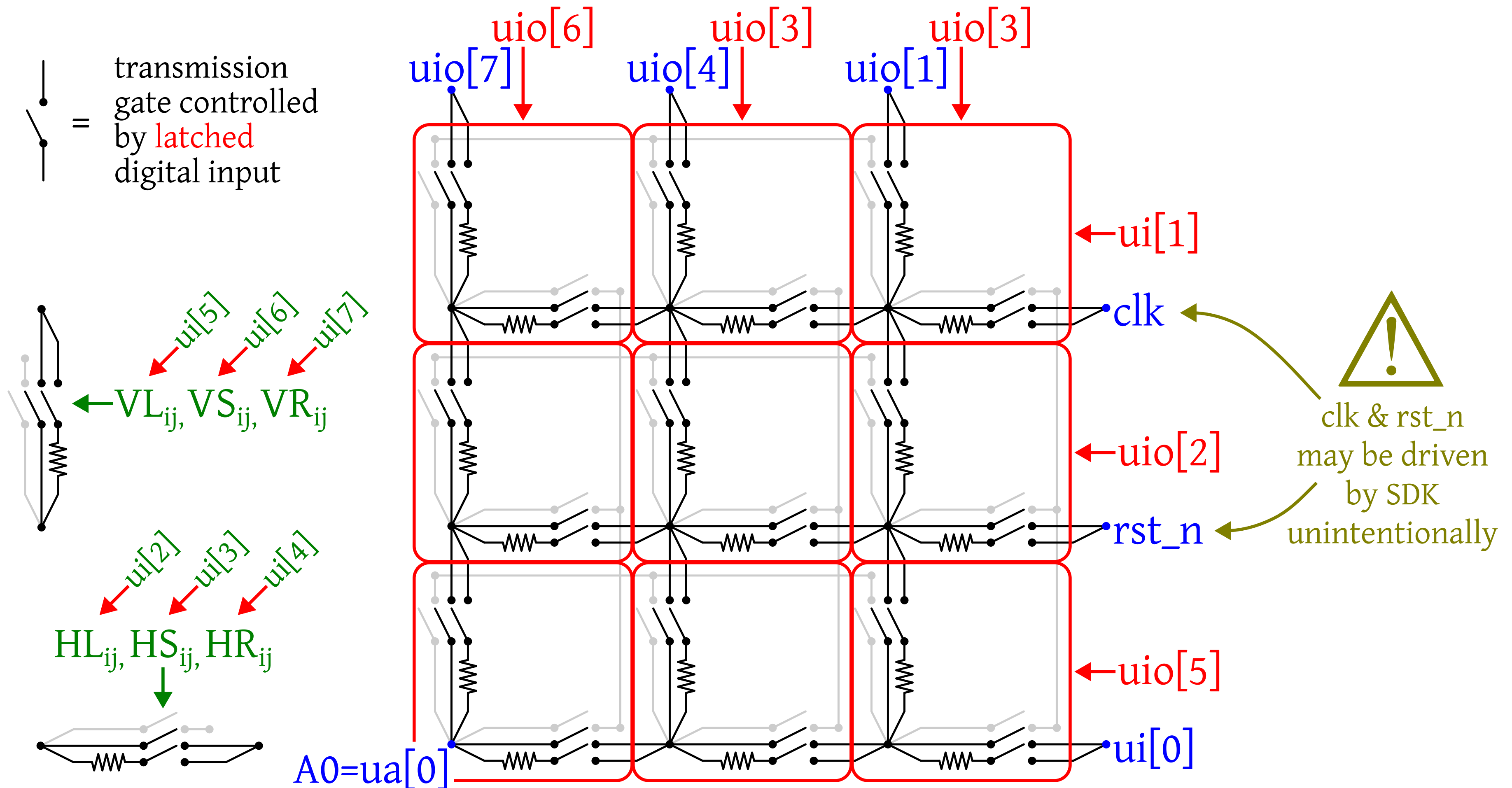
danger level
medium

safe power up
sequence:

- (1)
VD_{*}, HD_{*} off
VI_{*}, HI_{*} off
VG_{*}, HG_{*} on
- (2)
select the design
- (3)
VG_{*}, HG_{*} off
- (4)
configure as
intended

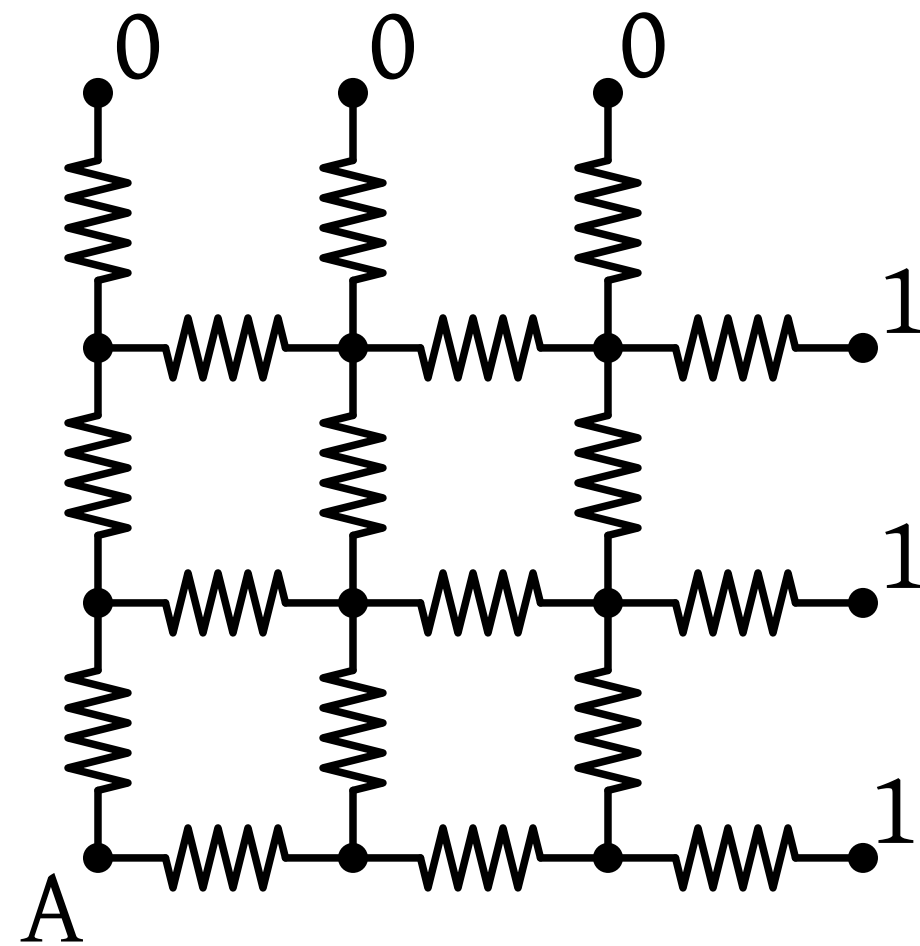
Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>

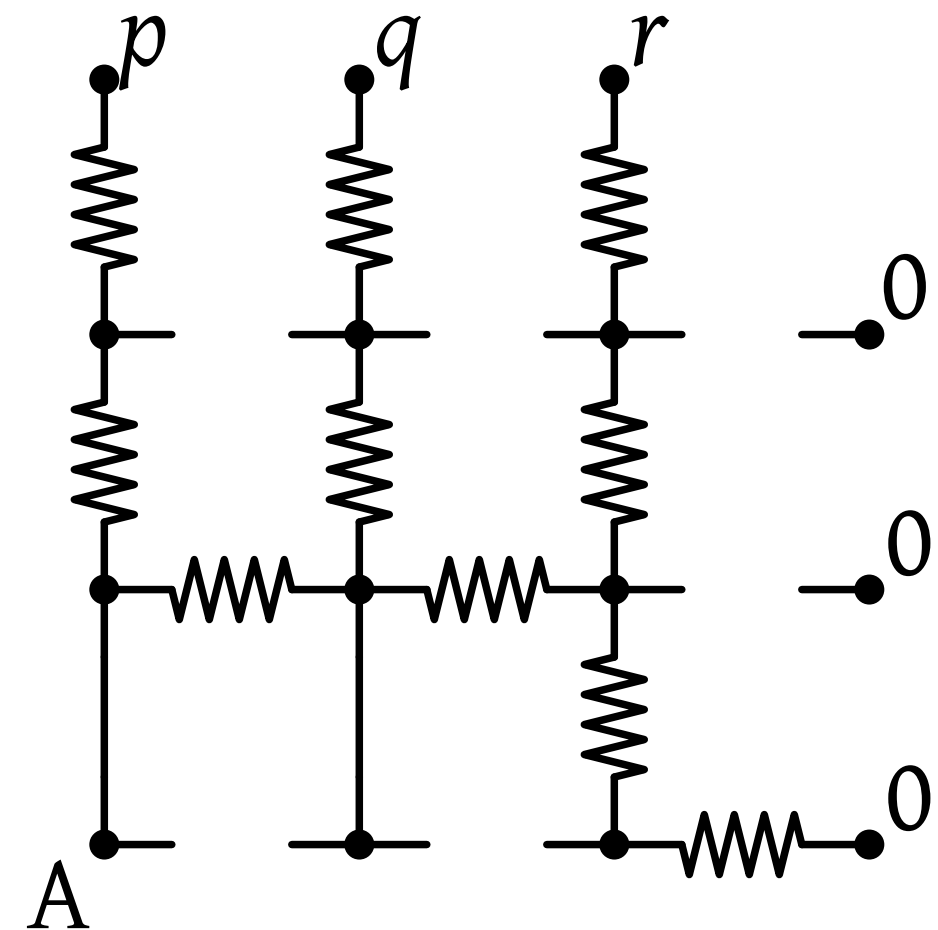


Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>



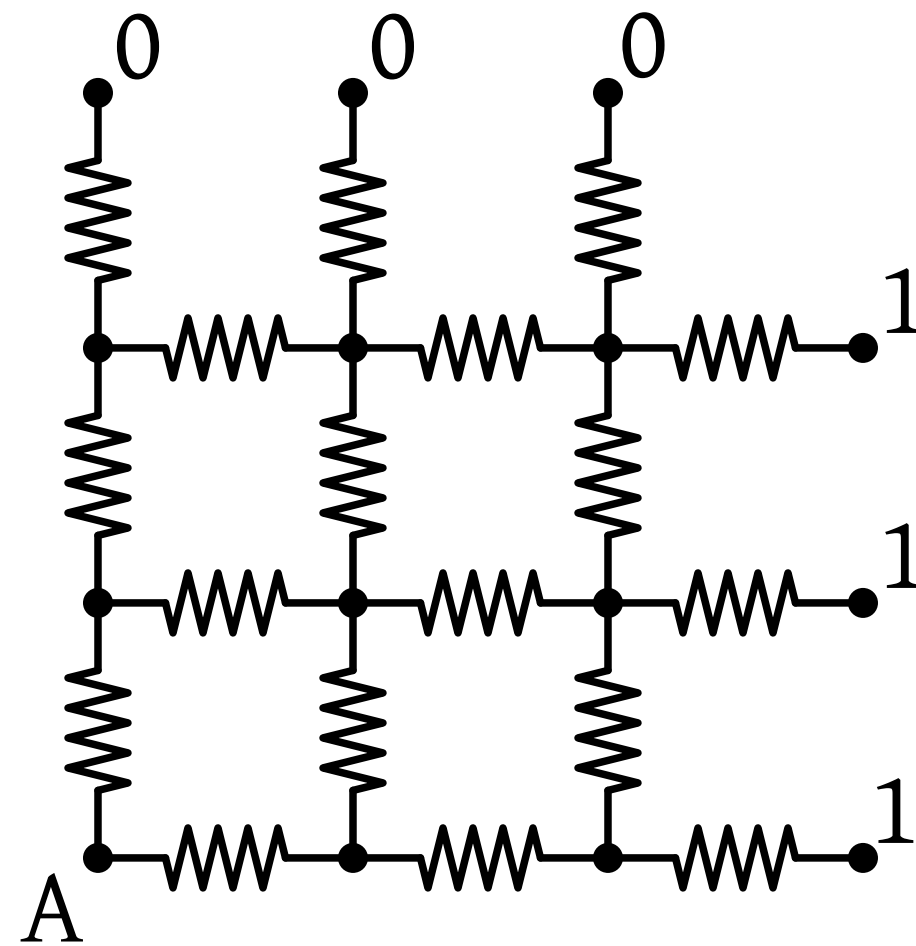
bringup/test1.py
(symmetric)



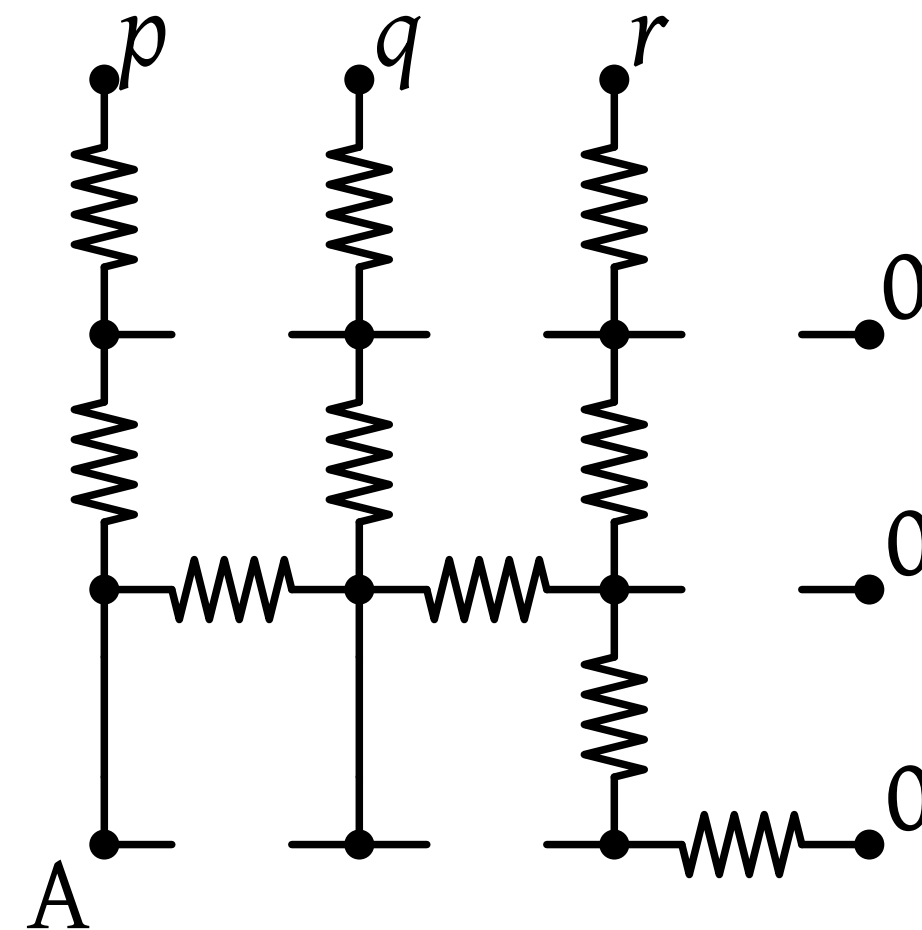
bringup/test2.py
(R-2R DAC)

Field Programmable Resistor Network

<https://github.com/htfab/tt07-fprn>



bringup/test1.py
(symmetric)



bringup/test2.py
(R-2R DAC)

The TT07 demo board has a 10 k Ω pull-down resistor on each analog pin. To get the right results, cut the jumper next to the “A0” U.FL connector.