

NetSpeed Orion

Release Notes

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NetSpeed Orion 16.09 Release Notes

About This Document

This document lists the release notes for NetSpeed Orion. Using NetSpeed NocStudio, users can define NoC architectures, describe specifications and requirements, optimize the NoC design and finally generate the NoC IP files such as RTL, testbench, synthesis scripts, NoC IP documentation etc.

Audience

This document is intended for users of NocStudio:

- NoC Designers
- NoC Architects
- SoC Architects

Prerequisite

Before proceeding, you should generally understand:

• Basics of NetSpeed Orion IP Technology

Related Documents

The following documents can be used as a reference to this document.

NetSpeed NocStudio User Manual

Customer Support

For technical support about this product, please contact support@netspeedsystems.com

For general information about NetSpeed products refer to: www.netspeedsystems.com



Contents

A	bout	This	Document	2
A	udien	ce		2
P	rerequ	iisite		2
R	elated	Doc	uments	2
C	ustom	er Sı	upport	2
1			bles	
2			ion	
3	Feat	ure l	Updates	7
	3.1	No	C Construction Improvements	7
	3.1.		Isolate and reduce congestion	
	3.1.	2	Automated FIFO sizing	7
	3.1.	.3	Automated tune_links	7
	3.2	GU	I Enhancements	7
	3.2.	1	Hide toolbars to increase screen real estate	7
	3.2.	2	Minimize the toolbars and the property panels to side tabs	7
	3.2.	.3	Hide inactive layers after mapping	7
	3.2.	4	New way to add/remove link and port blockages in the Blockage View	8
	3.2. but	5 ton	Collapse all the buttons to add the different agents and bridges to single 'Add' 8	
	3.2.	.6	Errored commands remain on console for editing	8
	3.2.	7	Change color of link when clicked	8
	3.3	Mu	lti-NoC	8
	3.4	Fur	nctional Safety	8
	3.5	Syr	chronizer Depth and FIFO Sizing	9
	3.6	Tra	ffic Class Optimization	9
	3.7	Use	er Defined System Resource Override	9
	3.8	Ver	rification Updates	9



	3.8.	1 Updated mailbox syntax	9
	3.9	System C Model	10
4	EDA	A Tool Compatibility	11
5	Erra	ta	12
	5.1	AHB	12
	5.2	Priority Address Map	12
	5.3	GUI add_bridge command doesn't work properly	12
	5.4	NocStudio calculated FIFO_depth may be 1 entry short	12
6	Cha	nges to Commands and Properties	13
	6.1	Command Changes	13
	6.2	Default Property Changes	
	6.3	Mesh Property Changes	14
	6.4	Bridge Property Changes	15
	6.5	Host Property Changes	15
	6.6	Interface Property Changes	15
	6.7	Link Property Changes	16
	6.8	Router Property Changes	16
	69	VC Property Changes	16



1 Deliverables

- NetSpeed NocStudio Package, N7 version supporting 16 layers and 256 bridges.
- NocStudio executable with interactive GUI.
- Verification checkers to be used in the DV environment.
- Sanity Test Bench.
- Documentation
 - a. NocStudio User Manual: The User Guide describes how to set up a system using NocStudio and how to use it to generate NetSpeed IP.
 - b. IP Integration Spec: The Integration Manual describes how to integrate a configured network into a larger subsystem.
 - c. Technical Reference Manual: The Technical Reference Manual describes how the functionality of the various NoC elements, the features and functions available, and how to dynamically change the functions using the programmer's mode.



2 Installation

- NocStudio uses FlexLM based licensing.
 - Copy over the license file emailed separately into a folder, and point LM_LICENSE_FILE environment variable to this license file before launching NocStudio.
 - NOTE: When untarring Linux files, ensure it is done on a Linux machine.
 Untarring Linux files on a Windows machine causes problems with symbolic links.
 - o The executable requires Linux CentOS 5.5 or higher.
- The release makes use of Qt libraries covered under LGPL:
 - http://qt-project.org/downloads



3 Feature Updates

3.1 NoC Construction Improvements

3.1.1 Isolate and reduce congestion

NocStudio can now take special consideration while mapping high bandwidth traffic (based on the traffic rate or overall load on the transmitting and receiving interfaces) to isolate them from other traffic so as to reduce congestion in the NoC.

- The user can also sort traffic flows so as to map high bandwidth traffic flows first using the option "high_rate_traffic_flows" in the sort order argument to the map_opt command. NocStudio also automatically uses "high_rate_traffic_flows" as one of the many sort orders to sort traffic flows in each iteration of map_opt.
- The user can choose to make NocStudio avoid the mapping of high rate traffic flows and low rate traffic flows on the same VC as part of its mapping algorithm by using the "separate_high_rate_traffic_flows" argument in map_opt.

3.1.2 Automated FIFO sizing

Automatic sizing of FIFO depth on VC's with high bandwidth traffic during tune_links to prevent congestion in the NoC and head of line blocking.

3.1.3 Automated tune_links

tune_links is now run automatically at the end of map_opt to tune the link widths and FIFO depths. *tune_links* is also a separate command that a customer can choose to run.

3.2 GUI ENHANCEMENTS

3.2.1 Hide toolbars to increase screen real estate

Press Ctrl+H to hide/unhide the drawing toolbar and the top toolbar to increase the screen real estate.

3.2.2 Minimize the toolbars and the property panels to side tabs

The drawing toolbar, top toolbar and the property toolbar can be minimized to the sides to appear as tabs.

3.2.3 Hide inactive layers after mapping

The layers that don't have any traffic mapped on them are automatically hidden after mapping.



3.2.4 New way to add/remove link and port blockages in the Blockage View

In the blockage view, left click on a link to add/remove a link blockage (add/remove blockage to that link on all layers), and right click to add/remove a port blockage (add/remove blockage on the link only on the clicked layer).

3.2.5 Collapse all the buttons to add the different agents and bridges to single 'Add' button The add buttons of different agents (Bridge and SIB) are all collapsed into one 'Add' button.

3.2.6 Errored commands remain on console for editing

An errored command remains on the console so that the user can edit it rather than having to retype the entire command.

3.2.7 Change color of link when clicked

When a link is clicked, its color changes to pink so that the user knows which link he is currently viewing the state/properties of.

3.3 MULTI-NOC

RTL from multiple NoCs can now be integrated into the same design for simulation. Each NoC must have a unique mesh name and the following NocStudio property setting:

prop_default tag_project_name yes

Two or more NoCs can be integrated with no upper limit to the number of NoCs. Only one coherent NoC is supported per set of NoCs. Crux NoCs cannot be mixed with Orion/Gemini NoCs.

3.4 FUNCTIONAL SAFETY

For safe and reliable operation of the device, NocStudio now supports:

- End to end transport error checking, including Data/Sideband ECC Protection, Data/Sideband Parity Error Detection
- 2. Hop to hop error checking of packet fields
- Error reporting

For more details, please refer to Chapter "Safety and Reliability" in the TRM.



3.5 SYNCHRONIZER DEPTH AND FIFO SIZING

NocStudio now supports programmable synchronizer depths, both for general synchronizers (ns_demet.v) and for reset synchronizers (ns_rst_n.v). Users may select synchronizer depths, based on their own library/process requirements, on a per-clock-domain granularity.

3.6 TRAFFIC CLASS OPTIMIZATION

In many cases, it may make sense to have requests and responses have different traffic classes. Traffic flows to memory, for instance, may want to have different traffic classes for the different requests. This is particularly true when different priorities are used for the traffic classes.

The response path may not need separate traffic classes. If a low priority response is stuck leaving the memory controller, it can block high priority responses as well. It will often make sense to have these responses share a single traffic class.

This can also significantly reduce area within a NoC. Traffic classes require separate virtual or physical channels, which requires more storage and possibly more links. While this may be useful for traffic going to memory, the expense of the response path may be unnecessary. And since data paths are often wider than request paths, this can be a significant area impact.

NocStudio allows each hop of a traffic flow to indicate a separate traffic class, to provide user with the flexibility to configure various requests and responses to either use different traffic classes or share them.

3.7 USER DEFINED SYSTEM RESOURCE OVERRIDE

Support for user defined NOC system level resource for compatibility of families of products. In NoC synthesis flow, the tool automatically calculates the resource needed for a given project to provide the most optimized solution. But in terms of derivative or re-spin, due to some ECO changes, NocStudio may expand the resource (e.g. AID width). In order for the NocStudio to preserve the NOC resource, SOC architect/designer needs to predict the address width, AID width, User width, etc. and use mesh_prop "sys_*_width <N>" to override the System level NOC resource.

3.8 VERIFICATION UPDATES

3.8.1 Updated mailbox syntax

A new argument "proj_tag" has been added as follows:

Old syntax:

`NS_E2E_CHECKER_TOP.ns_transaction_src_mbox.try_get(ns_src_transaction)



New syntax:

`NS_E2E_CHECKER_TOP.ns_transaction_src_mbox[proj_tag].try_get(ns_src_transaction) where "proj_tag" is a string that should be,

- Set to project_name if the noc is using 'tag_project_name'
- Set to an empty string if the noc is not using 'tag_project_name'

3.9 System C Model

In order to integrate C++ based models into custom environments it is necessary to define interfaces and APIs to enable integration and interacting with 3rd party models, debuggability and analysis of results. NetSpeed provides C++ based models for such integration purposes based on the SystemC TLM2 standard. The current mode specifically addresses the need for fast functional modeling in order to fit into platform models for software and firmware verification and development. Such models are typically referred to as LT (loosely timed) or PV (programmers view).



4 EDA Tool Compatibility

- Cadence EDA tools were used for verification and synthesis of this product.
- Compatibility testing has been done with VCS. Issues, if any, might be seen in the
 verification IP for specific configurations. The NetSpeed IP Integration specification lists the
 various defines to be used to enable / disable Verification IP. NetSpeed support will be
 available to resolve any issues.



5 Errata

5.1 AHB

There could be a deadlock between AHB master bridge and the AHB master if master is waiting for HREADY to be asserted before removing the BUSY command. Workaround: remove BUSY as soon as the next command is available.

5.2 Priority Address Map

The Priority Address Map has a potential issue when some agents do not have access to a slave in a foreground range. Instead of getting a decode error when they attempt to access those ranges, they can hit against the background range and send the request to that slave.

5.3 GUI ADD_BRIDGE COMMAND DOESN'T WORK PROPERLY

An extra -bridge_id string is invoked which results an GUI error. Please repeat the same command without the -bridge_id to workaround the issue.

5.4 NOCSTUDIO CALCULATED FIFO_DEPTH MAY BE 1 ENTRY SHORT

NocStudio automatically derived FIFO depth on link based on traffic requirement. When ILDC (In Link Domain Crosser) is enabled with ECC, the calculated FIFO depth may not be ideal. Please note that this issue only applies to NOC boundaries (i.e. master bridge --> router or router --> slave bridge). User can workaround it with increased FIFO depth using link_prop fifo_depth <depth> command.



6 Changes to Commands and Properties

6.1 COMMAND CHANGES

Command Name	Comment
add_output_reg_across_rtl_group	Name change. Previous name was
	add_output_reg_across_rtlgroup
show_project	New command to print the project name
set_clock_domain_sync_depth	New command to set the synchronizer depth of a clock
	domain
map	This command has been deprecated
tune_route	This command has been deprecated
tune_links	This command has been deprecated

6.2 DEFAULT PROPERTY CHANGES

Property Name	Default Value	Comment
msg_peak_bw	6.4	This property has been deprecated
msg_peak_rate	0.1	This property has been deprecated
peak_multiplier	1	New property to specify the default multiplier to calculate peak rate/bw from average rate/bw when the peak rate/bw is not specified
sync_input_register	no	New default property to enable or disable input registering at the bridge when there is no clock crossing at the bridge.
synchronizer_depth	2	New property to specify the default value of synchronizer depth for all clock domains.
axi4m_logical_processors	1	New property to specify the default number of logical processors supported by the master bridge



axi4m_exclusive_support	Yes	New property to specify if a master has AXI exclusive support by default
sysc_enable	no	Enable generation of SystemC LT model (Special License needed)
read_burstiness	1	The default value for this property has been changed from 3 to 1

6.3 MESH PROPERTY CHANGES

Property Name	Default Value	Comment
	varue	
compact_regbus_address_space	Yes	Default value of this property has changed
		from No to Yes
extra_bandwidth_provisioning	0	Default value of this property has changed
		from 25 to 0
sys_r_user_bits_per_byte	-1	New property to specify the default for the
		system R user width per byte.
sys_r_user_width	-1	New property to specify the default for the
		system R user width.
sys_b_user_width	-1	New property to specify the default for the
		system B user width
sys_ar_user_width	-1	New property to specify the default for the
		system AR user width
sys_axi4_aw_aid_width	-1	New property to specify the default for the
		system AXI4 AW AID width.
sys_mst_id_width	-1	New property to specify the default for the
		system master ID width
sys_aw_user_width	-1	New property to specify the default for the
		system AW user width.
sys_axi4_addr_width	-1	New property to specify the default for the
		system AXI4 address width.



sys_axi4_ar_aid_width	-1	New property to specify the default for the system AXI4 AR AID width.
sys_w_user_bits_per_byte	-1	New property to specify the default for the system W user width per byte.
stats_level	High	New property to specify the default level of statistics collection in NocStudio performance simulator
errorcheck_granularity	502	New property to specify the default upper limit of error check granularity (Special license needed)

6.4 BRIDGE PROPERTY CHANGES

Property Name	Comment
axi4s_logical_id_enb	New property that indicates whether logical ID is enabled for read and write requests to a slave bridge
sync_input_register	New property that enables input registering at the bridge when there is no clock crossing at the bridge
axi4m_logical_processors	New property to specify the number of logical processors supported by the master bridge
axi4m_exclusive_support	New property to specify if a master has AXI exclusive support

6.5 HOST PROPERTY CHANGES

None

6.6 INTERFACE PROPERTY CHANGES

Property	Comment
Name	
internal_pip	New property to decide the type of registering between switch and protocol
eline	side for each host interface. One of these options can be enabled to remove
	timing paths between switch and protocol processing logic.



6.7 LINK PROPERTY CHANGES

None

6.8 ROUTER PROPERTY CHANGES

None

6.9 VC PROPERTY CHANGES

None



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