

## ***1. Description***

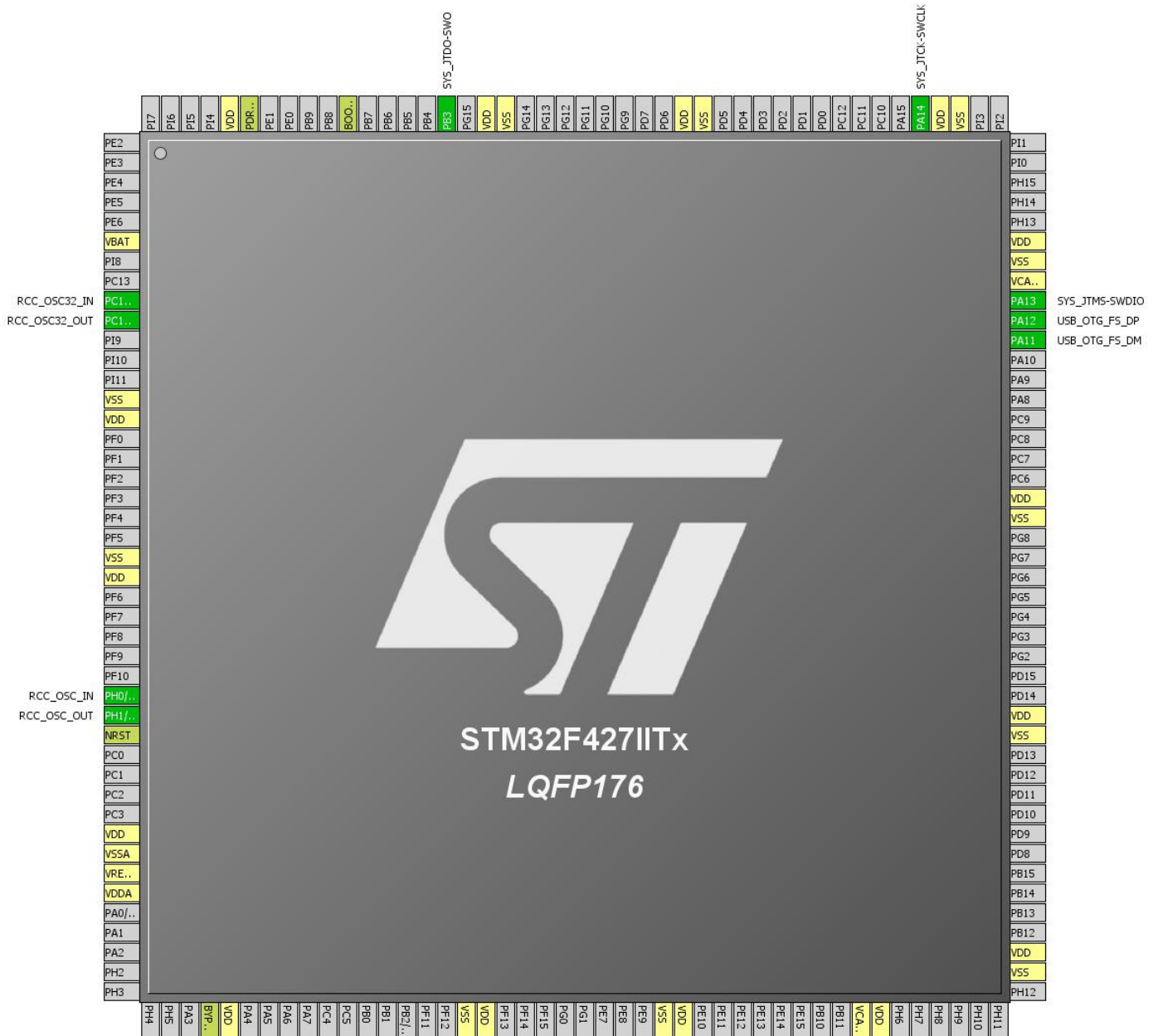
### 1.1. Project

Project Name	stm32_cube
Generated with:	STM32CubeMX 4.2.0
Date	09/11/2014

### 1.2. MCU

MCU Serie	STM32F4
MCU Line	STM32F427/437
MCU name	STM32F427IITx
MCU Package	LQFP176
MCU Pin number	176

## 2. Pinout Configuration



### 3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	PH0/OSC_IN
		RCC_OSC_OUT	PH1/OSC_OUT
	Low Speed Clock (LSE) : Crystal/Ceramic Resonator	RCC_OSC32_IN	PC14/OSC32_IN
		RCC_OSC32_OUT	PC15/OSC32_OUT
SYS	Debug: SWD and Asynchronous Trace	SYS_JTMS-SWDIO	PA13
		SYS_JTCK-SWCLK	PA14
		SYS_JTDO-SWO	PB3
USB_OTG_FS	Mode: Host_Only	USB_OTG_FS_DM	PA11
		USB_OTG_FS_DP	PA12

MiddleWare	Mode
FATFS	USB
FREERTOS	Enabled
USB_HOST	Class for FS IP: Mass Storage Host Class

## 4. Pins Configuration

Pin	Pos	Function(s)	Label
PC14/OSC32_IN	9	RCC_OSC32_IN	
PC15/OSC32_OUT	10	RCC_OSC32_OUT	
PH0/OSC_IN	29	RCC_OSC_IN	
PH1/OSC_OUT	30	RCC_OSC_OUT	
PA11	122	USB_OTG_FS_DM	
PA12	123	USB_OTG_FS_DP	
PA13	124	SYS_JTMS-SWDIO	
PA14	137	SYS_JTCK-SWCLK	
PB3	161	SYS_JTDO-SWO	

## 5. Power Plugin report

### 5.1. Microcontroller Selection

Serie	STM32F4
Line	STM32F427/437
MCU	STM32F427IITx
Datasheet	024030_Rev3

### 5.2. Parameter Selection

Temperature	25
Vdd	3.3

### 5.3. Battery Selection

Battery	Not set
Capacity	0.0 mAh
Self discharge	0.0 %/month
Nominal voltage	0.0 V
Max Cont Current	0.0 mA
Max Pulse Current	0.0 mA
Cells in series	1
Cells in parallel	1

## 6. Software Project

### 6.1. Project Settings

Name	Value
Project Name	stm32_cube
Project Folder	C:\!Work\stm32_cube
Toolchain / IDE	TrueSTUDIO 4.3.1
Firmware Package Name and Version	STM32Cube FW_F4 V1.1.0

### 6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

### 6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed