

**CSE- 311 , Microprocessors and Assembly language**  
**Class Test # 3, Marks= 20 minutes , Time= 20 minutes (Upload 15 minutes)**

1.	What are the functions of data bus transceiver and address latch in 8086 minimum mode configuration, explain with the help of pins $\overline{\text{DT/R}}$ , $\overline{\text{DEN}}$ and ALE.	8
2.	Specify the <b>functions</b> of the following pins i. $\overline{\text{S}}_0$ , $\overline{\text{S}}_1$ , $\overline{\text{S}}_2$ ii. $\overline{\text{RQ/GT0}}$ iii. $\overline{\text{MN/MX}}$	6
3.	Draw the block diagram of 8086 in <b>minimum mode</b> configuration.	6