August 23rd (Mon).

CMPE240 Section 1.

CMPE240

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Office Hours: M.W. 3:40-4:40 pm.

Advanced Microprocessor Systems = Prototype System with A CPU module

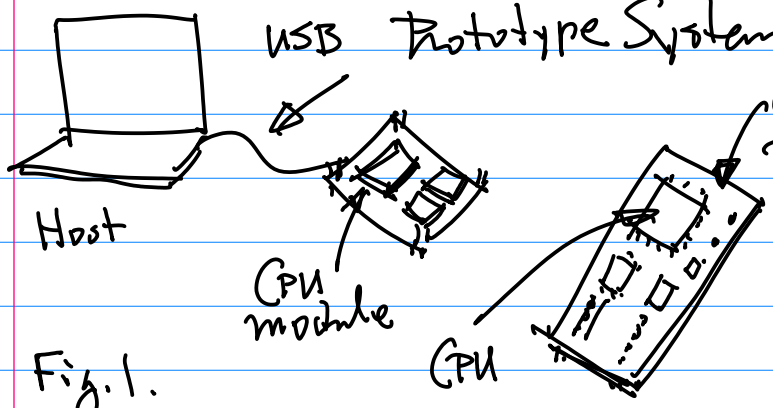

Fig. 1.

GPU (Graphics Processing Unit), Array of Processors, Machine Learning, AI. Autonomous Systems. Nvda Jetson TX2.

Textbooks, References

1° NXP LPC1769 CPU Datasheet 800+ pages Homework: Down Load pdf. Before Next Monday, Aug. 30th.

2. LPC1769 schematics of the CPU module

3. Nvda Jetson NANO Datasheet on TX2 (6 CPU + 256 GPU) 400+ pages. 5% Bonus. (optional)

4. RISC-V. Open Source Architecture, A super set of ARM. FPGA, Verilog. SoC. +RTOS. (optional) +5%

A proposal (One Paragraph) By Sept. 1st (Wed). submit to my Email;

Note: Buy LPC1769 CPU module.

digi-key.com, mouser.com, etc.

Grading Policy & Projects

2 projects (phase I & II)

2D Graphics Processing Engine → 3D Graphics Processing Engine } a le

CmpE240

2D Graphics Processing Engine → 3D Graphics Processing Engine { @ 3D shadow. 10% = 30pts, 30%.
& Diffuse Reflection 10%

10%

midterm: 30%, Final 40% (Comprehensive)

Option1. (5%+) NVDA NANO

{a. Likely Devices Drivers, O.S. C/C++, Python.
b. I/O Interface: "EdgeAI"
GPIO, SPI.

Option2. (5%+) RISC-V Target

SoC, FPGA Board,

Proposal (one paragraph), Submission
By Sept 1st (Wed) via E-mail.

Policy ON Project Submission.

1°. Form 3-4 person Team.

2° No Source Code/Design material Can be Copied; All Course material has to be completed individually;

3° Late Project, 10% per week;