

# CMPE240 Spring 22

✓

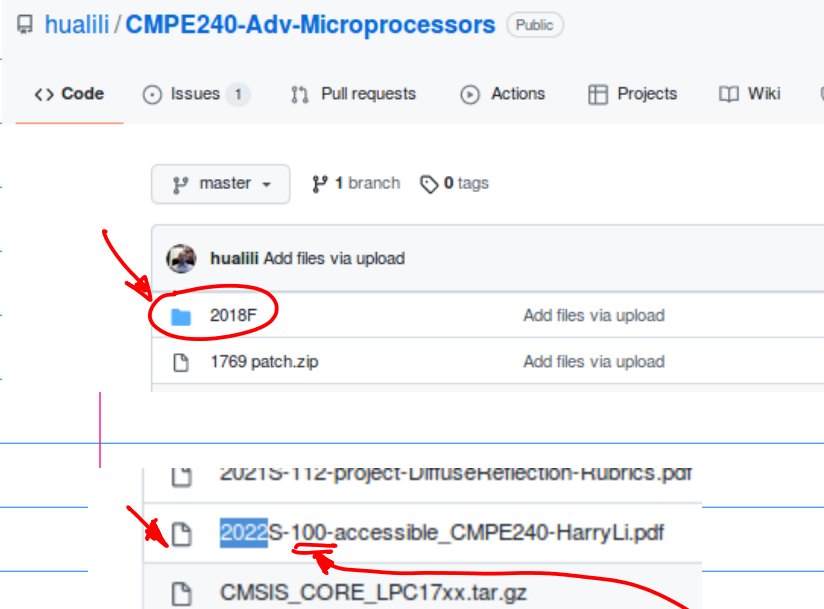
Jan 26 (Wed)

Today's Topics: Introduction  
& Organizational meeting.

HARRY LI, Ph.D.

GreenSheet: On-Line from github

<https://github.com/hualili/CMPE240-Adv-Microprocessors>



Naming Convention: Yr + Semester + ID +  
Name of the Doc.

E-mail: hua.li@sjsu.edu

Text message: (650) 400-1116

Office Hours: Mondays & Wednesdays  
4:30 - 5:30 PM.

Office hours Zoom link: Join Zoom Meeting  
[https://us04web.zoom.us/j/9841607683?](https://us04web.zoom.us/j/9841607683?pwd=UIA3aEk1TnV4bjNLQk5CQkw0dDk4UT09)  
pwd=UIA3aEk1TnV4bjNLQk5CQkw0dDk4UT09  
Meeting ID: 984 160 7683 Passcode:  
121092

TextBooks + Ref

1.

No text Book, But NXP CPU Datasheet  
is utilized as a Base Line Ref

2. SCH Design of the CPU module,  
PP5 (Rev.D.)

Prototype Board: Each person will  
Build his/her Prototype System.

Team work is encouraged, form 4 people  
team for this Class. However all the  
work has to be done individually.

Grading: midterm 30%  
Format: Written Exam But

Need to have prototype ready to execute  
programs, And get photos of your  
Prototype Board.

Final Exam: 40%, Similar format as  
the midterm.

Homework, Projects Counts Another 30%.

Written Announcement on White Board  
(Lecture Notes) And SJSU CANVAS.

Late Project submission will have 1 pt.

Penalty Per each Lecture Day.

Introduction.

1. Bill of material for L6C17bq  
Prototype Board Design.

CmPE240, S22

21