

CMPE 240

Bilinear Interpolations on Boundary Lines

Hi Harry Li,

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Apr 25, 2022 01:25 PM Pacific Time (US and Canada)

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Passcode: 3\$Q6F#2b

Diffuse Reflection Bilinear Interpolation on Boundary Lines

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Date: Apr 20, 2022 01:20 PM Pacific Time (US and Canada)

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Diffuse Reflection and Post Processing

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Date: Apr 18, 2022 01:20 PM Pacific Time (US and Canada)

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Diffuse Reflection

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Date: Apr 13, 2022 01:27 PM Pacific Time (US and Canada)

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3D Transformation Pipeline

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3D Shadows

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Midterm Review and World Coordinate System in 3D

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Mar 21, 2022 01:18 PM Pacific Time (US and Canada)

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Coding on SPI and DrawLine

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Mar 16, 2022 01:20 PM Pacific Time (US and Canada)

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Special Purpose registers PCONP, PINSEL, CR0, f_{spi} formula and drawline program

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Trees, SPI Special Purpose Registers (CR0 and CR1)

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Mar 9, 2022 01:16 PM Pacific Time (US and Canada)

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2D Transforms and Trees

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Date: Mar 7, 2022 01:11 PM Pacific Time (US and Canada)

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Squares and 2D transforms, Project Assignment

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Date: Mar 2, 2022 01:17 PM Pacific Time (US and Canada)

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SPI LCD interface and 2D vector graphics

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Feb 28, 2022 01:24 PM Pacific Time (US and Canada)

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LCD Interface and 2D Vector Graphics

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Topic: SP22: CMPE-240 Sec 02 - Adv Comp Design
Date: Feb 23, 2022 01:24 PM Pacific Time (US and Canada)

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LCD Hardware Interface and SPI Signals

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CPU Architecture, Memory Map and 2D Graphics Processing Engine Introduction with SPI Interface

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Date: Feb 16, 2022 01:24 PM Pacific Time (US and Canada)

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CPU Architecture and Memory Map

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IDE Xpresso and Import GPIO Project

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Schematics and GPIO Design

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Bill of Material and Schematics

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Introduction and Bill of Materials for prototype board, GPIO and SPI LCD display.

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First day of class

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