

CMPE240  
Sept. 7

10

Sept. 7.

Note: 1<sup>st</sup> LPC1769 from 2022S  
Semester, Waiting List.  
CANVAS. Scott.

2<sup>nd</sup> LPC1768 pin-to-pin  
Compatible. (Mbed)

a. Step 1. MCUXpresso IDE  
1768 Binary Code.

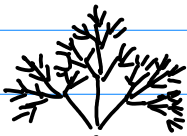
Step 2. "Firmware" Upload  
the binary file to the  
Flash. Need a prob

Step 3. Interactive Debugging.

3<sup>rd</sup> LPC1114 Digi-Key in Stock.  
LPC1114

GPP/SPI, FLASH (ON-Chip)

1/8 of the size  
Comparing to LPC1768/9



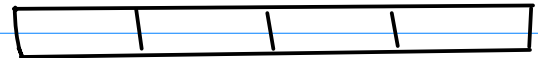
Homework (0 pt)

1. Form A Team By Wednesday.  
4-Person
2. Select/Finalize your target  
platform. By the end of the week.

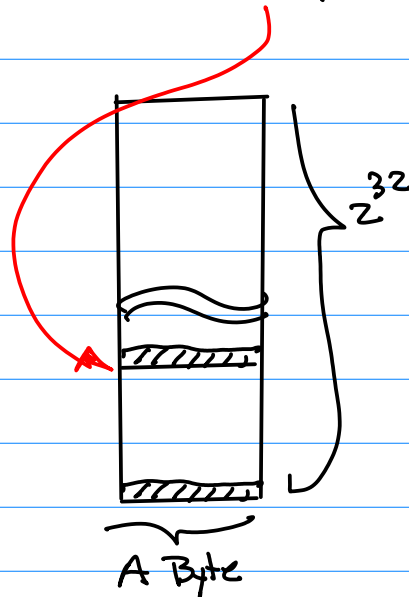
Example: Register File

} Special Purpose Registers  
General Purpose Register

GPx CON  
Prefix 3 Letters  
for Port "x", x=0,1,2,3



GPx CON its address is 32 Bits,  
it maps to the memory  
map.



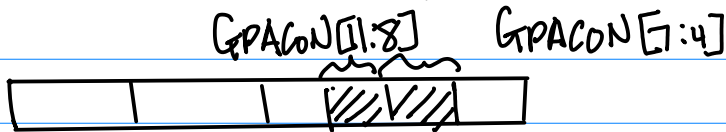
Note: The Task of Init & Config  
Can be realized by using HLL  
(High Level Language), C/C++,  
to deposit A Binary Pattern to that  
Memory Location (Addr. is a pointer)

QmpE240

Sept. 7

For Example for Samsung ARM-11.

Consider:  
Power Up Address + Power Traces.  
Booting.



GPxCON its address is 32 Bits,  
it maps to the memory  
map.

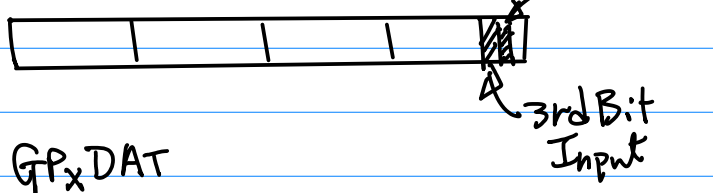
Design Requirements (Spec.)

1. 2nd Bit AS An Output
2. 3rd Bit AS An Input

To perform Init & Config.

$$GPACON[7:4] = 0001 = 0x1$$

$$GPACON[1:8] = 0000 = 0x0$$



$$GPACON[3:0] = 0x10$$

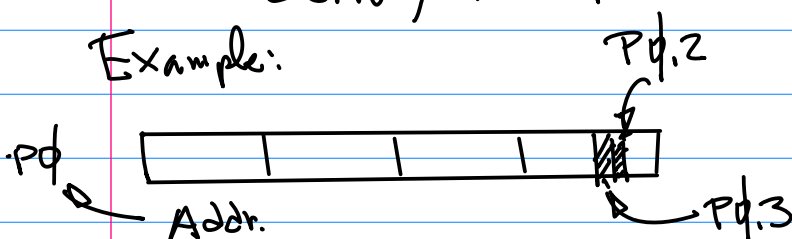
Sept. 12 (Monday)

1. Homework (2pts)
2. Special Purpose Register

Note: Target platform.

LC17ba, LC11C24

Example:



CMPE240  
Sept. 12 (Monday)

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Homework Due A week from this Wed.  
Sample Code: On Github.

"Legs" Layout

Note: Connectivity Table

CPU Pin	J2	Note
GPP/PD.2	D-?	Output $V_{CC} = I \cdot R$
GPP/PD.3	D-?	Input
GND	D-?	GND.

Prototyping Board Option 1.

1769

CMOS

$V_{CC} = I \cdot R + V_{LED}$  ... (1)

$I \approx 10 \text{ mA}$

Materials:

- ① LPC 1769 OR 1724
- ② Resistors.  $250 \Omega \sim 1 \text{ k}\Omega$
- ③ LED.
- ④

A. Output { "1" ON  
"0" OFF

B. Input Testing { "1"  
"0"

SW

3.3V

$\approx 1.2 \text{ V}_{CC} \sim \text{GPP} \dots \text{zip}$

10, 17

3.3V

?

1769

GPPx "1"

GPPy

R

LED

GND

1769

SW

B

1769 patch.zip

Sept. 14 (Wed).

Note: 1° Check Homework Assignment on CANVAS.

Two Options | Prototype Board  
| e-Bay, Board B.

Topics today: IDE

- 1° GPP Software/Program
- 2° 2D Graphics Processing Engine Design.

Example: Set up the Expresso. 2

Key points:

- 1° Make sure Select Target Board LPC1769. (Ref. on github, 35 slides)

2° C/C++ Project Settings. →

"Semi-host"

3° Import LPC1769 patch.

1769 patch.zip

Note: 1° LPC1769 patch is already Config by NXP.

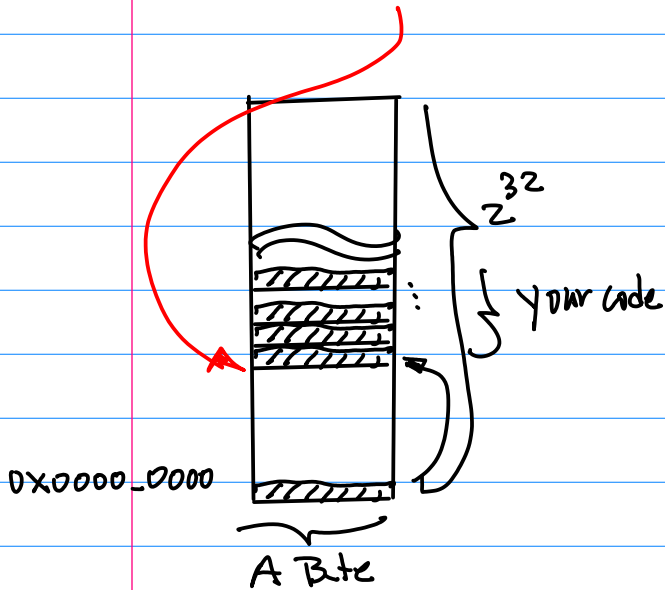
2° prob issue → fix:

Reconnect OR Reboot.

Import GPIO project to your MCU Expresso.

Run "Debug", Once prob is detected then  
Your Program (Binary)

Note: To Uniquely Define A Line,  
we can add a directional  
vector, Denoted as



Definition 1:

$$\vec{d}(x, y) \triangleq \vec{P}_{\text{end}}(x_{\text{end}}, y_{\text{end}}) - \vec{P}_i(x_i, y_i) \quad \dots (1)$$

Ending pt.                      Starting pt.

Definition 2: (Line Eqn. in Vector Form)

$$\vec{P}(x, y) = \vec{P}_i(x_i, y_i) + \lambda \vec{d}(x, y) \quad \dots (2)$$

$$-\infty < \lambda < +\infty$$

Consider G.E. Design.

Math. Formulation, for Vector Graphics.

Example.

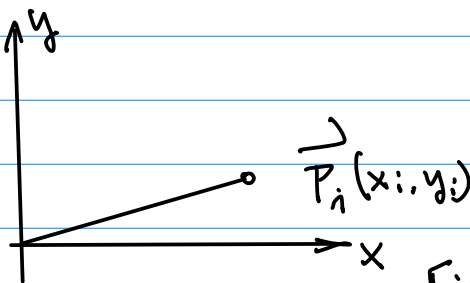


Fig. 1.

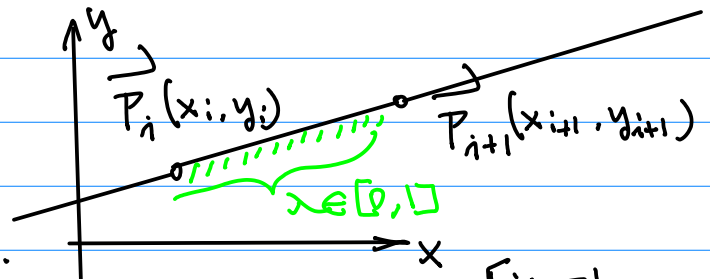


Fig. 2b.

A Point  $\rightarrow \vec{P}(x, y) \rightarrow \vec{P}_i(x_i, y_i)$   
Also, a line  
for  $i = 0, 1, 2, \dots$   
 $\downarrow$   
 $(x_i, y_i)$

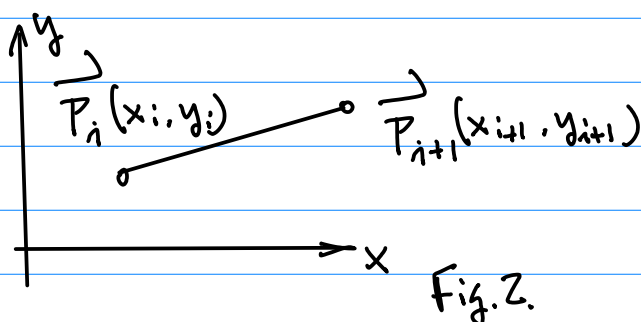


Fig. 2.

Observation 1:

When  $\lambda = 0$ , Eqn (2) gives the  
Starting pt.  $\vec{P}_i(x_i, y_i)$

$\lambda = 1$ ,  $\dots$  Ending point  
 $\vec{P}_{i+1}(x_{i+1}, y_{i+1})$

$0 < \lambda < 1$ ,  $\vec{P}(x, y)$  Any Arbitrary  
pt Between  $\vec{P}_i(x_i, y_i)$   
and  $\vec{P}_{i+1}(x_{i+1}, y_{i+1})$

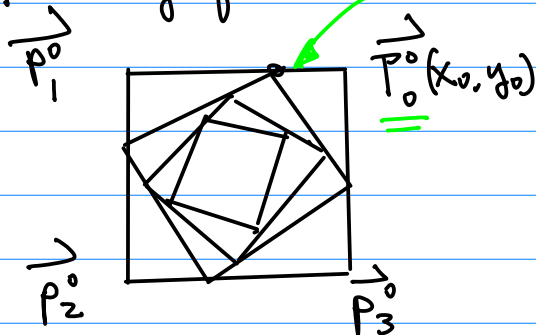
Sept. 21. Today's Topics:

1. Check Canvas for the Submission of the In-Class Exercise;
2. Graphics Lib has been ported to LPC1114, Ref. Code & PPT will be provided.

Example: Given the equation Below,

$$\vec{P}_i(x, y) = \vec{P}_i(x_i, y_i) + \lambda (\vec{P}_{i+1}(x_{i+1}, y_{i+1}) - \vec{P}_i(x_i, y_i)) \dots (1)$$

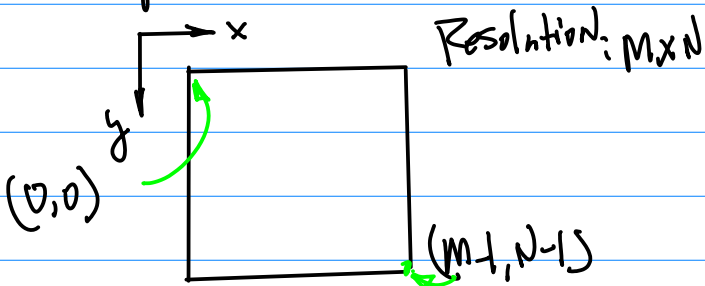
Let's Design A Graphic Algorithm to Create A Set of Counter Clockwise (CCW) Rotating Squares.



Step 1. Define the initial set of Data Points  $\{\vec{P}_i(x_i, y_i) | i=0, 1, \dots, 3\}$

for Resolution of A Display Device defined as  $m \times n$

$\max(m, n) \leq 200$ , Select the Size of the Cube  $\approx 50$



$m \times n$   
No. of Rows  
No. of Pixels per Row (Col).

2021S-105-CMPE240-2021-03-22-Note.pdf

$\vec{P}_0(x_0, y_0), \vec{P}_1(x_1, y_1), \dots, \vec{P}_3(x_3, y_3)$  are defined as the same data pts on P15.

Step 2. Get Line Equation, Based on Eqn (1). First pt on Level 1

$$\begin{aligned} \vec{P}_1(x_1, y_1) &= \vec{P}_0(x_0, y_0) + \lambda (\vec{P}_1(x_1, y_1) - \vec{P}_0(x_0, y_0)) \\ &= (60, 60) + \lambda ((10, 60) - (60, 60)) \\ &= (60, 60) + \lambda (-50, 0) \end{aligned}$$

From the Given Condition, CCW Rotation Let  $\lambda = 0.2$

Note: Based on the Above Calculation Can be conducted for the rest of the pts, And rest of the levels.

Step 3. Suppose we want to generate 10 Levels of the Rotating Squares. Let Write C++ for this purpose.

From Eq(1), we have

$$\begin{cases} x_{i+1}^j = x_i^j + \lambda (x_{i+1}^j - x_i^j) & \dots (2a) \\ y_{i+1}^j = y_i^j + \lambda (y_{i+1}^j - y_i^j) & \dots (2b) \end{cases}$$

$$x[i][j+1] = x[i][j] + \text{lambda} * (x[i+1][j] - x[i][j]);$$

$$y[i][j+1] = y[i][j] + \text{lambda} * (y[i+1][j] - y[i][j]);$$

Consider Creating A Screen Saver  
By Generating A tree.

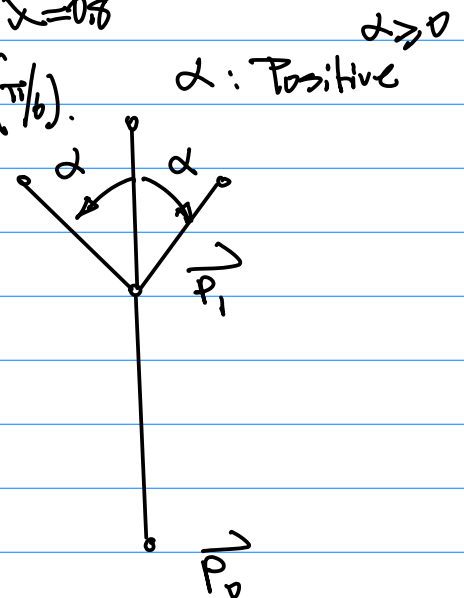
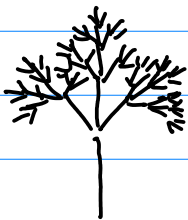
Note: Level 0: Tree Trunk; Same Direction, Directional Vekt. Same.

Level 1: Branch (Main): Mag. Reduction By 20%  $\lambda = 0.8$

Side Branch: L (CCW), Rotation by  $\alpha$  ( $\pi/6$ ).  
R (CW),  $\alpha < 0$

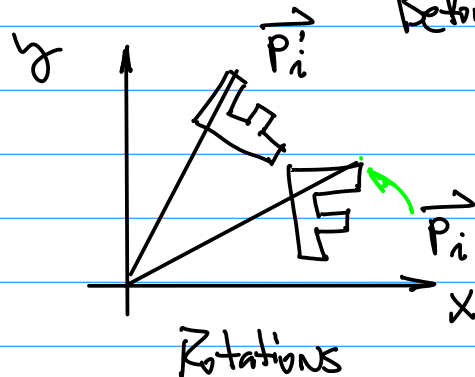
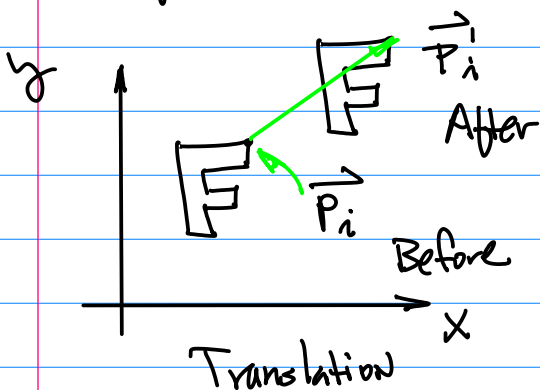
Level 2, 3, ..., k.

Repeat Level 1 with reference vector (pt)  
Updated Accordingly.



Background (2D Transformations)

Note: Mapping Between  $\vec{P}_i$  &  $\vec{P}_i'$  e.g.  
Before & After.



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Rotation: 1° Positive Angle is defined as a Counter Clockwise Rotation;

2° Reference pt is defined as the Origin.

3° physical Display (Coordinate System) v.s. Virtual Display (virtual Coordinate System).

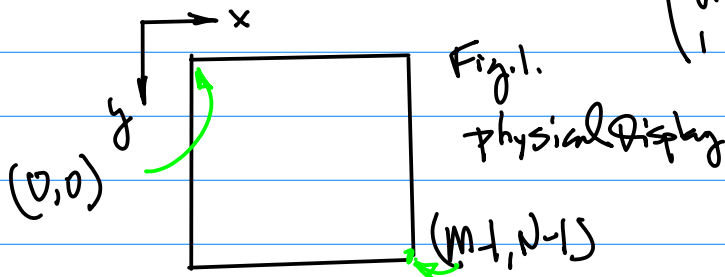
$$P^t(\text{After}) \quad P^t(\text{Before}) \vec{P}_i$$

$$\begin{pmatrix} x'_i \\ y'_i \\ 1 \end{pmatrix} = \begin{pmatrix} a_{11} & a_{12} & a_{13} \\ a_{21} & \dots & a_{23} \\ a_{31} & \dots & a_{33} \end{pmatrix}_{3 \times 3} \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \dots (1b)$$

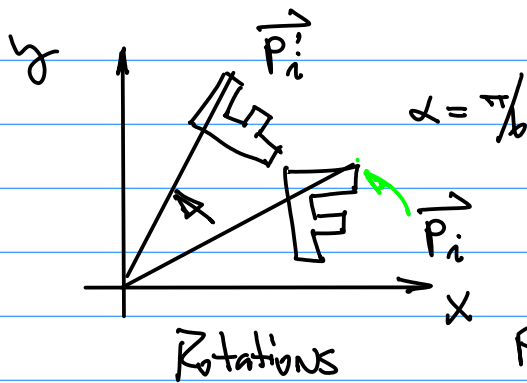
3D Vector, With One "Dummy" Dimension.

Rotation Matrix for Eqn (1b)

$$\begin{pmatrix} x'_i \\ y'_i \\ 1 \end{pmatrix} = \begin{pmatrix} \cos \alpha & -\sin \alpha & 0 \\ \sin \alpha & \cos \alpha & 0 \\ 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \dots (2)$$



$$\begin{cases} x'_i = x_i \cos \alpha - y_i \sin \alpha \dots (2-b) \\ y'_i = x_i \sin \alpha + y_i \cos \alpha \dots (2-c) \end{cases}$$



$$X\_Prim[i] = X[i] * \cos(\alpha) - y[i] * \sin(\alpha)$$

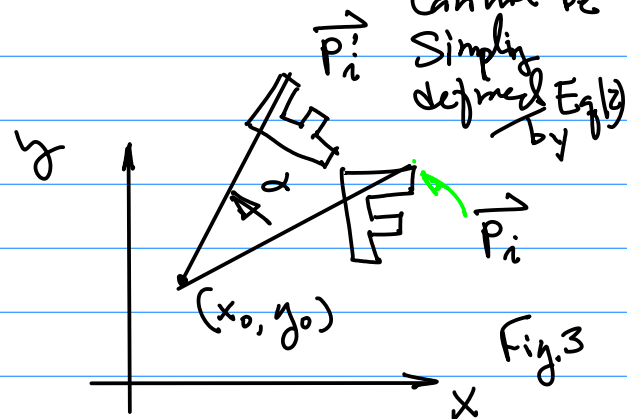
for the Reference of Doing C/C++ Coding.

Note: Reference pt. for the Definition of Rotation. This Rotation Can not be Simply defined by Eq (2)

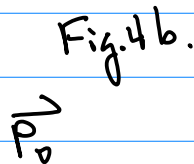
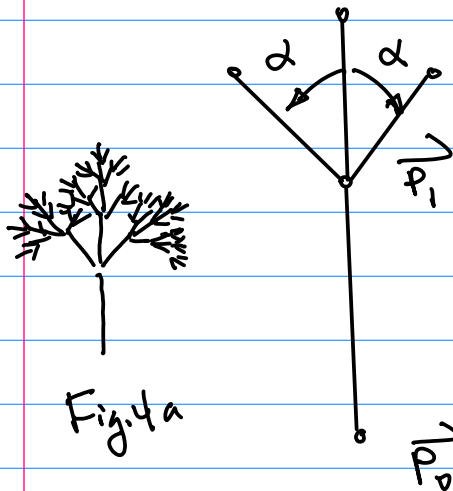
Example: for the Rotation illustrated in Fig. 2.

$$P^t(\text{After}) \quad P^t(\text{Before}) \vec{P}_i$$

$$\begin{pmatrix} x'_i \\ y'_i \\ 1 \end{pmatrix} \stackrel{?}{=} \phi \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \dots (1)$$







These are different Rotations than that in Eqn (2).

Consider the Composition of 2D Transforms.

Example: Build/Design to Realize a 2D Tree Pattern in Fig. 4.

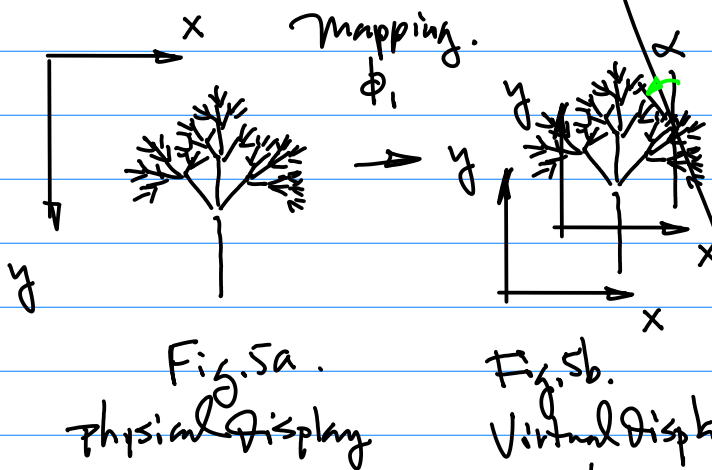


Fig. 5c. Rotation By  $\alpha$ . CCW.

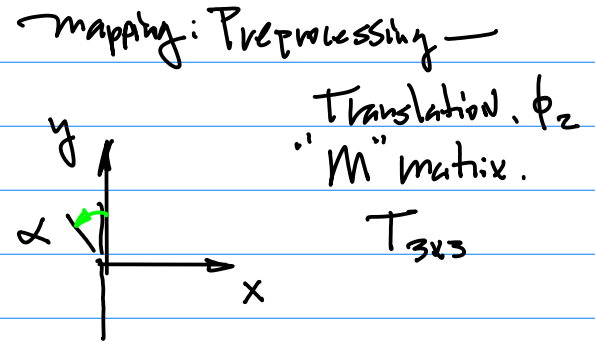
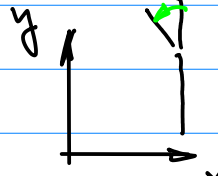
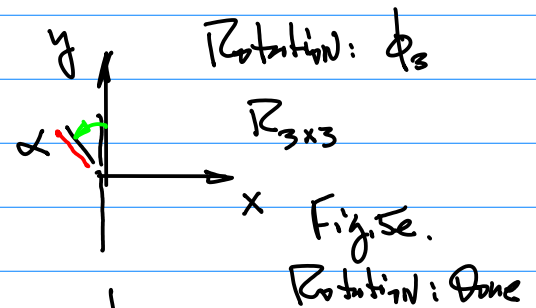
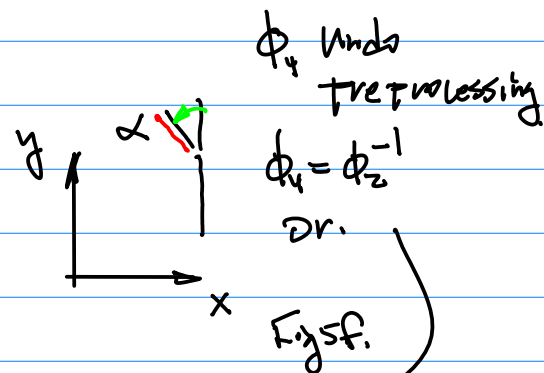


Fig. 5d. Preprocessing By Translation.

Rotation Eqn. (2).



Post Processing. To "move Back" to its original Position.



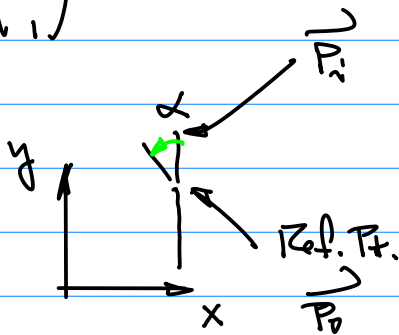
$$\phi_4 = \phi_2^{-1} = T_{3 \times 3}^{-1} \dots (3)$$



Based on Step by Step Analysis.  
(Analyze "Before" and "After"  
Relationship).

Start at given  
pt. to be Rotated

$$\vec{P}_i = \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \quad \dots (4)$$



Next, Preprocessing  $\phi_2$ ,  $T_{3 \times 3}$   
To make  $P_0(x_0, y_0)$  to overlap  
with the origin  $(0,0)$ .

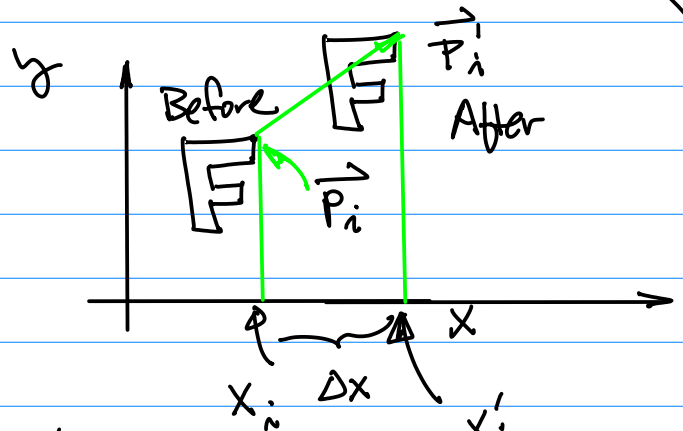
$$T_{3 \times 3} = \begin{pmatrix} a_{11} & a_{12} & a_{13} \\ a_{21} & a_{22} & a_{23} \\ 0 & 0 & 1 \end{pmatrix} \quad \dots (5)$$

$$\vec{P}'_i \text{ After} = T_{3 \times 3} \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \text{ Before} \quad \dots (6)$$

$$= \begin{pmatrix} a_{11} & a_{12} & a_{13} \\ a_{21} & a_{22} & a_{23} \\ 0 & 0 & 1 \end{pmatrix}$$

$$x'_i = a_{11}x_i + a_{12}y_i + a_{13} \quad \dots (6a)$$

Where  $a_{13} = \Delta x$



Hence,  $a_{12} = 0$ ,  $a_{11} = 1$ , then

So  $x'_i = x_i + 0 + \Delta x = x_i + \Delta x \quad \dots (6c)$

$$y'_i = y_i + \Delta y \quad \dots (6d)$$

Therefore

$$T_{3 \times 3} = \begin{pmatrix} 1 & 0 & \Delta x \\ 0 & 1 & \Delta y \\ 0 & 0 & 1 \end{pmatrix} \quad \dots (7)$$

Note:  $\Delta x = -(x_i - x_0)$ ,  
 $\Delta y = -(y_i - y_0)$ .

in Our Fig 5. Series.

Now, Rotation,  $R_{3 \times 3}$ .

Then, Post Processing.  $\phi_4$

$$M_{\phi_3} = T_{3 \times 3}^{-1} = \begin{pmatrix} 1 & 0 & -\Delta x \\ 0 & 1 & -\Delta y \\ 0 & 0 & 1 \end{pmatrix} \quad \dots (8)$$

Put All these together, we have

$$\begin{pmatrix} x''_i \\ y''_i \\ 1 \end{pmatrix} = T_{3 \times 3}^{-1} R_{3 \times 3} T_{3 \times 3} \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix} \dots (9)$$

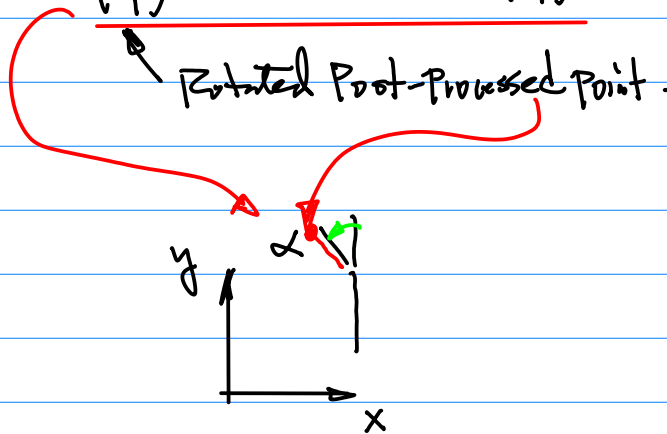


Fig. 7

Sept. 28 Wed.

Note: Conclusion on 2D Transforms  
is given Composition of  
in the Above Eq. (9), Coding/Implementation  
in C/C++.

$$\begin{pmatrix} x''_i \\ y''_i \\ 1 \end{pmatrix} = \overbrace{T_{3 \times 3}^{-1} R_{3 \times 3} T_{3 \times 3}}^{T_{\Sigma}} \begin{pmatrix} x_i \\ y_i \\ 1 \end{pmatrix}$$

$T_{\Sigma}$  is  $3 \times 3$  matrix

↓  
Last Row of  $T_{\Sigma}$  ( $3 \times 3$ ) is  
(0, 0, 1) No Need for  
Coding.

↓  
Only the top 2 Rows from  
the matrix matter.

Step by step Derivation is also  
provided at the Lecture Notes.

$$x_L[i] = \cos \alpha * x[i] - \sin \alpha * y[i] \\ + \Delta x \cos \alpha - \Delta y \sin \alpha - \Delta x$$

Similarly for  $y_L$ ,

$$y_L[i] = \sin \alpha * x[i] + \cos \alpha * y[i] \\ + \Delta x \sin \alpha + \Delta y \cos \alpha - \Delta y$$

Note: Work on  
Virtual to physical  
mapping

Notation is introduced to  
Keep track if the new point  
is generated By CCW or CW

→ Lead

→ Enumeration of the  
Points.

→  $P_{i,l}$  or  $P_{i,r}$

(l - left - CCW,  
r - Right - CW)

1st project, 2D Graphics Engine

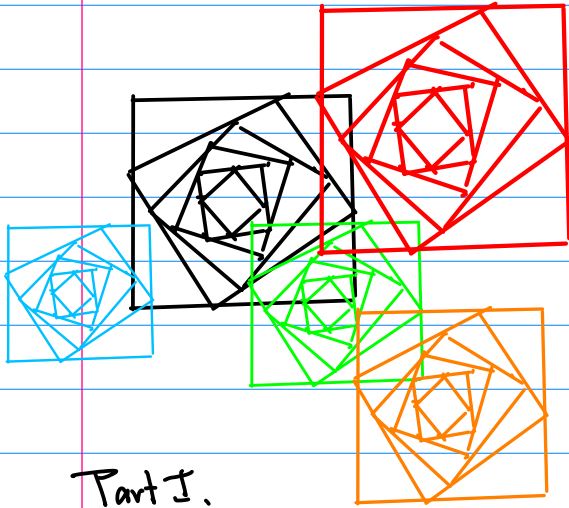
2D Graphics Screen Saver  
a collection of Rotating  
Squares.

Impe 240

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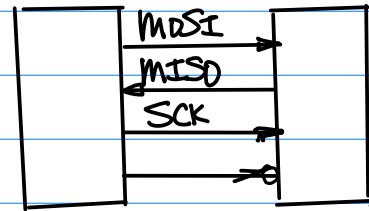
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Interface Between the LPC1769 and LCD is By S.P.I (Serial Peripheral Interface)



Part I.

Fig.1  
(Level 10 or higher)



LPC1769  
CPU

LCD

Part II. Trees. Optional for Artistic Presentation.

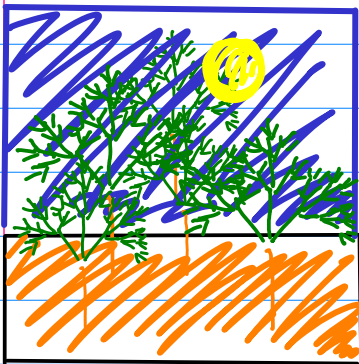


Fig.2

"3+1" pins for S.P.I.

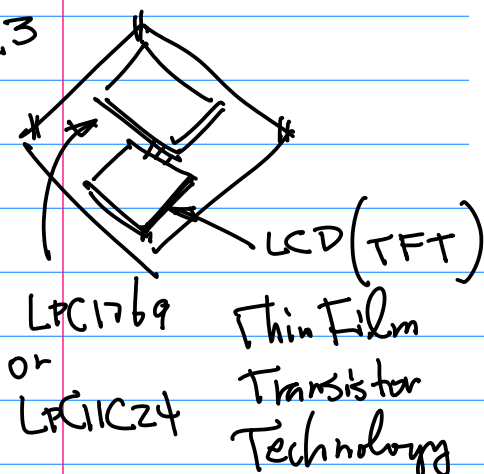
1. MOSI Output
2. MISO Input
3. SCK (Clock) Output
4. SS (CS) Output

Now, consider LPC1769, CPU Datasheet  
7p431. CR0

Consider the Graphics Engine Design from System View.

NXP Semiconductors

Fig.3



a. SSP  
b. CR0, CR1

UM10360

Chapter 18: LPC176x/5x SSP0/1

Table 371: SSPn Control Register (SSP0CR0 - address 0x4008 8000, SSP1CR0 - 0x4003 0000) bit description

Bit	Symbol	Value	Description	Reset Value
3:0	DSS		Data Size Select. This field controls the number of bits transferred in each frame. Values 0000-0010 are not supported and should not be used.	0000
		0011	4-bit transfer	

(SSP0CR0)

CR0



Addr. 0x4008-0000

