

CMPE 240

LCD Hardware Interface and SPI Signals

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CPU Architecture, Memory Map and 2D Graphics Processing Engine Introduction with SPI Interface

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CPU Architecture and Memory Map

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## IDE Xpresso and Import GPIO Project

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## Schematics and GPIO Design

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## Bill of Material and Schematics

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Introduction and Bill of Materials for prototype board, GPIO and SPI LCD display.

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First day of class

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