

CMPE 240

Special Purpose registers PCONP, PINSEL, CR0, f_{spi} formula and drawline program

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Trees, SPI Special Purpose Registers (CR0 and CR1)

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2D Transforms and Trees

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Squares and 2D transforms, Project Assignment

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SPI LCD interface and 2D vector graphics

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LCD Interface and 2D Vector Graphics

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LCD Hardware Interface and SPI Signals

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CPU Architecture, Memory Map and 2D Graphics Processing Engine Introduction with SPI Interface

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CPU Architecture and Memory Map

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IDE Xpresso and Import GPIO Project

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Schematics and GPIO Design

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Bill of Material and Schematics

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Introduction and Bill of Materials for prototype board, GPIO and SPI LCD display.

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First day of class

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