



# 190q-6b-SZ-485-Motor-Control-HL-YZ- 2023-3-14.odp

CTI One Corporation  
This document is created by:

Harry Li, Ph.D.

Youran Zheng

Yusuke Yakuwa

March 14, 2023

Jan30(Tue).

Note1.

A. Test 485 software for the motor drive on the windows Machine:

A1. Get the software on the Windows Machine

A2. Have the motor cable connection to the motor drive

B. Test Python Code for the motor drive on the Jetson NANO

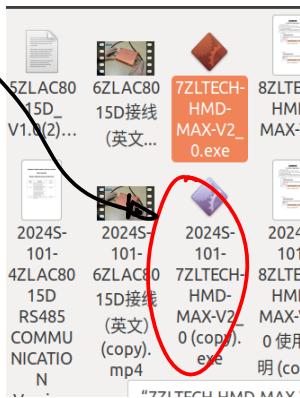
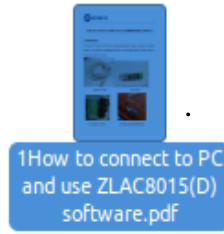
B1. Have the J41 on Jetson NANO cable ready to connect to the prototype board

B2. Add the level shifter for GPIO and PWM output

B3. have UART output from J41 connected to the motor controller to establish 485 communication

2)

Software: On the github.  
2024-101-1 ~



Hardware: PC windows.

- ① Cable USB.
- ② USB to 485 Converter

③ Controller Box

④ Motor (BLDC)

⑤ PWR unit.

# The Hardware for Task A. ↴

3/

ZLTECH) → ZLAC8015(D) CAN&RS485 port ( PIN 2, 4 correspond RS485 A, B signal, PIN 1, 3 correspond RS485 A, B signal)



USB to LPT cable



485 module

(1) Cable

(2) USB-485  
Converter.

(4)  
BLDC  
motor



⑤ PWR unit. Conversion  
from 110 VAC to 3bV 24V DC

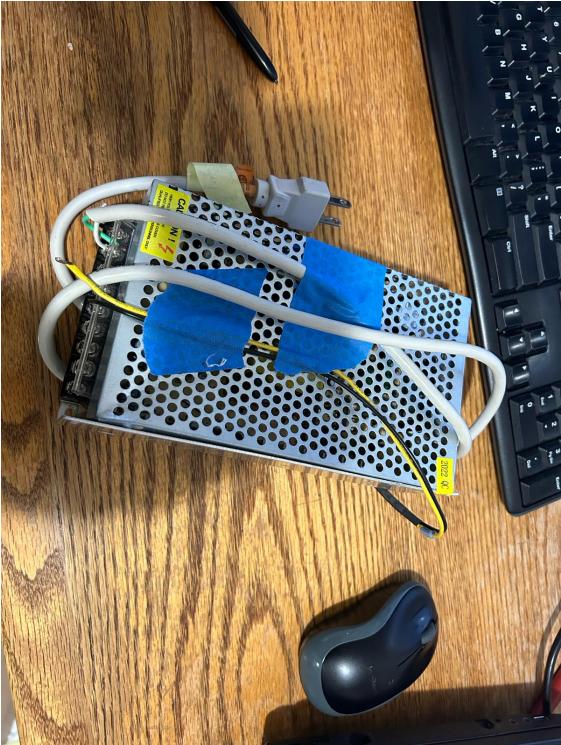
Need 110 to 3bV DC

Note: Use either

Li-BAT (3bVDC)

OR order the

right 110 ACV - 3bV DC.



# Ref SZ Motor Controllers with 8 Documents (6/28-4/21, 2023)

CANopen standardizes communication between devices and applications from different manufacturers. It has been used in automation and motion applications

## The Basics of CANopen Updated Mar 31, 2023

<https://www.ni.com/en-us/shop/seamlessly-connect-to-third-party-devices-and-supervisory-system/the-basics-of-canopen.html>



- 1How to connect to PC and use ZLAC8015(D) software.pdf
- 2ZLAC8015 CANopen Communication Routine V1.05.pdf
- 3ZLAC8015 MANUAL V2.00.pdf
- 4ZLAC8015 RS485 COMMUNICATION Version 1.03.pdf
- 5ZLAC8015\_V1.0(2).eds
- 6ZLAC8015D\_接线 (英文) .mp4
- 7ZLTECH-HMD-MAX-V2\_0.exe
- 8ZLTECH-HMD-MAX-V2\_0 使用说明 .pdf

/media/harry/easystore3/backup-2020-2-15/CTI/3projects/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/190q-6b-hub-motors-controllers/sz-motor-and-controller/ZLAC8015D\$

1. CAN and/or 485 interface for the motor controller



2. HL: CAN protocol over 485 hardware

From chatGPT: the CAN (Controller Area Network) protocol can be implemented on hardware that uses the RS-485 physical layer. While CAN and RS-485 are different protocols, they can be used together by incorporating a **CAN transceiver** that supports RS-485 signaling.

The RS-485 standard defines the electrical characteristics and signaling for communication over a differential pair of wires. On the other hand, CAN is a higher-level protocol that provides a robust and efficient communication method for devices on a network.

To implement CAN on RS-485 hardware, you would need a **CAN controller** and a **CAN transceiver that supports RS-485 signaling**. The CAN controller manages the CAN protocol and handles tasks such as message framing, error checking, and arbitration. The CAN transceiver ...

To use CAN over RS-485, you would need a transceiver that supports both protocols. This transceiver would convert the differential CAN signals to the RS-485 electrical signaling and vice versa. Additionally, you would require appropriate software or firmware to implement the CAN protocol on top of the RS-485 physical layer.

It's worth noting that while CAN and RS-485 share some similarities in their electrical signaling, they are different protocols with different features and capabilities. Adapting CAN to work over RS-485 may require additional considerations and modifications to ensure proper functionality and compatibility.

# YK Motor Testing with SZ Motor Controller (1/22, 2024)

Notes: HL 2024-1-22

Jan 22nd Monday

y

1. Motor Add. Angular Displacement  
Sensor, Optical Encoder.

Two Wires (Differential Signal)

$$\frac{360}{1024} \approx 0.36 \text{ Degrees. ... (1)}$$

1024 Measurement for 36° circle

Note: Stepper Motor

$$\frac{360}{1024 \text{ Steps}} = 1.8^\circ/\text{Step} \quad \dots (2)$$

0.9 Degree/Half Step ... (3)

0.45 Degree/1/4 Step ... (4)

0.225 Degree/1/8 Step ... (5)

Equation (1) with Optic Encoder is  
Comparable with 1/8 Step.

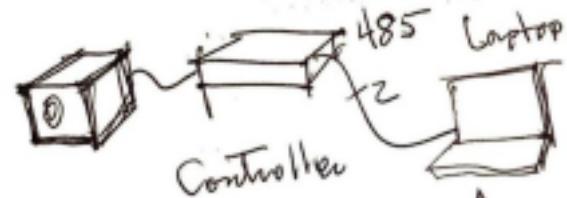
2. Wire Configuration of the Motor  
With Optic Encoder.

{  
 5 Wires - Hall Sensor  
 3 Wires - motor U, V, W  
 2 Wires - Optical Encoder  
 2 Wires - Thermal Sensor (Temperature  
 Sensor)  
 12 Wires.

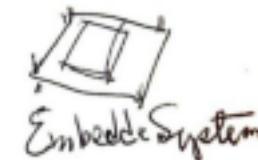
3. Torque Compensation Using  
SZ motor Company's Controller

Torque/Friction  $\rightarrow$  Torque/Curent  
Increases  $\uparrow$  Increases By  
motor Controller  $\uparrow$

bit Rate of the  
testing over  
485: 400b  
Speed Kept  
Constant.



Notes Python Code  
Based Testing.



# Python Code Testing of SZ Motor Controller (10/8-7/27,2023)

To prepare the python program test, make sure (1) to have the python code ready, from here:

```
(pymodbus) harry@harrys-gpu-laptop:~/PycharmProjects/w100/485-controller$ tree -L 1
.
└── 190q-6a-4-#485pymodbus39-2023-7-13.yml
    ├── main.py
    └── modbus_test.py
└── pymodbus.yml
```

(2) locate the pymodbus.yml file then create anaconda environment, using the following command:

```
$ conda env create -f pymodbus.yml
# Activate the Anaconda environment:
$ conda activate pymodbus
```

Note 2023-10-8 for installation of anaconda environment, see readme document by YZ, 190q-6a-1-README-pymodbus-YZ-2023-7-11 (1).txt

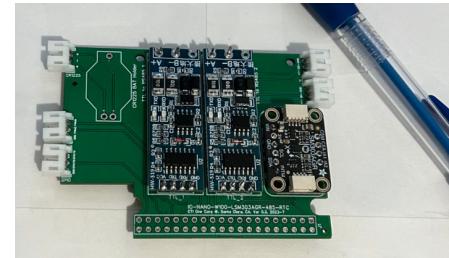
(3) once the pymodbus environment is created, then run the python testing code. If the GPIO port is connect connected, then you will see port not connected error message.

(4) both motor uses J41-8/10 with SZ controller, UART to 485 board;

|               |     |        |           |
|---------------|-----|--------|-----------|
| UART1(left)_1 | VCC | J41-1  | 3V3       |
| UART1(left)_2 | TXD | J41-10 | UART_2_RX |
| UART1(left)_3 | RXD | J41-8  | UART_2_TX |
| UART1(left)_4 | GND | J41-8  | gnd       |
| UART2(left)_1 | VCC | J41-1  | 3V3       |
| UART2(left)_2 | TXD | J50_RX | J50-3     |
| UART2(left)_3 | RXD | J50_TX | J50-4     |
| UART2(left)_4 | GND | J41-8  | gnd       |

Connect Tx/Rx to SZ controller to test motor control, perform loopback test (optional) if needed. Run minicom to set up the UART communication parameters by HL. Putty testing and Python code testing by Youran Zheng

Note 2023-10-8: SZ motor drive just need one UART.

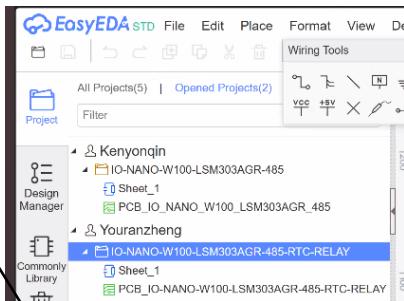
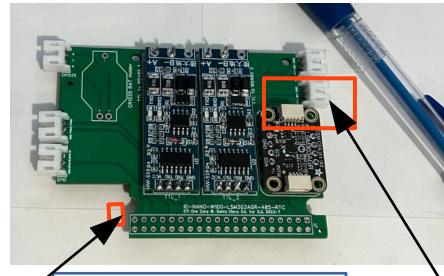


# UART/485 Connection To Motor Controller (10/8-7/27,2023)

Continued from the previous slides

1. The CTI One's IO board UART to 485 conversion board:  
input connectors (JST 3 pins connector) from NANO J41-8/J41-10  
and GND;

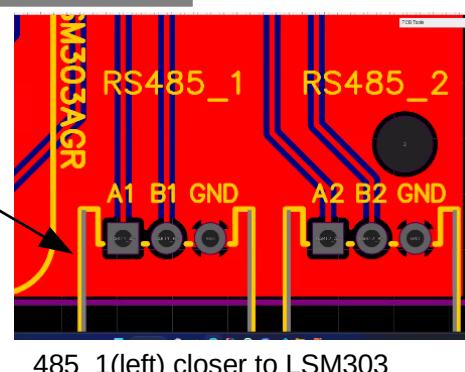
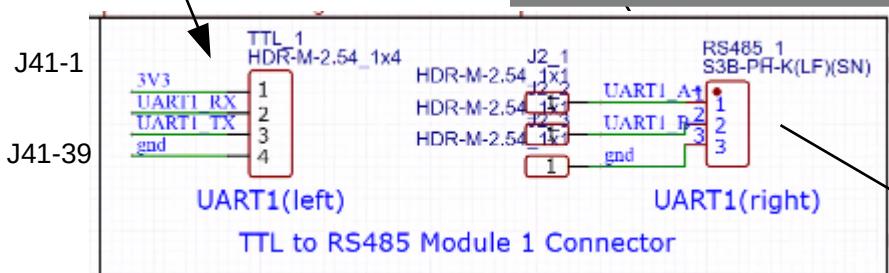
Note 2023-10-8: the pin connectivity is given in the Schematic from EasyEDA below



J41-8 Rx, J41-10 Tx

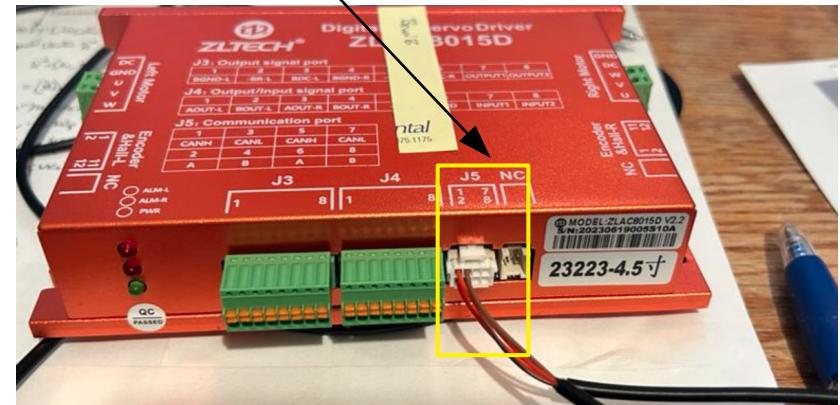
Note: connector for mounting 485 board

Note 2023-10-8: To save time for soldering location identification, screen print pin1, 8,10,39



2. 485 output from CTI One's IO board as the input to the SZ motor controller

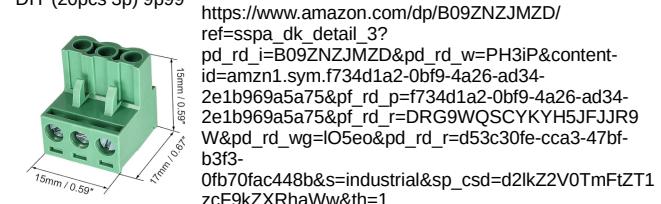
J5-1 A Orange (red)  
J5-7 B Brown



20 Sets Mini Micro Jst 2.0 Ph 3 Pin Connector Plug Male With 150mm Cable & Female by daier, 8p99

[https://www.amazon.com/Micro-Connector-150mm-Cable-Female/dp/B01DUC1PW6/ref=sr\\_1\\_4?crid=26S9FU1J62CBL&keywords=jst+connector+3+pin+male&qid=1696809717&sprefix=jst+connector+3+pin+male%2Caps%2C125&sr=8-4](https://www.amazon.com/Micro-Connector-150mm-Cable-Female/dp/B01DUC1PW6/ref=sr_1_4?crid=26S9FU1J62CBL&keywords=jst+connector+3+pin+male&qid=1696809717&sprefix=jst+connector+3+pin+male%2Caps%2C125&sr=8-4)

Cermant 20 pcs 3 Pin 5.08mm Pitch Pluggable PCB Angle Screw Terminal Block Plug Connector Socket Panel Mount DIY (20pcs 3p) 9p99





# Testing SZ Motor Controller (9/29-7/27,2023)

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(3) once the pymodbus environment is created, then run the python testing code.



# Update on J50 UART Port (7/27,2023)

The screenshot shows three windows side-by-side. The left window displays a table of pins from 1 to 40 with their corresponding functions: 3V, I2C, GND, and various UART, SPI, PWM, and unused pins. The middle window is a terminal-like interface titled 'Select desired functions (for pins)' showing a list of pins with their current function and a checkbox to change it. Pin 10 is selected as 'DEFAULT JetsonIO'. The right window is a text editor titled 'extlinux.conf' containing a configuration file for the boot loader. It includes sections for menu titles, kernel labels, and append parameters for the primary kernel, including memory settings and rootfs types.

In the W100 product, the 2 pwm ports on the J41 Connector will be used.

```
$ sudo /opt/nvidia/jetson-io/jetson-io.py
```

Run the command above can configure J41 settings.

However, once the Jetson Nano save the new J41 connector configuration and reboot, the file: /boot/extlinux/extlinux.conf will be added a new LABEL and which will be selected as the default LABEL.

```
Open ▾ Save ▾ extlinux.conf /boot/extlinux
1 TIMEOUT 30
2 DEFAULT JetsonIO
3
4 MENU TITLE L4T boot options
5
6 LABEL primary
7   MENU LABEL primary kernel
8   LINUX /boot/Image
9   INITRD /boot/initrd
10  APPEND ${cbootargs} quiet root=/dev/mmcblk0p1 rw rootwait rootfstype=ext4 console=tty0 fbcon=map:0 net.ifnames=0
11  APPEND tegraid=21.1.2.0.0 ddr_die=4096M@2048M section=512M memtype=0 vpr_resize usb_port_owner_info=0 lane_owner_info=0
  emc_max_dvfs=0 touch_id=0@63 video=tegrafb no_console_suspend=1 debug_uartport=lsport_4 earlyprintk=uart8250-32bit,0x70006000 maxcpus=4
  usbcore.old_scheme_first=1 lp0_vec=0x1000@0xffff80000 core_edp_mv=1125 core_edp_ma=4000 gpt earlycon=uart8250,mmio32,0x70006000 root=/
  dev/mmcblk0p1 rw rootwait rootfstype=ext4 console=tty0 fbcon=map:0 net.ifnames=0 quiet root=/dev/mmcblk0p1 rw rootwait rootfstype=ext4
  console=tty0 fbcon=map:0 net.ifnames=0
12
13 # When testing a custom kernel, it is recommended that you create a backup of
14 # the original kernel and add a new entry to this file so that the device can
15 # fallback to the original kernel. To do this:
16 #
17 # 1, Make a backup of the original kernel
18 #   sudo cp /boot/Image /boot/Image.backup
19 #
20 # 2, Copy your custom kernel into /boot/Image
21 #
22 # 3, Uncomment below menu setting lines for the original kernel
23 #
24 # 4, Reboot
25
26 LABEL backup
27 #   MENU LABEL backup kernel
28 #   LINUX /boot/Image.backup
29 #   INITRD /boot/initrd
30 #   APPEND ${cbootargs}
31
32 LABEL JetsonIO
33   MENU LABEL Custom 40-pin Header Config
34   LINUX /boot/Image
35   FDT /boot/kernel_tegra210-p3448-0000-p3449-0000-b00-user-custom.dtb
36   INITRD /boot/initrd
37   APPEND tegraid=21.1.2.0.0 ddr_die=4096M@2048M section=512M memtype=0 vpr_resize usb_port_owner_info=0 lane_owner_info=0
  emc_max_dvfs=0 touch_id=0@63 video=tegrafb no_console_suspend=1 debug_uartport=lsport_4 earlyprintk=uart8250-32bit,0x70006000 maxcpus=4
  usbcore.old_scheme_first=1 lp0_vec=0x1000@0xffff80000 core_edp_mv=1125 core_edp_ma=4000 gpt earlycon=uart8250,mmio32,0x70006000 root=/
  dev/mmcblk0p1 rw rootwait rootfstype=ext4 console=tty0 fbcon=map:0 net.ifnames=0
```

The J50 UART port (/dev/ttys0) will belong to tty group again. The debug serial console will reopen. The J50 UART port CANNOT be freely used.

# Ref SZ Motor Controller with 8 Documents (6/28-4/21, 2023)

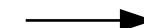
CANopen standardizes communication between devices and applications from different manufacturers. It has been used in automation and motion applications

## The Basics of CANopen Updated Mar 31, 2023

<https://www.ni.com/en-us/shop/seamlessly-connect-to-third-party-devices-and-supervisory-system/the-basics-of-canopen.html>



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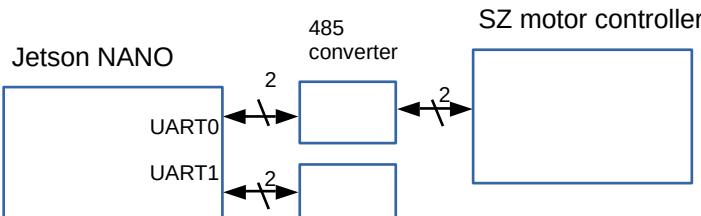
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- 1How to connect to PC and use ZLAC8015(D) software.pdf
- 2ZLAC8015 CANopen Communication Routine V1.05.pdf
- 3ZLAC8015 MANUAL V2.00.pdf
- 4ZLAC8015 RS485 COMMUNICATION Version 1.03.pdf
- 5ZLAC8015\_V1.0(2).eds
- 6ZLAC8015\_接线 (英文) .mp4
- 7ZLTECH-HMD-MAX-V2\_0.exe
- 8ZLTECH-HMD-MAX-V2\_0 使用说明 .pdf

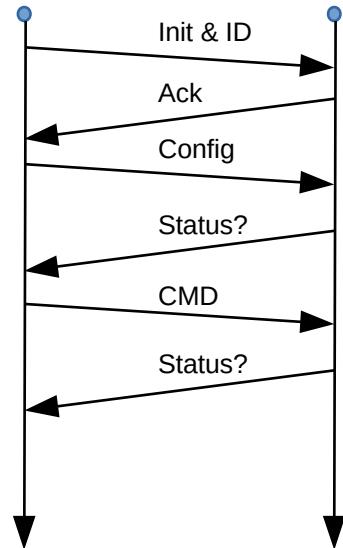
harry@workstation:/media/harry/easystore1/backup-2020-2-15/CTI/  
3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-  
robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-  
motor-and-controller/ZLAC8015D\$

# RS485 for SZ Motor Controller (6/30,2023)



485 host  
(Jetson NANO)

485 client  
(motor controller)



1. (a) Perform UART loopback test; (b) Use USB-485 module on Jetson NANO and laptop, connect them then send "hello" from NANO to laptop, then laptop receives the message by sending "ACK" and time stamp.

2. Run 485 motor controller code on Jetson NANO.

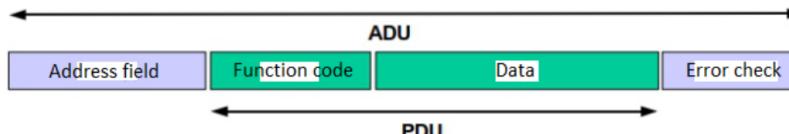
3. Run 485 motor controller code on Jetson NANO with SZ motor controller connected for testing motor drive

4ZLAC8015D RS485 COMMUNICATION Version 1.03.pdf



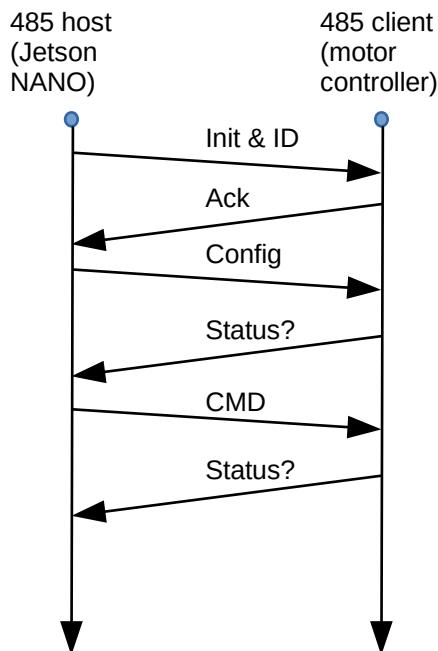
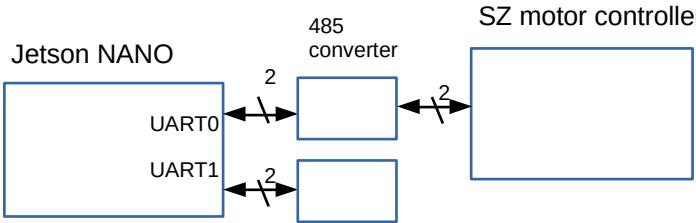
ZLAC8015D SERVO DRIVER (SPECIAL FOR HUB SERVO MOTOR) RS485 COMMUNICATION INSTRUCTION

| Version | Description   | Date      |
|---------|---|-----------|
| V1.00   | First edition   |           |
| V1.01   | 1. Revise some control routine errors;<br>2. Add or delete some addresses | 2021-3-23 |



Document:  
[harry@workstation:/media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/ZLAC8015D](mailto:harry@workstation:/media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/ZLAC8015D)

# Communication Protocol for the RS485 SZ Motor Controller (7/6-6/30,2023)



- Ref: 1. [/media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/source-9ZLAC8015D-RS485-Master-demo\\$ tree -L 2](https://media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/source-9ZLAC8015D-RS485-Master-demo$ tree -L 2)
- 9ZLAC8015D-RS485-Master-demo.rar
  - CHN-version
  - 3\_ZLAC8015D-RS485-Master-demo.rar
  - ZLAC8015D-RS485-Master-demo
  - CORE
  - HARDWARE
  - keilkill.bat
  - OBJ
  - STM32F10x\_FWLib
  - SYSTEM
  - USER
  - USMART
- Ref: 2. [/media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/ZLAC8015D\\$ tree -L 1](https://media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-6-hub-motors/sz-motor-and-controller/ZLAC8015D$ tree -L 1)
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  - 2ZLAC8015D CANopen Communication Routine V1.05.pdf
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  - 4ZLAC8015D RS485 COMMUNICATION Version 1.03.pdf
  - 5ZLAC8015D\_V1.0(2).eds
  - 6ZLAC8015D 接线 (英文) .mp4
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  - 8ZLTECH-HMD-MAX-V2\_0 使用说明 .pdf
  - 9ZLAC8015D-RS485-Master-demo.rar

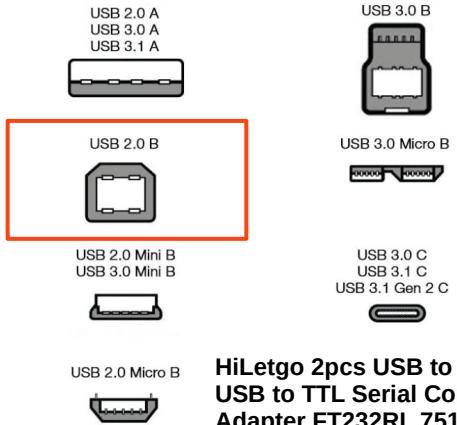
0 directories, 9 files

# USB to RS485 Converter for Testing (7/6,2023)

1. USB-485 Converter with USB-B  
Male connector

→ USB-485 Converter with USB-B  
connector

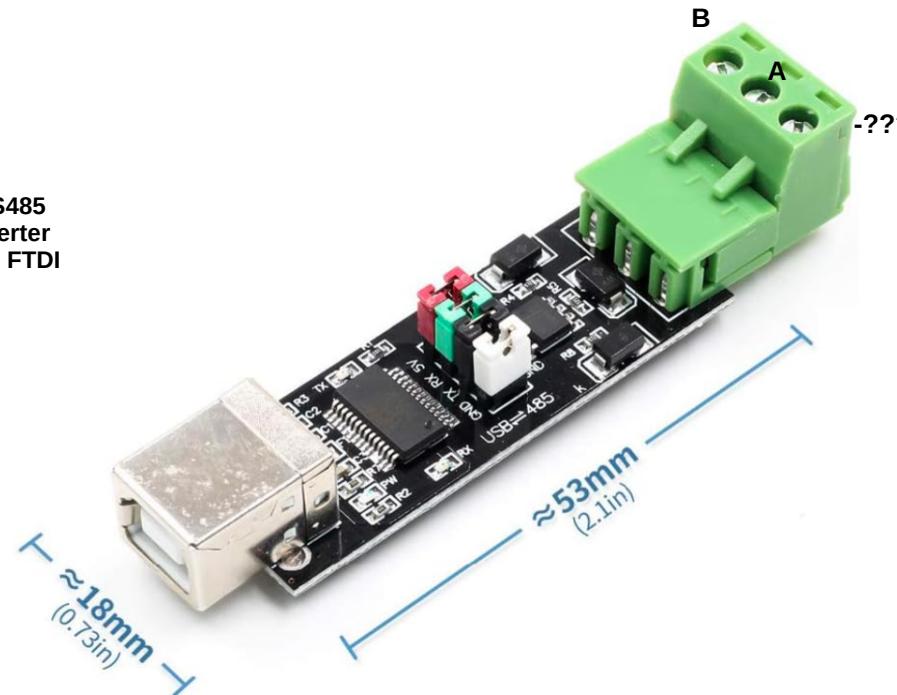
USB Connection Type Reference Chart



rgzhihuifz 3 Pack USB 2.0 AF/BM Adapter



**HiLetgo 2pcs USB to RS485  
USB to TTL Serial Converter  
Adapter FT232RL 75176 FTDI  
Interface**



# Sample Control for the RS485 SZ Motor Controller (7/6-6/30,2023)

Example:

For language translation on line: DeepL, yy

另外请提供一个用于贵公司驱动器的软件源程序样本，实现（1）初始设置；（2）转动左轮5秒，然后转动右轮5秒；（3）然后反向转动左轮5秒，再反向转动右轮5秒钟。

HL: 2023-6-30: Please also provide a sample software source program for your drive that implements (1) the initial setup, (2) turning the left wheel for 5 seconds, then turning the right wheel for 5 seconds, and (3) then reversing the left wheel for 5 seconds, then reversing the right wheel for 5 seconds.

```

01 06 20 0D 00 03 53 C8
01 06 20 0E 00 08 E2 0F
01 10 20 88 00 02 04 00 0A 00 0A C3 AD (set left motor and right motor target speed to 10RPM in positive direction)
+set 5s delay time in keil
01 10 20 88 00 02 04 FF F6 FF F6 72 38(set left motor and right motor target speed to 10RPM in negative direction)
+set 5s delay time in keil

```

01 06 20 0D 00 03 53 C8

Note: the syntax of the command (1)  
address of the register (1 byte); (2) the  
content going into the register (1 byte)

Drive address: 01

Flow: 1<sup>st</sup> command, select function mode  
(enable) >> 2<sup>nd</sup> command, control word to  
enable with E2 0F for CRC (3) write mul  
byte function > 20 88 start address, 00 02  
(2 regs), 04 (no. of bytes to be written) 00  
0A

Function code: 06

Control mode/ velocity profile 20 0D (2 bytes addr)

??? the content: 00 03 (per table 3.1) (content)

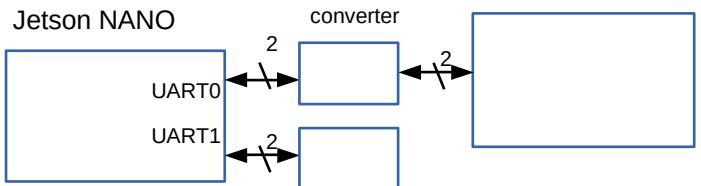
Ref1, pp. 5

16 bit Register  
(address)

High byte

Low byte

# The Instruction Format for RS485 SZ Motor Controller (7/11,2023)



Example:

For language translation on line: DeepL, yy

另外请提供一个用于贵公司驱动器的软件源程序样本，实现（1）初始设置；（2）转动左轮5秒，然后转动右轮5秒；（3）然后反向转动左轮5秒，再反向转动右轮5秒钟。

HL: 2023-6-30: Please also provide a sample software source program for your drive that implements (1) the initial setup, (2) turning the left wheel for 5 seconds, then turning the right wheel for 5 seconds, and (3) then reversing the left wheel for 5 seconds, then reversing the right wheel for 5 seconds.

```
01 06 20 0D 00 03 53 C8
01 06 20 0E 00 08 E2 0F
01 10 20 88 00 02 04 00 0A 00 0A C3 AD (set left motor and right motor target speed to 10RPM in positive direction)
+set 5s delay time in keil
01 10 20 88 00 02 04 FF F6 FF F6 72 38(set left motor and right motor target speed to 10RPM in negative direction)
+set 5s delay time in keil
```

01 06 20 0D 00 03 53 C8

Note: the syntax of the command (1) address of the register (1 byte); (2) the content going into the register (1 byte)

Drive address: 01  
 Function code: 06  
 Control mode/ velocity profile 20 0D (2 bytes addr)  
 ??? the content: 00 03 (per table 3.1) (content)

Ref1, pp. 5

Flow: 1<sup>st</sup> command, select function mode (enable) >> 2<sup>nd</sup> command, control word to enable with E2 0F for CRC (3) write multi byte function > 20 88 start address, 00 02 (2 regs), 04 (no. of bytes to be written) 00 0A

16 bit Register  
(address)

High byte

Low byte

(1) Level 1: Slave/client Address, 1 byte, the top block; (2) Level 2: Function (Command), 1 byte, the level 2 in the block diagram; (3) Level 3: Control registers address, 2 bytes; (4) Level 4: write the control pattern into the control register, 2 bytes (the control register is 16 bits, U16); (5) Level 5 (the end level): CRC code, 2 bytes.

# The Flow Chart for the RS485 SZ Motor Controller (7/18,2023)

Example:

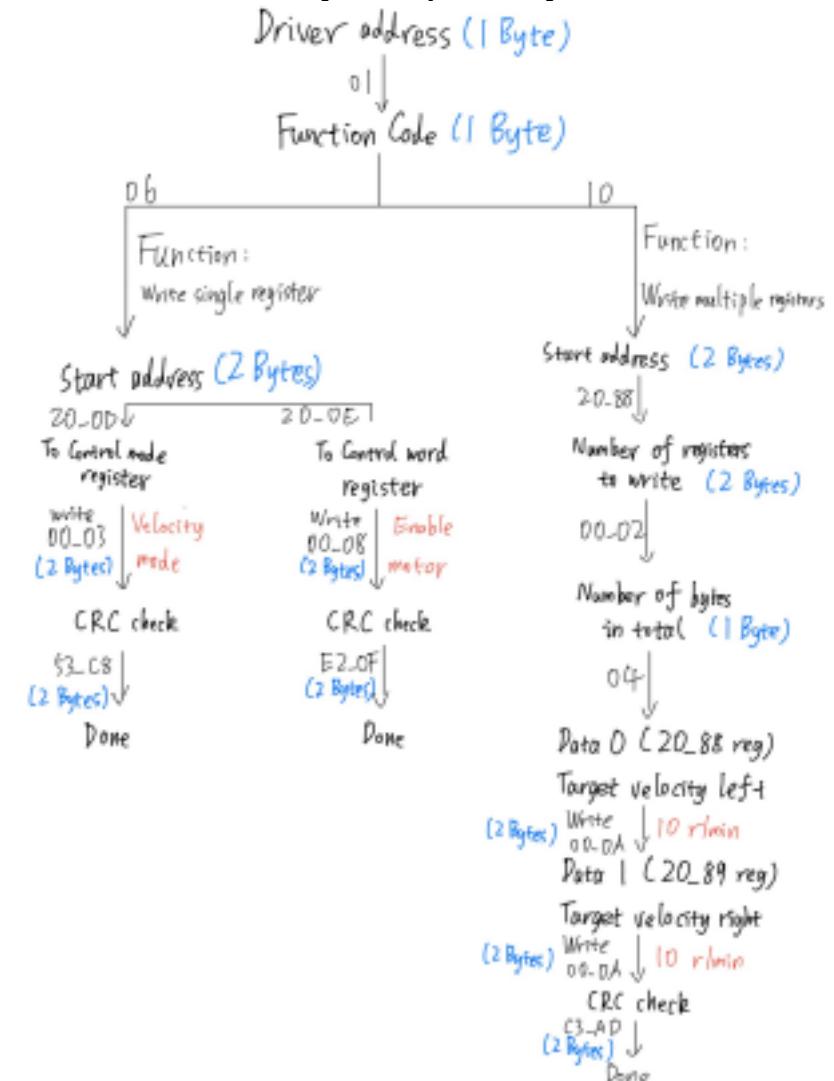
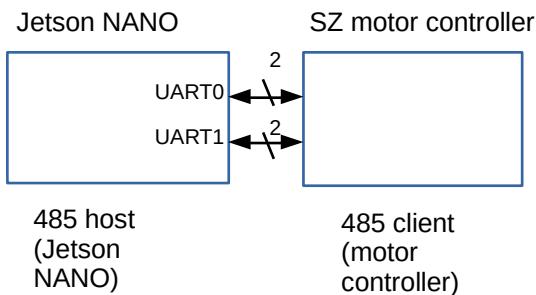
For language translation on line: DeepL, yy

另外请提供一个用于贵公司驱动器的软件源程序样本，实现（1）初始设置；（2）转动左轮 5 秒，然后转动右轮 5 秒；（3）然后反向转动左轮 5 秒，再反向转动右轮 5 秒钟。

HL: 2023-6-30: Please also provide a sample software source program for your drive that implements (1) the initial setup, (2) turning the left wheel for 5 seconds, then turning the right wheel for 5 seconds, and (3) then reversing the left wheel for 5 seconds, then reversing the right wheel for 5 seconds.

```

01 06 20 0D 00 03 53 C8
01 06 20 0E 00 08 E2 0F
01 10 20 88 00 02 04 00 0A 00 0A C3 AD (set left motor and right motor target speed to 10RPM in positive direction)
+set 5s delay time in keil
01 10 20 88 00 02 04 FF F6 FF F6 72 38(set left motor and right motor target speed to 10RPM in negative direction)
+set 5s delay time in keil
  
```





# Nnn-n-Ctione-Default-SD-Card- Installations-hl-2023-3-14.odp (Under Construction)

CTI One Corporation  
This document is created by:  
Harry Li, Ph.D.  
Yusuke Yakuwa

March 14, 2023



# Ctione Default SD-Card Installations

## (7/11,2023)

| No. | Package Name | Purpose |
|-----|--------------|---------|
|-----|--------------|---------|

1.

..

|         |               |
|---------|---------------|
| minicom | for UART test |
|---------|---------------|

# Bring Up Jetson NANO (7/12,2023)

Step 1. we are using the following development kit, please check its version first. It should be B01.

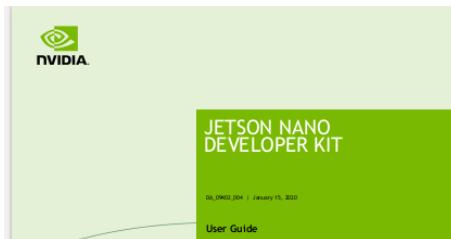
- The latest Jetson Nano Developer Kit (part number 945-13450-0000-100), which includes carrier board revision B01.
- The original Jetson Nano Developer Kit (part number 945-13450-0000-000), which includes carrier board revision A02.

Note HL 2023-7-12 Confidential:  
User id: ctione  
Pwd: ctione

Step 2. Insert prepared SD card with the kernel image, then connect the power, if the pwe LED light does not on, then check the jumper J48, The J48 jumper has to be placed to connect both pins to make sure the barrel jack power supply is selected.

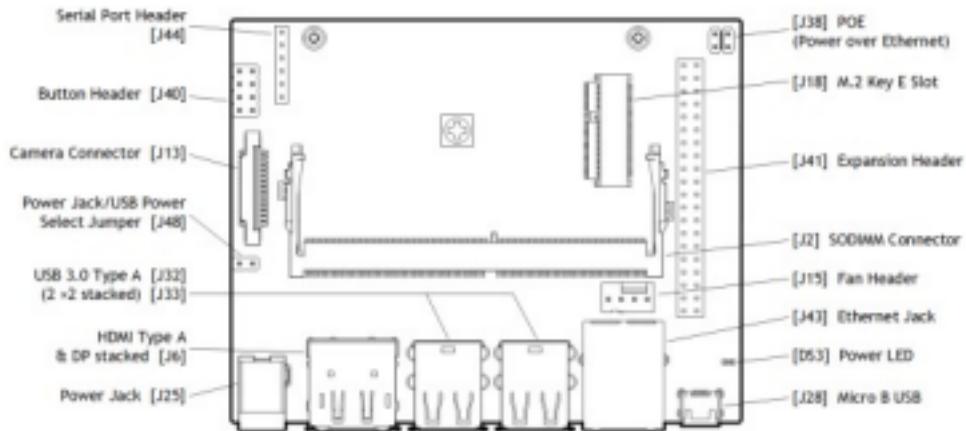
Document:

```
/media/harry/easystore1/backup-2020-2-15/CTI/3proejcts/3-8-smart-tech/3-8-4-CTI/3-8-4-6-products/AIV200/190-robots-health/190q-manufacturing-production/190q-7-integration/190q-7e-bringUp-nano/190q-7e-0-Nano-dev-user-guide$
```

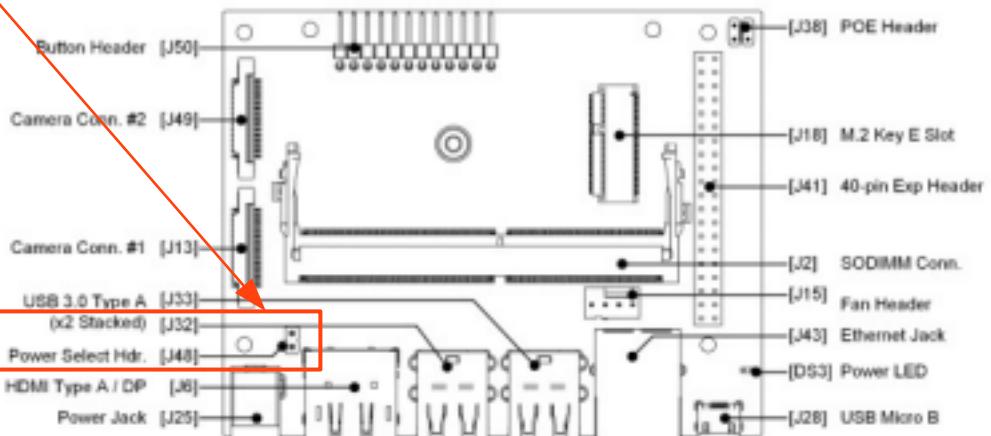


Tutorial link: <https://github.com/YahboomTechnology/Jetson-NANO-4GB>

## Developer kit carrier boards: rev A02 top view



## Developer kit module and carrier board: rev B01 top view



# Jetson NANO UART Loopback Test (7/12,2023)

Step 1. On NANO board, find 2 UART ports, pp. 13, 190p-1b-io-board-nano-v6-hl-2023-7-09.odp  
J41-8 Tx, J41-10 Rx, connect them to form a loop;

|               |     |        |           |
|---------------|-----|--------|-----------|
| UART1(left)_1 | VCC | J41-1  | 3V3       |
| UART1(left)_2 | TxD | J41-10 | UART_2_RX |
| UART1(left)_3 | RxD | J41-8  | UART_2_TX |
| UART1(left)_4 | GND | J41-8  | gnd       |
| UART2(left)_1 | VCC | J41-1  | 3V3       |
| UART2(left)_2 | TxD | J50_RX |           |
| UART2(left)_3 | RxD | J50_TX |           |
| UART2(left)_4 | GND | J41-8  | gnd       |



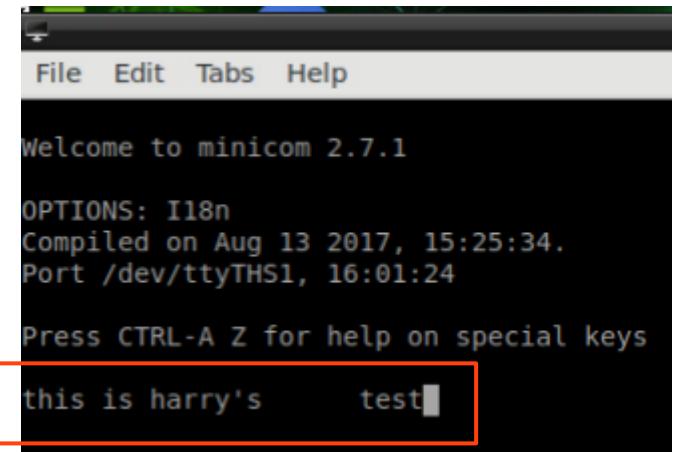
Note: change the NANO display resolution:

```
xrandr --output HDMI-0 --mode 1024x768
```

2. Find UART ports:

```
dmesg | grep tty
```

Run minicom to set up the UART communication parameters by HL.  
Putty testing and Python code testing by Youran Zheng



```
File Edit Tabs Help
ctione@ctione-desktop:~$ dmesg | grep tty
[    0.000000] Kernel command line: tegraid=21.1.2.0.0 ddr_die=4096M@2048M section=512M memtype=0 vpr_
b_port_owner_info=0 lane_owner_info=0 emc max_dvfs=0 touch_id=0@63 video=tegrafb no_console_suspend=1
[    0.001841] console [tty0] enabled
[    1.128037] console [ttyS0] disabled
[    1.128145] 70006000.serial: ttyS0 at MMIO 0x70006000 (irq = 63, base_baud = 25500000) is a Tegra
[    1.128245] console [ttyS0] enabled
[    1.129216] 70006040.serial: ttyTHS1 at MMIO 0x70006040 (irq = 64, base_baud = 0) is a TEGRA_UART
[    1.129618] 70006200.serial: ttyTHS2 at MMIO 0x70006200 (irq = 65, base_baud = 0) is a TEGRA_UART
[    2.985128] systemd[1]: Created slice system-serial\x2dgetty.slice.
ctione@ctione-desktop:~$
```



# Jetson NANO UART to 485 Motor Controller Test (7/12,2023)

Note: You should perform the UART loop back test first before to start this test.

Step 1. Make sure the UART to 485 converter is ready, the power Vcc = 3p3 V or 5 V.

[https://www.amazon.com/HiLetgo-Reciprocal-Hardware-Automatic-Converter/dp/B082Y19KV9/ref=sr\\_1\\_4?crid=3ND4JA7TYWJEG&keywords=uart+to+485&qid=1688190894&srefix=uart+to+485%2Caps%2C136&sr=8-4](https://www.amazon.com/HiLetgo-Reciprocal-Hardware-Automatic-Converter/dp/B082Y19KV9/ref=sr_1_4?crid=3ND4JA7TYWJEG&keywords=uart+to+485&qid=1688190894&srefix=uart+to+485%2Caps%2C136&sr=8-4)

SZ Motor Controller Wiring (Source: 6ZLAC8015D 接线 (英文) .mp4)

|        |   |
|--------|---|
| Orange | A |
| Brown  | B |

# Field Service Engineer: Laptop Based RS232 Loopback Test (7/12,2023)

Step 1. On Laptop, UART loopback test.

Hardware: need UART-RS232 converter

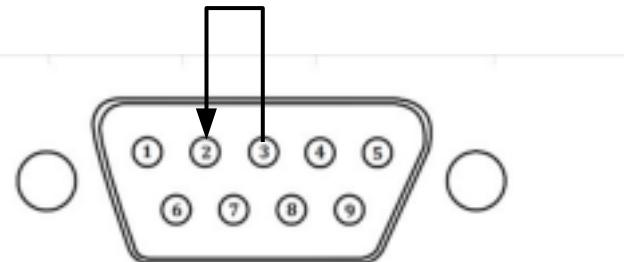
Software:

1. Find USB-to-UART (ttyx) devices:

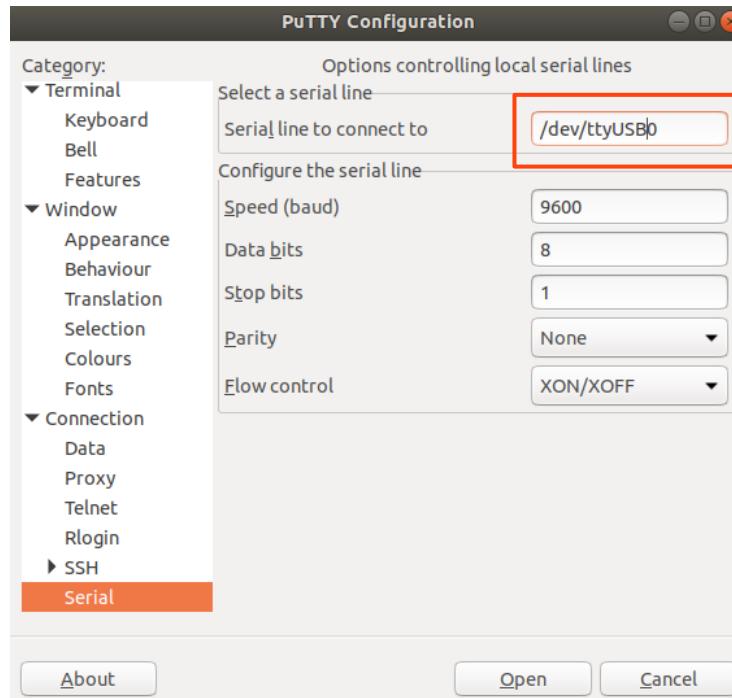
```
dmesg | grep tty
```

```
File Edit View Search Terminal Help
harry@workstation: ~
(base) harry@workstation:~$ dmesg | grep tty
[ 0.101516] printk: console [tty0] enabled
[ 4768.370139] usb 1-10.1.3: FTDI USB Serial Device converter now attached to /dev/ttyUSB0
```

2. Connect pin 2 (Rx) and 3 (Tx), now start putty, see Fig. 2.



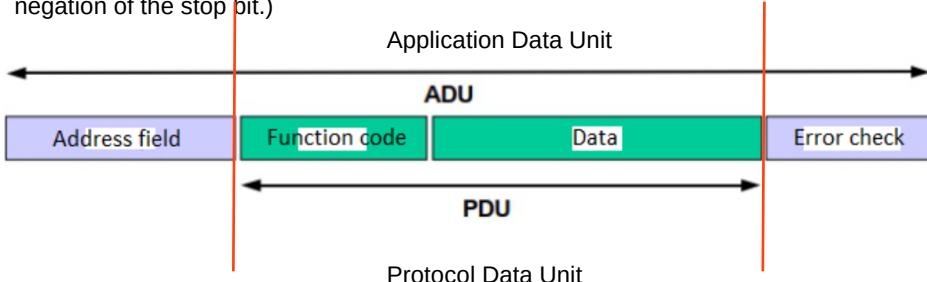
|       | RS232 | CAN  | CAN (OBD-II) | RS485 |
|-------|-------|------|--------------|-------|
| PIN1  |       |      | GND          | A     |
| PIN 2 | RX    | CANL |              | B     |
| PIN 3 | TX    | GND  | CANH         |       |
| PIN 5 | GND   |      | CANL         | GND   |
| PIN 7 |       | CANH |              |       |



**Modbus RTU for the RS485 Motor Controller (7/6-6/30,2023)**

HL 2023-7-10: from SZ manufacturer's document. The conditions for the controller setting:

1. Modbus RTU protocol.
  2. The driver address can be set to 0-127, the default address is 1.
  3. Baud rate: 115200 by default (ZLAC8015D has 7 optional baud rates: 9600, 19200, 38400, 57600, 115200, 128000, 256000).
  4. There are 8 data bits, No Parity, Stop bit is 1, (HL: the start bit is the negation of the stop bit.)



Ref1.



ZLAC8015D Servo Driver (Special for Hub Servo Motor) RS485 Communication Instruction Version 1.0

## ZLAC8015D SERVO DRIVER (SPECIAL FOR HUB)

## SERVO MOTOR)

## RS485 COMMUNICATION INSTRUCTION

1 | <https://www.youtube.com/watch?v=6MtFLA0NW1c>

default Modbus port 502 in raspberry pi

<https://pymodbus.readthedocs.io/en/latest/> Requires Python  $\geq 3.8$

## Pymodbus installation and sample code

## Simple synchronous client

[https://buildmedia.readthedocs.org/  
media/pdf/pymodbus/latest/  
pymodbus.pdf](https://buildmedia.readthedocs.org/media/pdf/pymodbus/latest/pymodbus.pdf)

**PyModbus**  
*Release 3.4.0dev*

## Open Source volunteers

Ref3.

[https://github.com/ahanjaya/Modbus-RTU/blob/main/modbus\\_min.py](https://github.com/ahanjaya/Modbus-RTU/blob/main/modbus_min.py)



Nnn-n-product-liability-insurance-hl-2023-7-17.odp

(Under Construction)

CTI One Corporation  
This document is created by:

Harry Li, Ph.D.  
Yusuke Yakuwa

July 17, 2023

# Product Liability Insurance (7/17, 2023)

www.wixsite.com  
Login Account: contact@ctione.com  
Password: Biophotonics06!

<https://www.insurancecanopy.com/product-liability-medical-devices>

 Insurance Canopy  
<https://www.insurancecanopy.com> › product-liability... ::

## Product Liability Insurance for Medical Devices

Typically, policy premiums start around \$2,298 per year for medical equipment insurance. premium can vary based on different factors, such as: Business ...

### Product Liability Insurance Cost

May 18, 2023 – Product liability insurance is typically included in general liability insurance, which costs small businesses an average of \$42 per month.

[Product Liability Insurance - Buy Instantly From \\$19 a Month](#)

# HDMI Bike Mount Monitor (7/17,2023)

**10.1 or 7 Inch Small Monitor, Raspberry Pi Monitor  
Built-in Speakers, Mini Monitor 1024 x 600, HDMI  
Monitor 60 Hz, 5ms, IPS Screen Display w/Remote  
Control, HDMI VGA AV BNC Input, Computer  
Monito \$49 for 7", 52.99 for 10.1"**

[www.wixsite.com](http://www.wixsite.com)  
Login Account: contact@ctione.com  
Password: Biophotonics06!

[https://www.amazon.com/Monitor-Raspberry-Speakers-Display-Computer/dp/B09ZDMNZPT/ref=asc\\_df\\_B09ZDMNZPT/?tag=hyprod-20&linkCode=df0&hvadid=615861270911&hvpos=&hvnetw=g&hvrand=1458690738210876213&hvpone=&hvptwo=&hvqmt=&hvdev=c&hvdvcmdl=&hvlocint=&hvlo cphy=9031968&hvtargid=pla-1835315613251&psc=1](https://www.amazon.com/Monitor-Raspberry-Speakers-Display-Computer/dp/B09ZDMNZPT/ref=asc_df_B09ZDMNZPT/?tag=hyprod-20&linkCode=df0&hvadid=615861270911&hvpos=&hvnetw=g&hvrand=1458690738210876213&hvpone=&hvptwo=&hvqmt=&hvdev=c&hvdvcmdl=&hvlocint=&hvlo cphy=9031968&hvtargid=pla-1835315613251&psc=1)



# NVIDIA Jetson Nano J41 Header Pinout

<https://www.jetsonhacks.com/nvidia-jetson-nano-j41-header-pinout/>

I2C and UART pins are connected to hardware and should not be reassigned. By default, all other pins (except power) are assigned as GPIO. Pins labeled with other functions are recommended functions if using a different device tree.

|         |                        |    |    |                        |         |
|---------|------------------------|----|----|------------------------|---------|
|         | GND                    | 25 | 26 | SPI_1_CS1              | gpio20  |
|         | I2C_1_SDA<br>I2C Bus 0 | 27 | 28 | I2C_1_SCL<br>I2C Bus 0 |         |
| gpio149 | CAM_AF_EN              | 29 | 30 | GND                    |         |
| gpio200 | GPIO_PZ0               | 31 | 32 | LCD_BL_PWM             | gpio168 |
| gpio38  | GPIO_PE6               | 33 | 34 | GND                    |         |
| gpio76  | I2S_4_LRCK             | 35 | 36 | UART_2_CTS             | gpio51  |
| gpio12  | SPI_2_MOSI             | 37 | 38 | I2S_4_SDIN             | gpio77  |
|         | GND                    | 39 | 40 | I2S_4_SDOUT            | gpio78  |

gpio-nano-2021-10-14

| Sysfs GPIO | Name                   | Pin | Pin | Name                                   | Sysfs GPIO |
|------------|------------------------|-----|-----|--|------------|
|            | 3.3 VDC<br>Power       | 1   | 2   | 5.0 VDC<br>Power                       |            |
|            | I2C_2_SDA<br>I2C Bus 1 | 3   | 4   | 5.0 VDC<br>Power                       |            |
|            | I2C_2_SCL<br>I2C Bus 1 | 5   | 6   | GND                                    |            |
| gpio216    | AUDIO_MCLK             | 7   | 8   | UART_2_TX<br><code>/dev/ttyTHS1</code> |            |
|            | GND                    | 9   | 10  | UART_2_RX<br><code>/dev/ttyTHS1</code> |            |
| gpio50     | UART_2_RTS             | 11  | 12  | I2S_4_SCLK                             | gpio79     |
| gpio14     | SPI_2_SCK              | 13  | 14  | GND                                    |            |
| gpio194    | LCD_TE                 | 15  | 16  | SPI_2_CS1                              | gpio232    |
|            | 3.3 VDC<br>Power       | 17  | 18  | SPI_2_CS0                              | gpio15     |
| gpio16     | SPI_1_MOSI             | 19  | 20  | GND                                    |            |
| gpio17     | SPI_1_MISO             | 21  | 22  | SPI_2_MISO                             | gpio13     |
| gpio18     | SPI_1_SCK              | 23  | 24  | SPI_1_CS0                              | gpio19     |
|            | GND                    | 25  | 26  | SPI_1_CS1                              | gpio20     |