CMPE242
Spring 2023
PART II

April 3rd (Monday)

Road map for the 2nd half of the Semester:

IIOT (Industrial IOT):

- ADC Data Validation → ① F.F.T. (Fast Fourier Transform)
- Analog Sensor interface Design.

② Power Spectrum Technique.

③ Hardware Architecture Aspects

Ion Selective Electrode Sensors { Many Applications in different Industry Sector.

Homework Extension Next Monday with Demo.

Project: Due the End of the Semester.

Implementation (1/3) 33% to { PID. I2C Sensor, PWM Motor Control { Pre-processing CKT., ADC

Research Part (1/3): ① P.P.T Presentation on State-of-the-Art Technology in the Embedded world.

② Report (GuideLine)

③ Proposal (One page, No more than), Submit to the CANVAS for Approval.
— By Wednesday / Monday Next

(1/3) Demo & Presentation: By the end of the Semester.

Example: for Analog ISE Sensor interface Design.

Ref: 2018S-20-NH3- 4Design2018-1-16.pdf

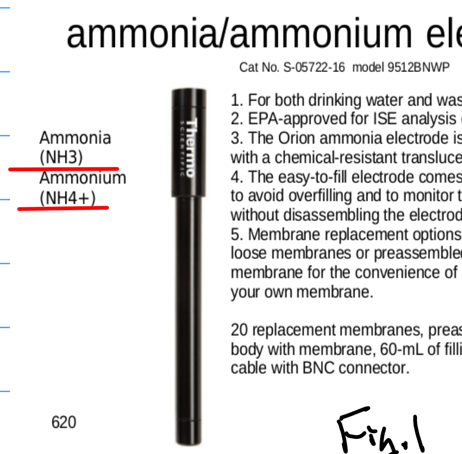

Fig.1

google Definition: ①

Principle of ion-selective electrode (I.S.E.) An ideal I.S.E. consists of a thin membrane across which only the intended ion can be transported. The transport of ions from a high conc. to a low one through a selective binding with some sites within the membrane creates a potential difference.

→ Example of A Battery

ISE Sensor

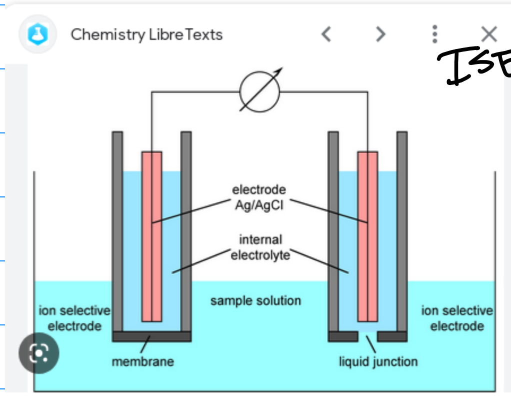

Fig.2

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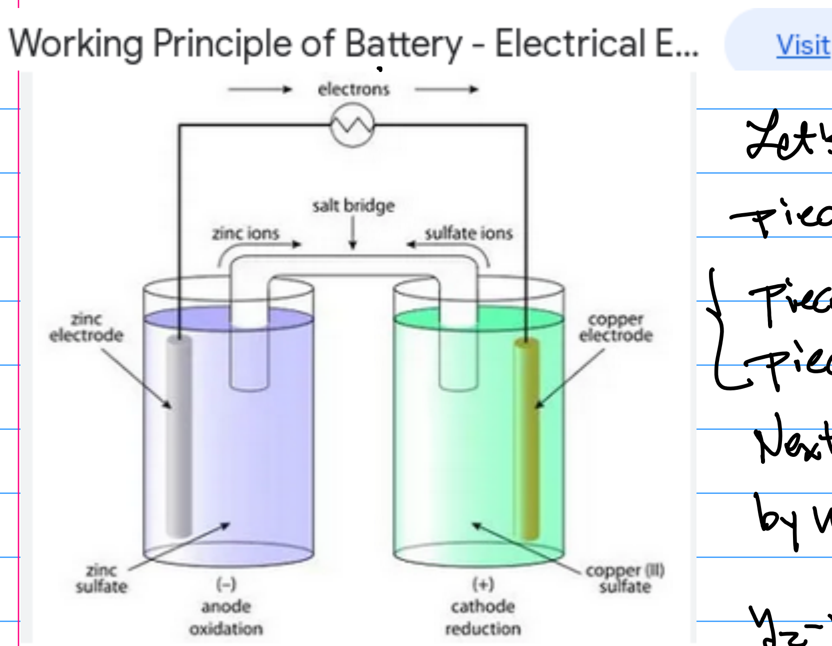


Fig. 3

Observation: Use Battery As An Example to Demonstrate Ion Selective Electrode Sensor. See NH_3/NH_4^+ sensor in Fig. 1.

Characteristic

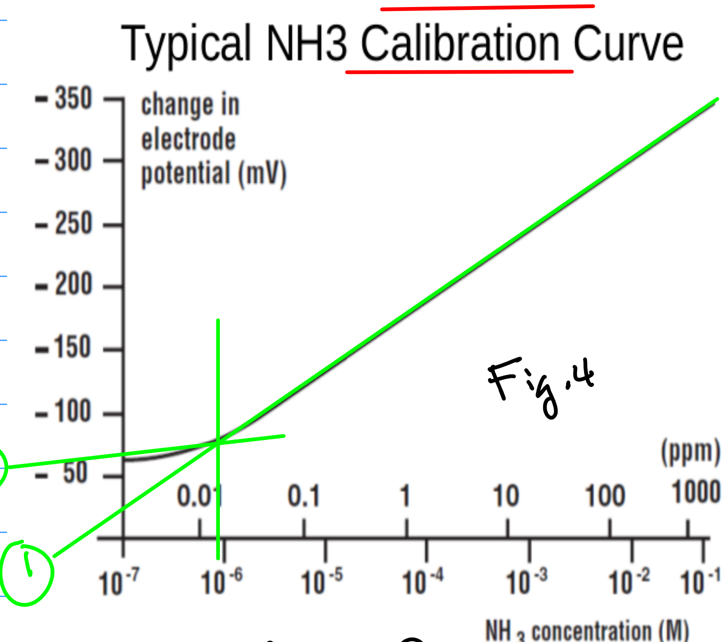


Fig. 4

Note 1. We like to have the Linear Characteristics from the Calibration Curve, Such as [1, 10], [10, 100], [100, 1000], etc.

Note 2: For the Non Linear Part, Let's Perform Linearization — By using Piece-wise Linear Lines.

$$\begin{cases} \text{Piece-wise Line 1.} \\ \text{Piece-wise Line 2.} \end{cases}$$

Next Step is to formulate Each Line by using Linear Equation.

$$\frac{y_2 - y_1}{x_2 - x_1} = \frac{y - y_1}{x - x_1} \quad \cdots (1)$$

Solve for $y = bx + c$ (see the Previous Notes).

With Simplification By Removing Very Low Concentration Part, we have

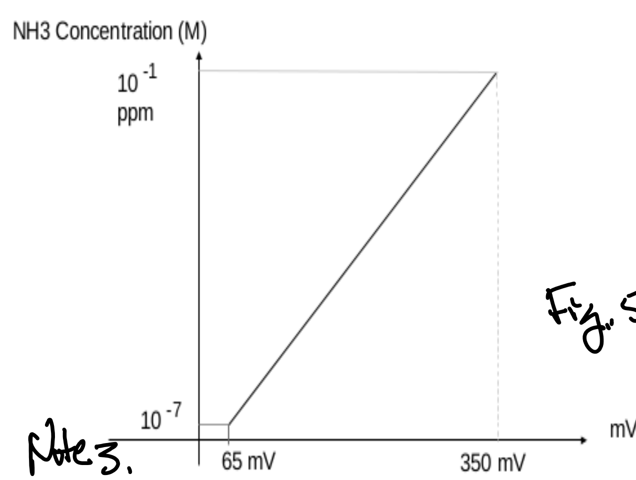
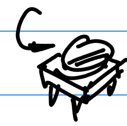


Fig. 5

Note 3. Then, Change the Cal-Curve to the Characteristic Curve e.g. Horizontal Axis is voltage for the Design of interface.



Industrial Analog Sensor Interface Protocol:

1° Output has to be defined by Current. 2° The Range 4~20mA.

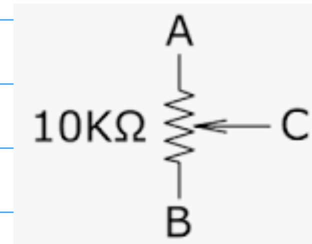


Fig. 1

Control.com
https://control.com › Technical Articles

Why is 4-20 mA Current Used for Industrial Analog Sensors?

Note 4. Fit to the Dynamic Range of your target platform.

↓

External ADC Needed

CMOS Range ADC [0, 3.3]

TTL " " [0, 5.0]

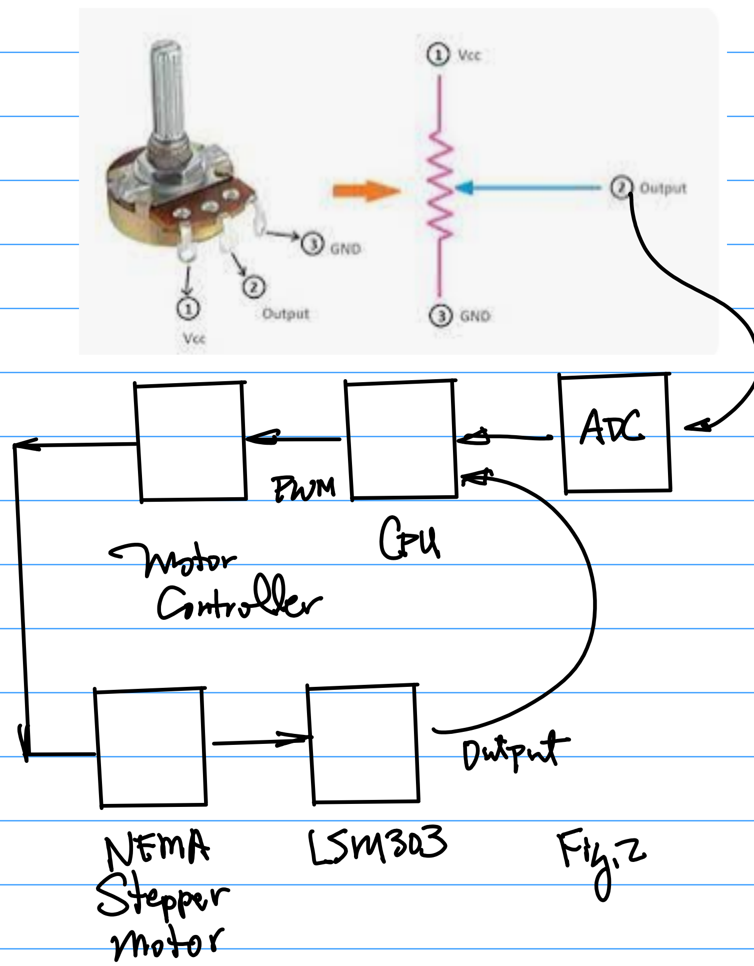


Fig. 2

https://www.adafruit.com/product/1083

April 5th (Wed)

Note 1° New updated Due Date for the Motor Control, please use PWM from your target platform as the input to the motor controller.

2° ADC Unit for the project

Selection Guide.

1° Interface protocol
I2C; ~4 mbps

2° 0.5~2 MSPS
(Million Samples per Second)

3° 10~12 bits per Sample.

Example: P.O.T.

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ADS1015 12-Bit ADC - 4 Channel with Programmable Gain Amplifier - STEMMA QT / Qwiic

Product ID: 1083

$9.95

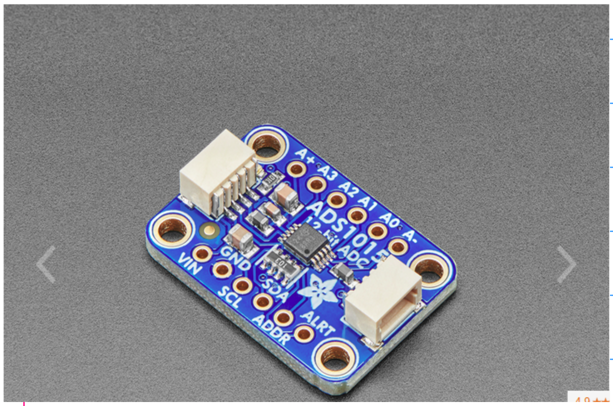


Fig.3

Note: Input Voltage Range

TEXAS INSTRUMENTS

ADS1013
ADS1014
ADS1015

www.ti.com

SBAS473C – MAY 2009 – REVISED OCTOBER 2009

ELECTRICAL CHARACTERISTICS

All specifications at –40°C to +125°C, VDD = 3.3V, and Full-Scale (FS) = ±2.048V, unless otherwise noted. Typical values are at +25°C.

PARAMETER	TEST CONDITIONS	ADS1013, ADS1014, ADS1015 MIN	TYP	MAX	UNIT
ANALOG INPUT					
Full-scale input voltage(1)	$V_{IN} = (AIN_P) - (AIN_N)$		±4.096/PGA		V
Analog input voltage	AIN_P or AIN_N to GND	GND		VDD	V
Differential input impedance			See Table 2		
Common-mode input impedance	FS = ±6.144V(1)		10		MΩ
	FS = ±4.096V(1), ±2.048V		6		MΩ
	FS = ±1.024V		3		MΩ
	FS = ±0.512V, ±0.256V		100		MΩ

Example: Design Objective.

1° Selection of An ADC.

Construct the Characteristic Curve of the ADC

$V_{max} = 3.3V$ for CMOS

Output of the ADC (10 bits)

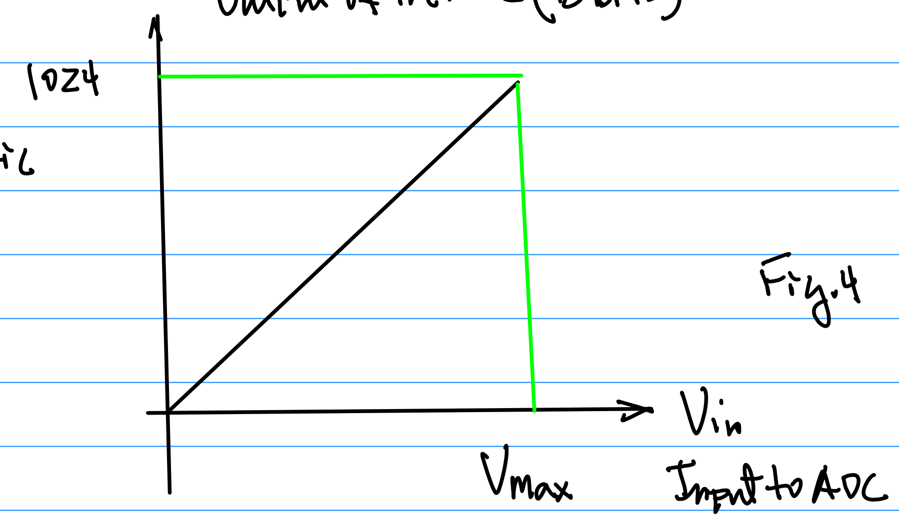


Fig.4

2° Design Objective: To Design A pre-process unit to make the Analog Sensor Output match to the ADC input dynamic Range.

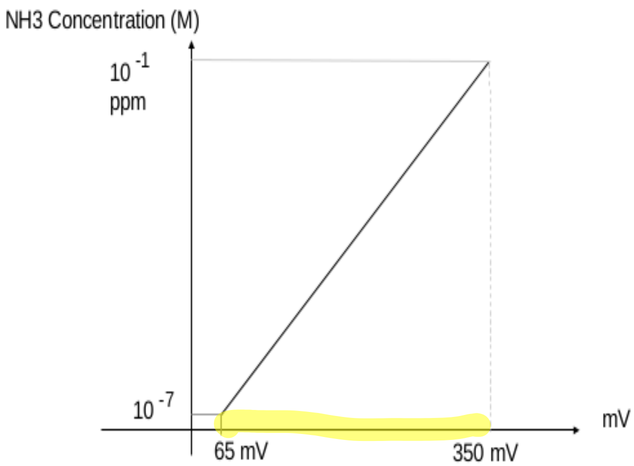
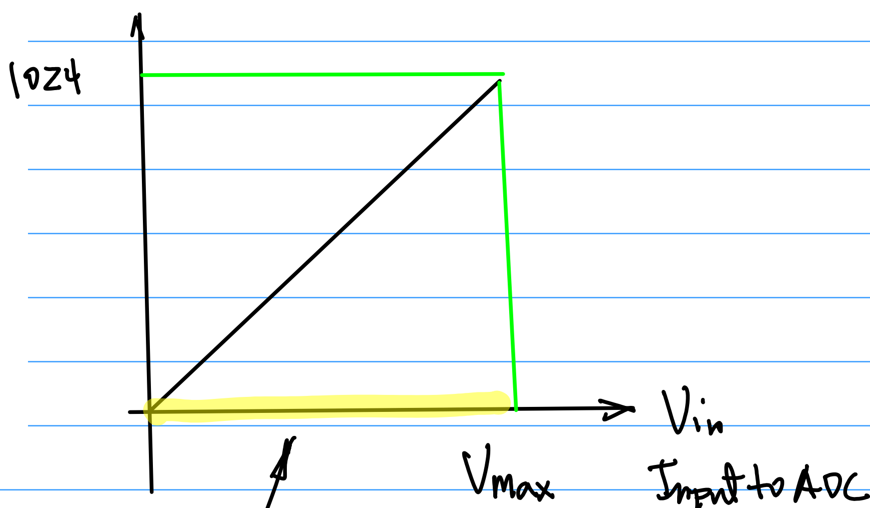


Fig.5



Step 1. 65mV → 0

350mV → Vmax

In general,

$V_{s,min} \to V_{ADC,min}$ ... (1)

$V_{s,max} \to V_{ADC,max}$ ... (2)

April 10 (Mon).

Example: Continuation of the Preprocessing Design.

Note 1. Check 1015 ADC Dynamic Range for the Input.

[0, 3.3V]. Verification is Needed

Theoretical Analysis:

Step 1. Provide "offset" to shift the Sensor dynamic Range, Subtraction Can be utilized for this purpose. e.g.

$V_{sen} - V_{offset}$ ... (1)

which will Lead to the Result Below.

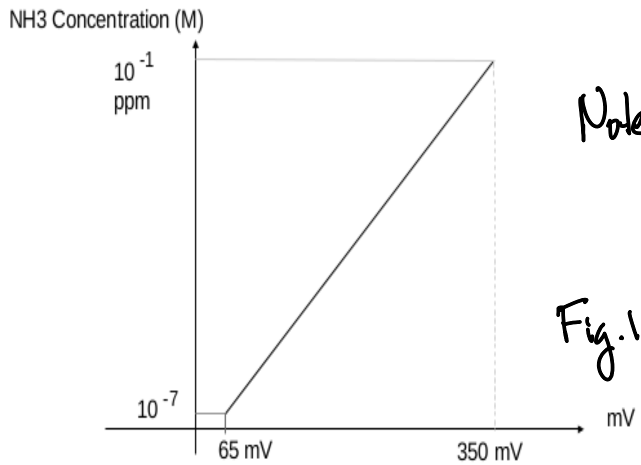


Fig.1a

Note 1. For more generalized Case, Let
$V_{min} = 65\,mV$, $V_{max} = 350\,mV$.
So, the offset $= -V_{min}$.
then, the Upper Bound after offset is
$V_{max} - V_{min}$

↓ Subtraction/offset

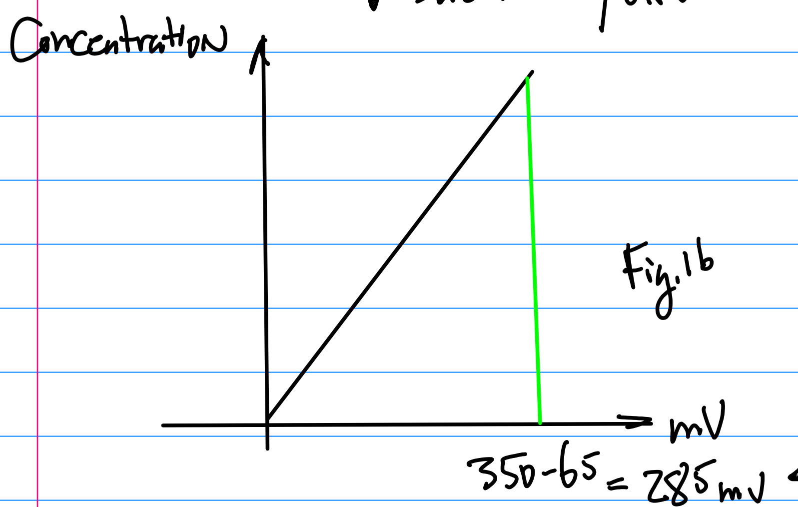


Fig.1b

Step 2. To magnify the Sensor Output Range to Match the entire Dynamic Range of the ADC.

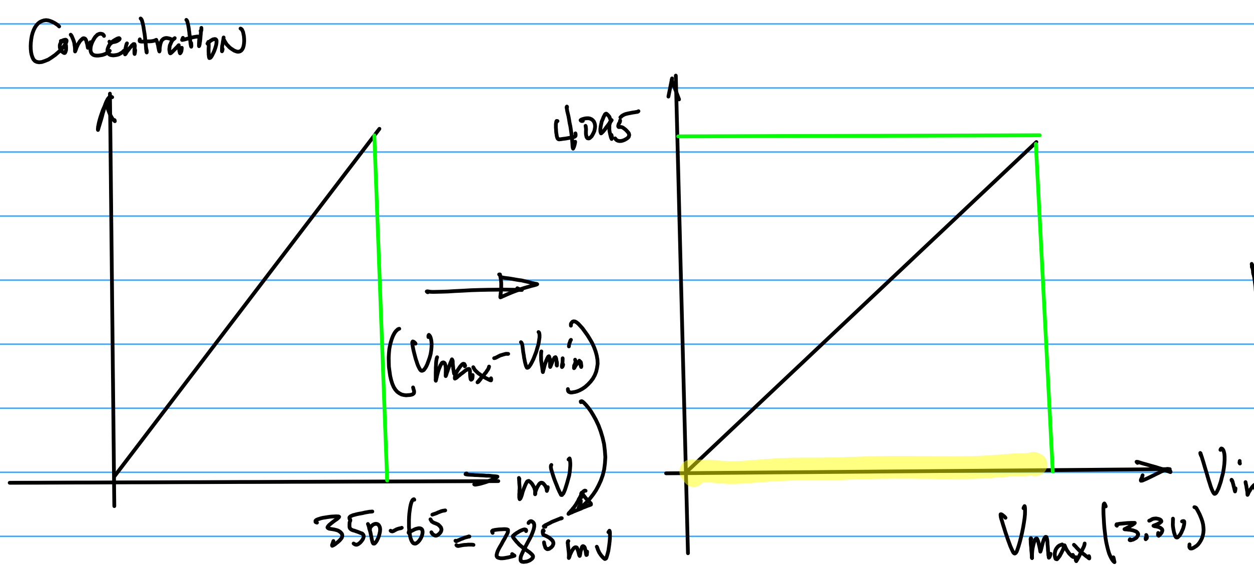


Fig.2

Find the Gain for the Magnification

$$A = \frac{V_{\text{Output Range}}}{V_{\text{Input Range}}} = \frac{3.3}{285 \times 10^{-3}} \cong 11.58$$

where 3.3 VDC is from 1015 ADC
for Example.

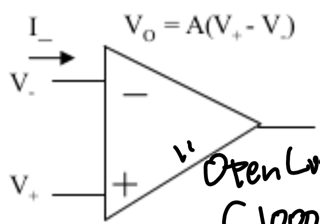
Example: Hardware Design for the pre-processing.
Ref. 1-0lecture10_OpAmp Circuits.pdf –

Note1: Using OpAmp for Pre-processing
Not for the Buffering.

OpAmp Device As a Buffering Stage

Both Analog and Digital Circuit

Note 2: Background

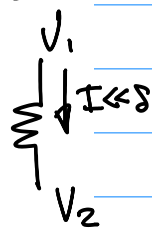


"Open Loop"

(1) To protect the previous stage's output signal, which is the input to the next stage, while sampling/connecting the signal to its next stage logic circuit. (2) Unit gain non-inverting OpAmp configuration is an excellent choice.

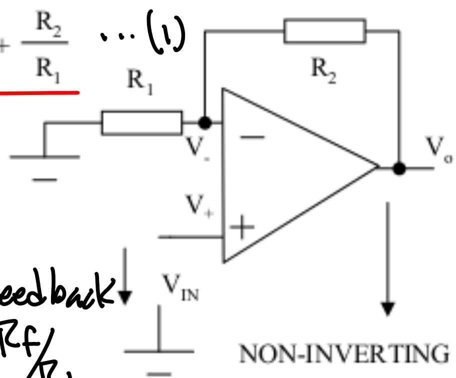
1000 or Bigger 1×10^{-9} A or Smaller

Ideal OpAmp Properties: (1) very large gain, A>>M; (2) draws very little current, $I_- \sim 0$, e.g., very high impedance; (3) $V_O = A(V_+ - V_-)$ is finite range, which leads to $V_+ = V_-$.



for Example 100MΩ or higher.

$$\frac{V_O}{V_{IN}} = 1 + \frac{R_2}{R_1} \quad \dots (1)$$



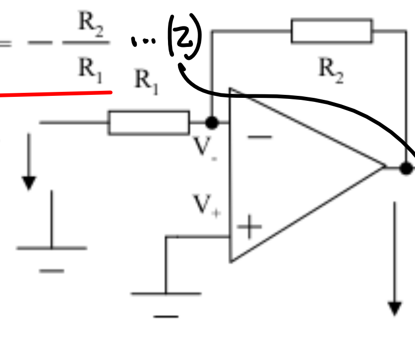
R_2 for feedback

So $A = 1 + R_f / R_1$

NON-INVERTING

Harry Li, PH.D. SJSU

$$\frac{V_O}{V_{IN}} = -\frac{R_2}{R_1} \quad \dots (2)$$



INVERTING

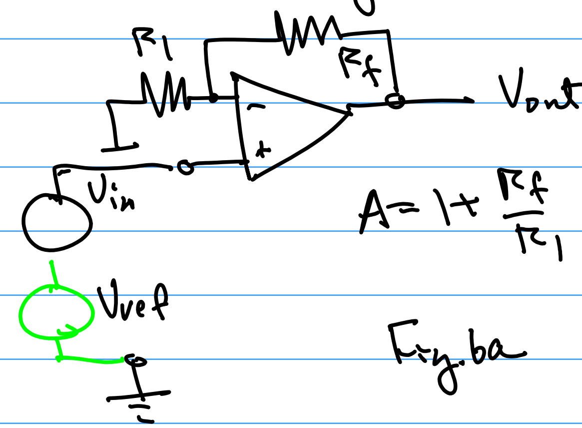
$$A = -\frac{R_f}{R_1}$$

Fig.3

Now, Consider the design Implementation for 2-Step Process.

Tools { Non-Inverting~ | Inverting~
└ Linear System: SuperImpose One Signal (offset) on to the other Signal (Input).

For Non-Inverting



$A = 1 + \frac{R_f}{R_1}$

Fig. 6a

"Black Box"

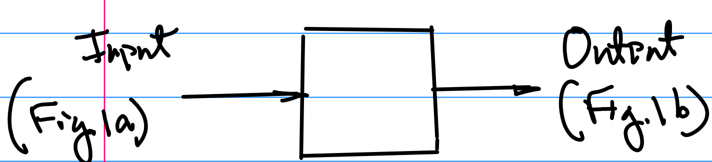


Fig. 4.

Linear System. "SuperImpose Signal for Offset"

For Inverting.

$A = -R_f/R_1$

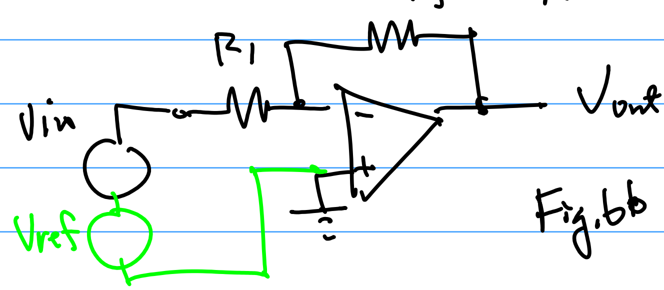


Fig. 6b

Note: Selection of Non-~ v.s. Inverting Configuration Depends on your design Need. This Design is an illustration of SuperImposing an "offset", e.g. V_{ref}.

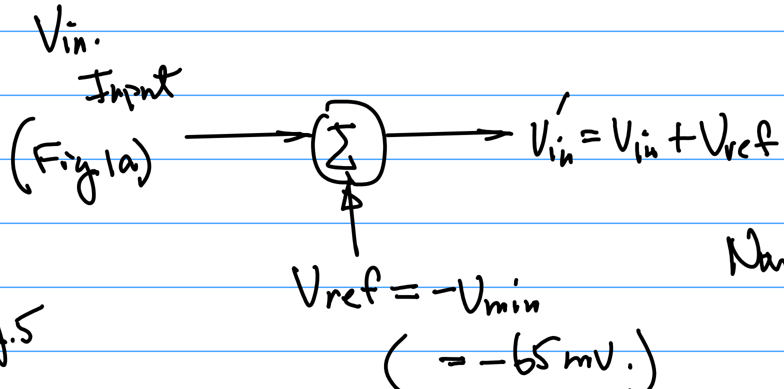


Fig. 5

In the Circuit Design. Just Connect 2 inputs together.

Now, to Magnify the Signal to match ADC's Dynamic Range.
Choose Non-Inverting Configuration (see Fig. 2)

Hence

$$A = 1 + \frac{R_f}{R_1} = 11.58$$

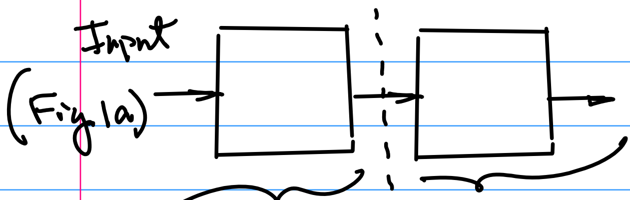
Choose $R_1 = 1\,k\Omega$

solve for $R_f \approx$ ~~77~~ $k\Omega$? 10.58 $k\Omega$

please verify it !

Stage 1   Stage 2

Box 1   Box 2



offset   Gain Amplification

Analog Devices
https://www.analog.com › ltspice-simulator

LTspice Information Center

LTspice® is a powerful, fast, and **free SPICE simulator** software, schematic capture and waveform viewer with enhancements and models for improving the ...

Free for Down Load, Originated from "Linear" A Silicon Valley Company.

EasyEDA
https://easyeda.com

EasyEDA - Online PCB design & circuit simulator

EasyEDA is a **free** and easy to use circuit design, **circuit simulator** and in your web browser.

Requirements: To Be Able to Run SPICE Simulator.

April 12 (Wed).

Note 1. The Last Project Preparation.
(Requires the Semester End Presentation).

Note 2. Implementation of ADC Unit.

P.O.T. 47K or 470K or Similar.

↓

ADC → Target → PWM → Controller

LSM303 ← Motor ←

Note 3. ADC Data Validation

FFT. Power Spectrum.

Note 4. Road Map.

IIOT (Analog Sensors. 4~20mA)

↓

Preprocessing

↓

OpAmp. (On-Line Simulation Tool)

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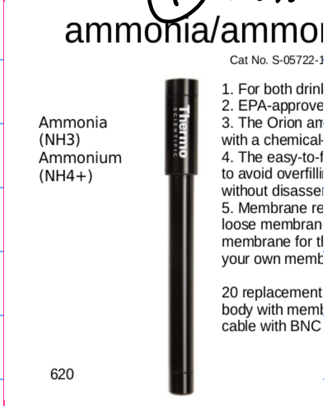
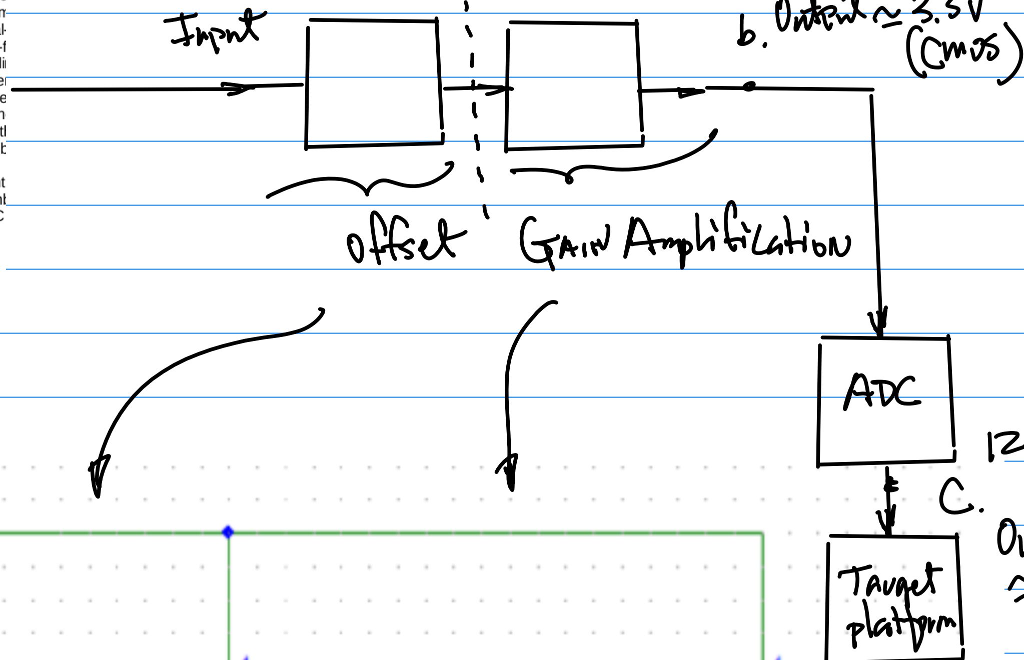
Example:

① Analog Sensor (ISE)



Stage 1 Stage 2

a. ~285mV Input

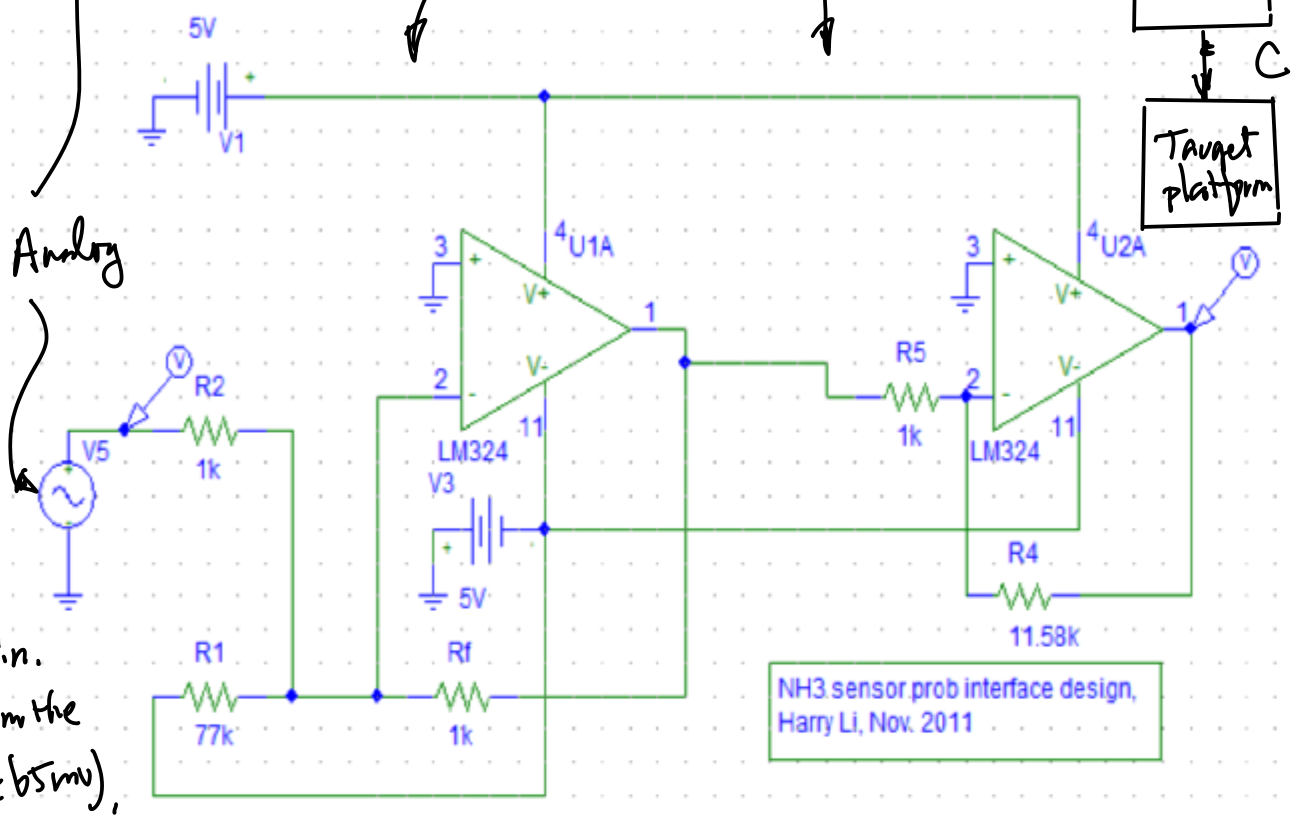


b. Output ~3.3V (CMOS)

offset Gain Amplification

12 bit ADC

c. Output ~4095



Note: 1° Analog Sensor — Signal Source
Sine wave Signal

2° for min. Input from the Sensor (~65mV), we want to get ADC Output = 0.

for max Input from the Sensor, we want to get Output (ADC): (3.3VDC) 4095

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Note: 3. Simulation of the Sensor Output as the input to the pre-processing Circuit.