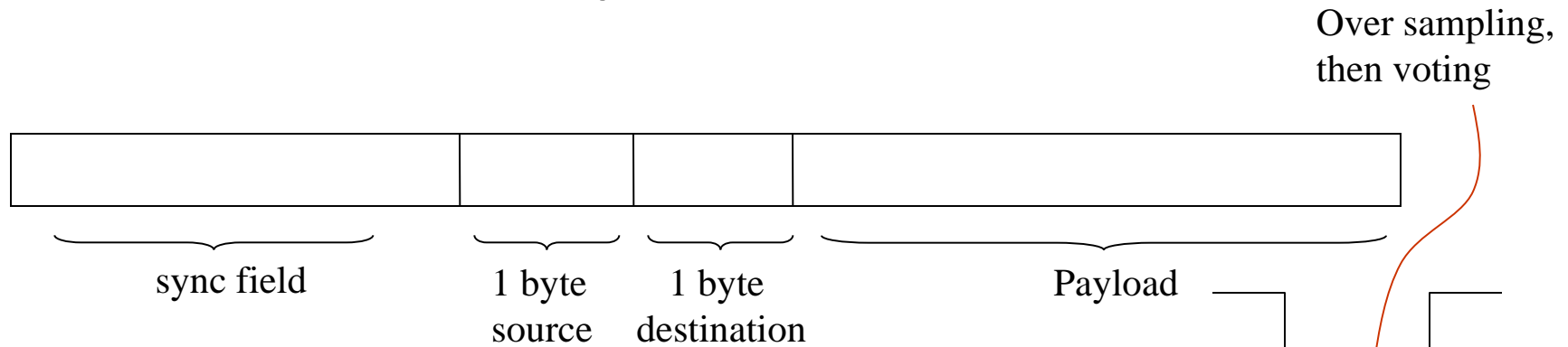


# LISA Synchronization



0's and 1's alternating pattern to capture each clock period (timing)

Design requirements: capture the timing with minimum matching bits to allow recover of the starting bit of the payload;

Therefore, you will have:

0xA0, 0xA1, ..., 0xAE, 0xAF

and

0x50, 0x51, ..., 0x5E, 0x5F.

Guideline Use 0xA and 0x5 as prefix to preserve 0-1 alternating patterns, and use linear increment numeral to mark the location to recover the start bit of the payload

**LISA: Linear Invariant Sync Algorithm**