After the Midtern Exam. Nov8 (Monday) Topics today: Modulation, Demodulation Example: Road Map for the 2nd half of the Semester. Industrial TSK-Phase Shift Keying Touristed ACOS(20076244)

Tot Solution 802.11b Standard) "Coming of the Phase, ASin (12) 4-1 (...) I mod/DEMOD Technique. Note: Thase, Asin(well+4) ... (1) phase" shift" We can change phase value to make to curry impormation, e, y, o Z. Background on modulation. What is modulation! A technique

By multiplying a modulating punction

to a exist function to move the

modulating function to a higher frequency Vanye. Block Angram to illustrate modulation Technique f(x) "modualating Signal" Why? (The objective) The objective of move the Base Band Signal (e.g. modulating Signal) to Righer Fregrency Range. Better Move Efficient Transmission Better Random Poise Resistance,

Ref on Theoretical Background, Former Translam. Z018F-117. on github

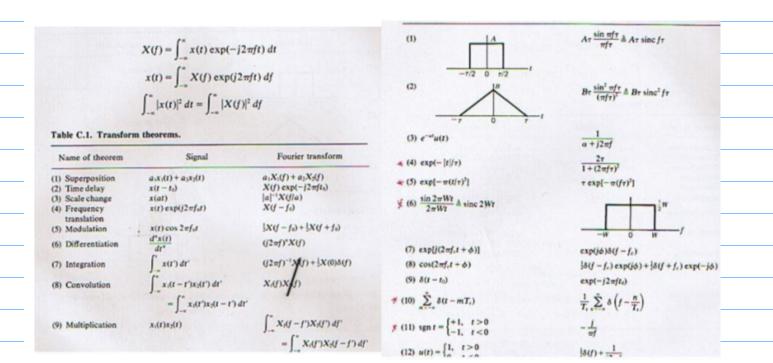
To gain fast Transmission Bit Rate

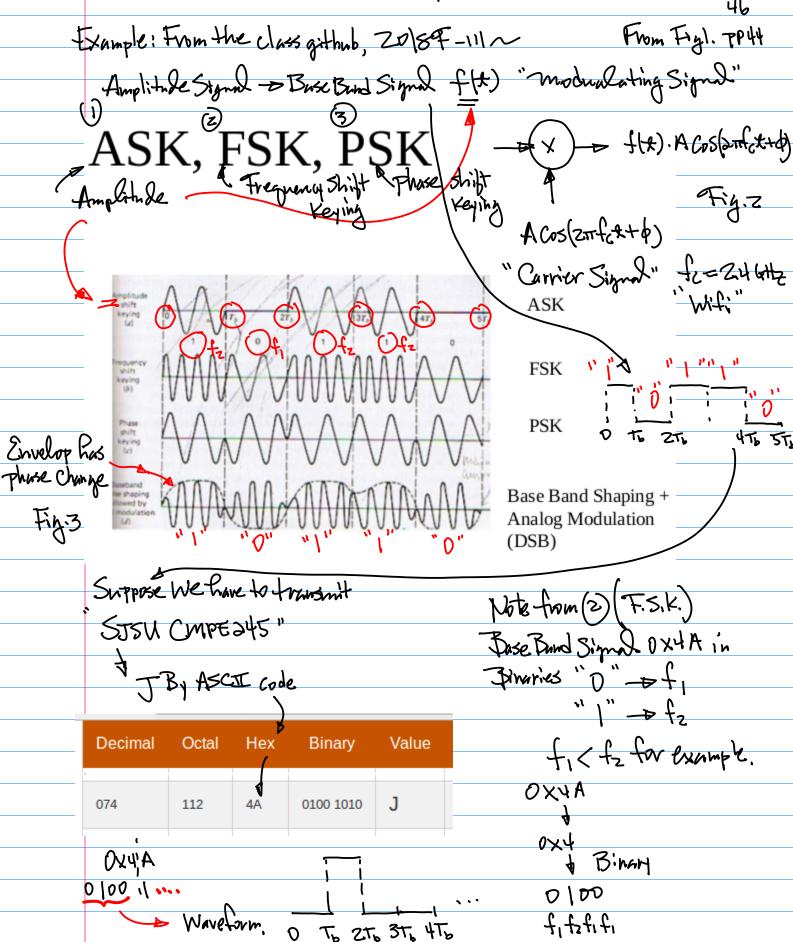
Troperlies in Formier Transform provides
foundation for good understanding of the Technique

Ref: github, 2018F-118~

Ref: from the class github. Z0/8F-111~

Theoretical Background. Review





0 X4, V Modulated Signal 0×4 * A Cosbarfette;) 0100 PIPZPIPI for Phase 1: A Cos(2mfct+on) = A Cos(Smfct to) =A cos 2mfc+ for Phase Z: A cooperfictory $=A\cos(2\pi f_c + T) = -A\cos 2\pi f_c +$ Nov.10 (Wed) Note: 1° References on PSK-modulation Demodulation on githrub ~ Compezy5;

(2021F-111~)

Project By End of the Semester. LORA P.F. to establish G.R. (Countive Radio), Team Bused Project. (1) LORA R.F. Ritto Implement SPI Based I/F to your tanget platform; One Kit perteam;

PI=Thate | = 0 Degree R=+hasez=180 Degree from Egnli). =(2) Formal Presentation

With Dam, Regimes Both RF. Kit, Any Shypostians! (3) Project Counts 20 pts. regimes integration to your target platform. Sample code-for CPC1769 partform 5 Provided as it is, Individual Vesponsibility to make it as

Homework (Due Aweek from today)

an interpret pout of your

1. Bild Hardware Interface to TZF module (Lava)!

final project.

Target Platform Problype System ORA R.F. Kit

SP.I.IIF. 3+1" PINS

g(+) = - (f(+) -... ()

SP.I. | MOSI. (master Out) Slave In) SCK (Sevial clock) "plus / pin"; Enable or Select Submission: 10 photo of the Setup should P.F. module integrated with you embedded wiveless 20 Photo or Jeeg, or Pof Shows the pin Connection/Connectivity diagram. 30 Ohe Paragraph Description For System Bring Inp. (LDRA R.F. KH) Be sure to provide UPL. 40 Elect a team Coordinator, provide Coordinator's name and all the team members where. to Create one pdf file for all the homework material, then Zipit Note: please indicate the Jeannember who has the trycked K.F. Kit. In your first photo, please

provide this information.

Time Domain

A g(t)

A

The period:

The per

2017F-108-lec-BB-Sign... A

Now, for the Znd Signalin Fig. 1 (modulation Block Diagram)

 $\frac{1}{2}[S(f-f_c)+S(f+f_c)]$ (Assuming $\phi=0$)

(8) $\cos(2\pi f_i t + \phi)$

 $\frac{1}{2}\delta(f-f_e)\exp(j\phi)+\frac{1}{2}\delta(f+f_e)\exp(-j\phi)$

Cos(sinfittb)

Carrier Signal", fc: Carrier frequency.

In Wifi Communication, fc=2.4 GHZ

from this table,

Assuming p=0 COS (211fct+b) = COS (211fct)

COS(>mfct) = = = [3(f-fc)+ 2(f+fc)]