Homework Assignment
Loar RF1 Hardware Implementation
CMPE 245

1. Build Lora RF hardware interface to integrate the Loar RF module to your prototype. Note SPI interface requires "3+1" SPI protocol based pins (MOSI, MISO, SCK, and chip enable, like SSP for LPC1769 based embedded host platform)

2. What to submit:
(2.1) Photo of the system setup which shows the Lora RF module integrated to the prototype board;
(2.2) Photo or PDF of the pin assignment diagram of the Lora RF module. (Note if your embedded target platform is not LPC1769, then it is a good idea to include the pin assignment of your chosen target platform).
(2.3) One paragraph description for the system bring-up procedure for Lora RF kit, be sure to provide URL link from the vendor of the RF kit.
3. Elect a team coordinator and provide the coordinator's contact information (Name, email) and provide also all the other team member names.
4. Please create one pdf file to put all your submission material in this one pdf file.
5. Submission to CANVAS, Put the submission material into one zip file, name it with
your first name + your last name + lora-rf-hardware1 + yy/mm/dd.zip

(END)