

9-10-2018 Sync Design

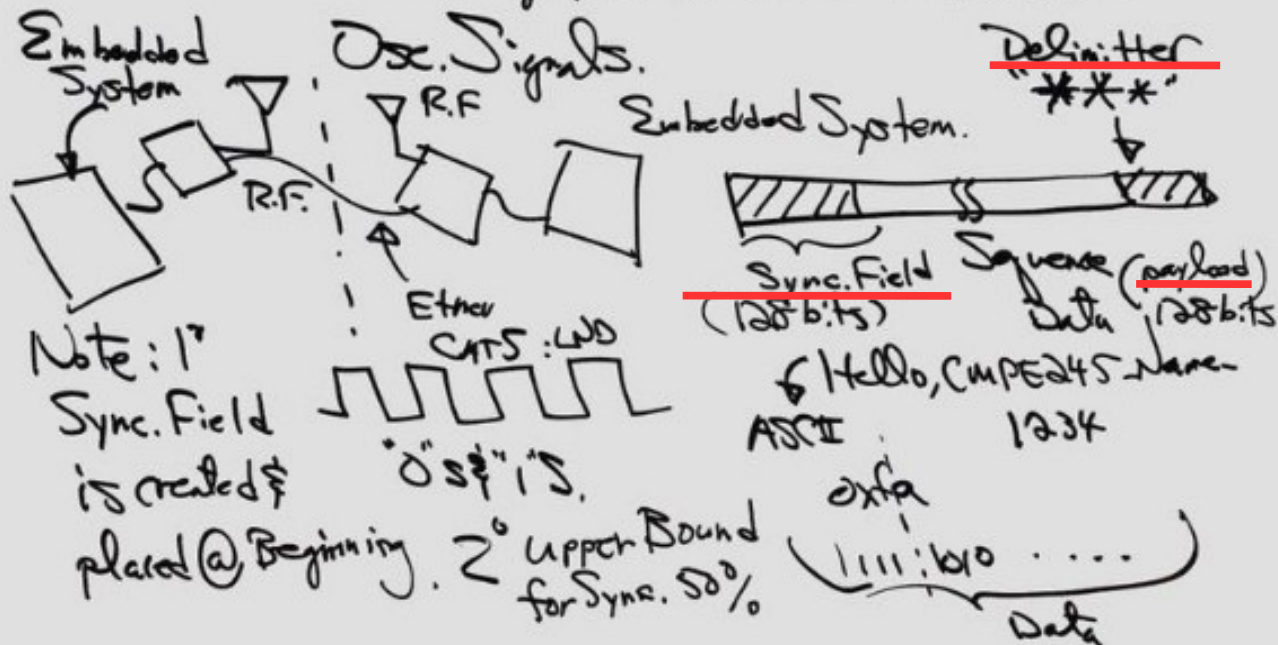
CMPE245 Embedded Wireless Sept. 10, 18
 Harry Li y.

Today's Topics:

1° Sync. Algorithm Design & Prototyping. S/N

Check github/hualili

Example: To Establish Handshaking B/W
 N_i & N_j , From D.B.3: Generate



9-9-2018 LISA Algorithm

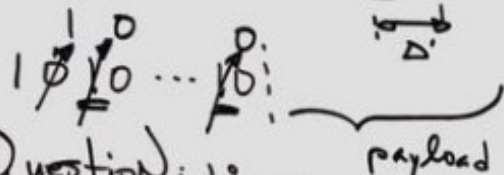
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Step 3. Parse the Payload. By look-up table (ASCII)

Step 4. Check Delimiter "4*" Symbols.

3°



Question: 1°

R_x to Decode

N_i Sync field.

How to Detect Corruption Bit(s) in the Sync Field? Parity?

Step 1: t = 0 Communication Starts.

Step 2: N_i(R_x) listens to N_i via SPP_x(R_x) → Tx

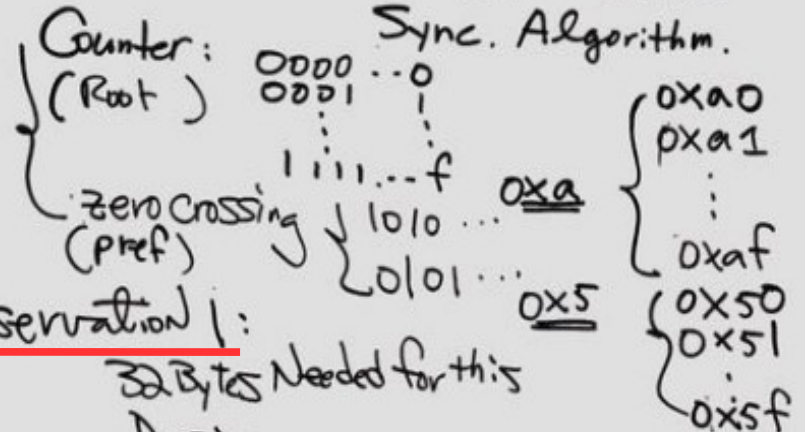
Form N_i Detect "1010...10 (Total 128 bits)".

Once done, then the next bit (129th) is the starting bit of the payload.

Diagram showing a sequence of bits: 1, 0, 1, 0, ..., 1, 0.

Question: How to handle Corruptions in the Sync Field? yet to be able to pinpoint to the Starting bit of the Payload?

Counter → 129th Bit → "LISA" LINEAR Invariant Sync. Algorithm.

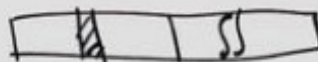


Observation 1:

32 Bytes Needed for this

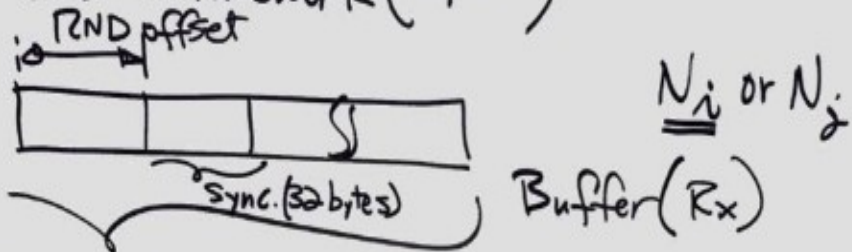
Observation 2: Design. minimum Byte for Sync is 1.

Observation 3: ~ whose Confidence level is 1/32.



9-12-2018 LISA Implementation

1° LISA Homework (C/C++)



- ① mytestdata.txt 2k bits
Size (2k bits) $2048 = 2 \cdot 2^{10}$
e.g. 256 Bytes 2^3 (unsigned 8 bits) 2^8
- ② Random offset 2^3 (unsigned 8 bits)
- ③ payload, SSU-CMPE245_Harry_1234 256 Bytes 2^8
- ④ Sync Field w/ Random Corruption Per User Input
- ⑤ User defines Confidence level for Sync. decoding
Hint: "Mask" (perfect Sync. Pattern) 0xA0

Algorithm (LISA Implementation):

- 1° Define A mask. e.g. 0xA0, for Example;
- 2° Start from the 1st bit of the Data buffer,
Perform bitwise pattern matching. If matched,
then the sync. is established w/ Confidence Level $\frac{1}{32}$. If No matching, Shift to the right 1 bit,
repeat the matching Process. (bit wise matching)
till the matching Pattern found or the end of the
buffer Reached;
- 3° Start New Mask, e.g. 0xA1, and continue this
process as the above till all the masks checked
And/or the Confidence Level reached.

9-17-2018 LISA C/C++ Implementation

Table 1. Sync Bytes vs. Confidence Level, vs. QI index

32 Bytes Sync Field			Q.I
No. of Byte Matched	Confidence Level	Resource Needed	Quality of Comm.
1	$\times 32$		(Design Part.)
2	$2 \times$		
3	\vdots		
\vdots			
32	(100%) $\times 32 \times 32$		

LISA For Sync Adaptation

Define QI (quality index) between Node i and Node j communication.

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Example: "LISA" Implementation.

Diagram illustrating the LISA implementation for sync adaptation:

- Buffer (2K bits)**: Contains offset RND 123 bits, Sync 32 Bytes, and Payload.
- Mask (kernel)**: A mask used for the sync field.
- As "Sync"**: A box representing the sync field.
- Increment of $\frac{1}{32}$ Each**: A step in the process.
- Systematic way to Increase C.L. (Confidence Level)**: A step in the process.
- Smallest mask**: A step in the process.
- Locality**: A step in the process.
- Consecutive Pattern (8 bits)**: A step in the process.
- Non-Consecutive**: A step in the process.

Examples of masks and patterns:

- Mask (kernel)**: $0xA0, 0xA1, \dots, 0xAF, 0x50, 0x51, \dots, 0x5F$
- Consecutive Pattern (8 bits)**: $0xA0, 0xA1, \dots, 0xAF$
- Non-Consecutive**: $0xA0, 0x50, \dots, 0x5F$

9-17-2018 C/C++ Sync Implementation

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10100000
offset: 13 bits
Sync 0xAP 1010,0000
Starting bit of Payload

"for" Loop. for-loop 1: to slide the mask
 $(i=0; i \leq \text{upperBd}; i++)$
 $\{$
 mask
for-loop 2: to check each bits of the mask w/Buffer.
 $(j=0; j \leq \text{upperBdm}; j++)$
 $\}$

Today's Topics:
 LPC NoD. → ① LND Testing.
 NXP IDE. GPIO OP CATS → $N_i \neq N_j$ → Timing
 Time Line: GPIO OP CATS → $N_i \neq N_j$ → Timing
 By Next Monday. Embedded System Ready. ① I/O testing. EXINT INT Timer
 Embedded System → R.F. → LPC → NXP → RISC ARM Cortex M3 → Compiler HAT IDE

Loop-1 (use for loop) to situate (slide) the kernel (mask)

Loop-2 (use for loop) to check the matching of the kernel (mask) with the data from the buffer