# Computer-Aided VLSI System Design
# Lab9: High-Level Synthesis

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## Introduction

In this lab, you will synthesize an image filter using Stratus High-Level Synthesis. Three different circuits will be synthesized for the image filter with different configurations.

## Environment Setup

1. Upload **image_filters_1210.tgz** to your working directory

2. Unpack the data:

   % **tar -zxf image_filters_1210.tgz**

3. Enter Lab9 directory:

   % **cd image_filters**

4. Source the licenses:

   % **source /usr/cad/cadence/CIC/xcelium.cshrc**

   % **source /usr/cad/cadence/CIC/stratus.cshrc**

5. Follow the instructions and answer the questions in **homework_clean_1210.docx**

## Checkpoints

1. Answer all the questions (10 questions) in **homework_clean_1210.docx**, and export the Microsoft Word file into a PDF file.

## Submission

1. **Due Tuesday, Dec. 10, 19:00. No delay is allowed.**

2. Selected students need to accomplish the PDF file in the previous section, and submit it to NTU COOL.
   **Title: CVSD_Lab9_studentID (E.g. CVSD_Lab9_r12943008.pdf)**