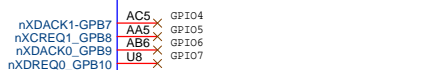
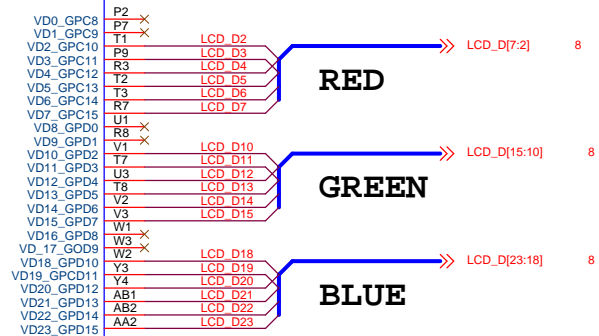


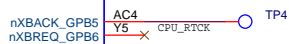
LCD



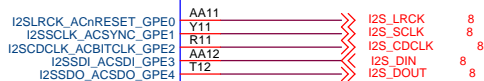
CAM



DMA

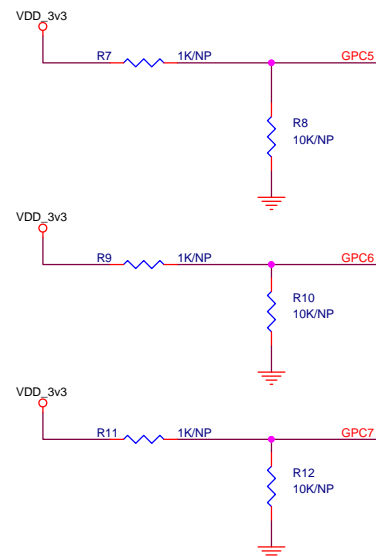


Audio



5 GPC5/6/7 PIN CONFIGURATION TABLE IN IROM BOOT MODE

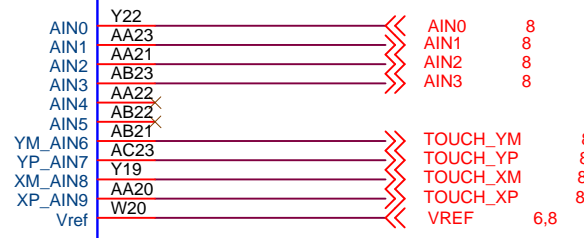
	Page	Address Cycle	GPC7 [2]	GPC6 [1]	GPC5 [0]
MMC(MoviNAND/iNand)	-	-	0	0	0
Reserved	-	-	0	0	1
Nand	512	3	0	1	0
		4	0	1	1
	2048	4	1	0	0
		5	1	0	1
	4096	5	1	1	0



U10E
S3C2450

S3C2450 Peripheral II

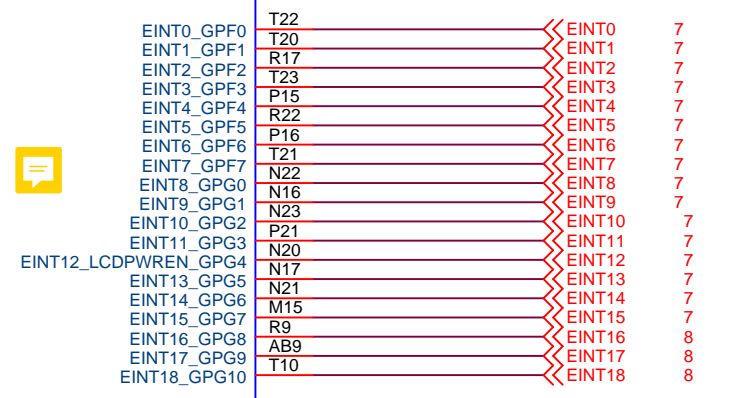
ADC



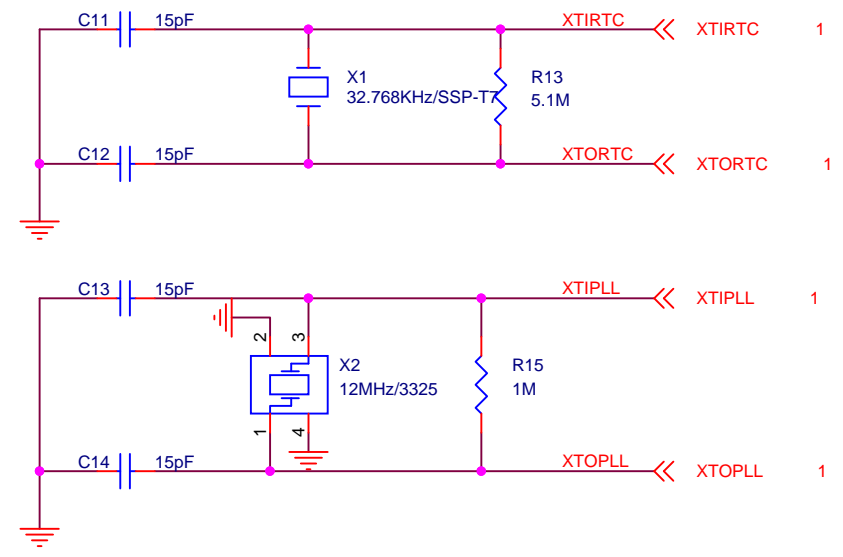
PWM



EINT

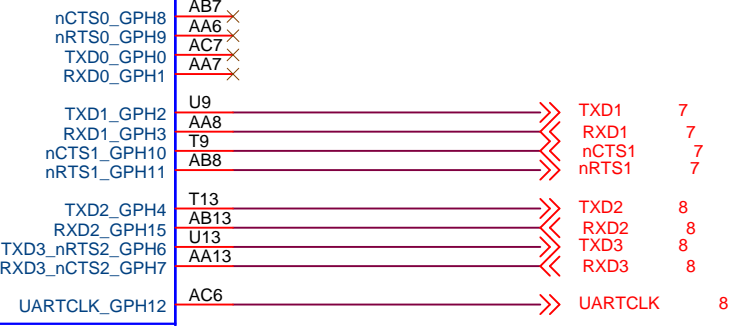


XTAL

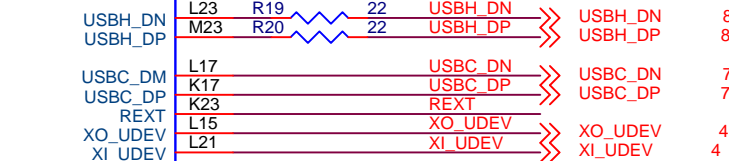


S3C2450 Peripheral III

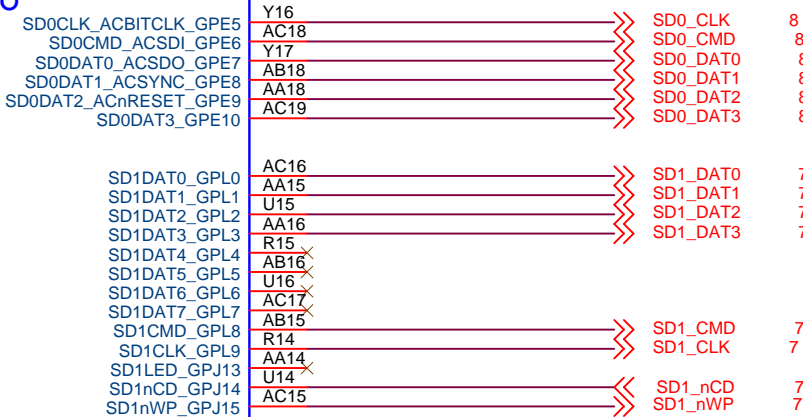
SERIAL



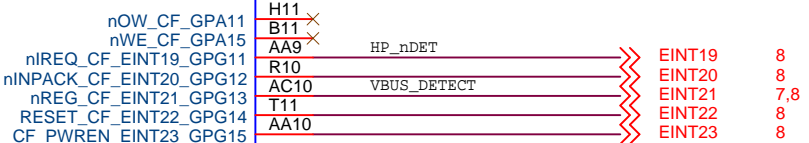
USB



SDIO



CF



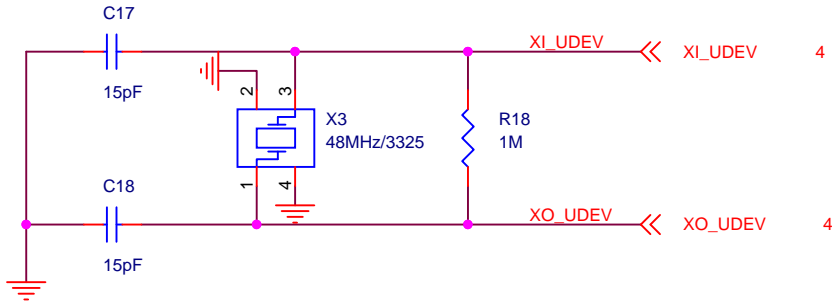
SPI



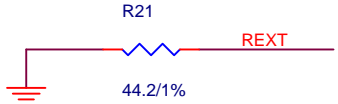
I2C



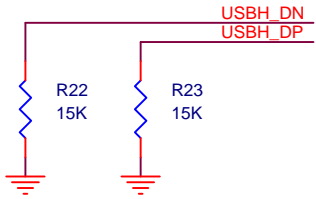
XTAL

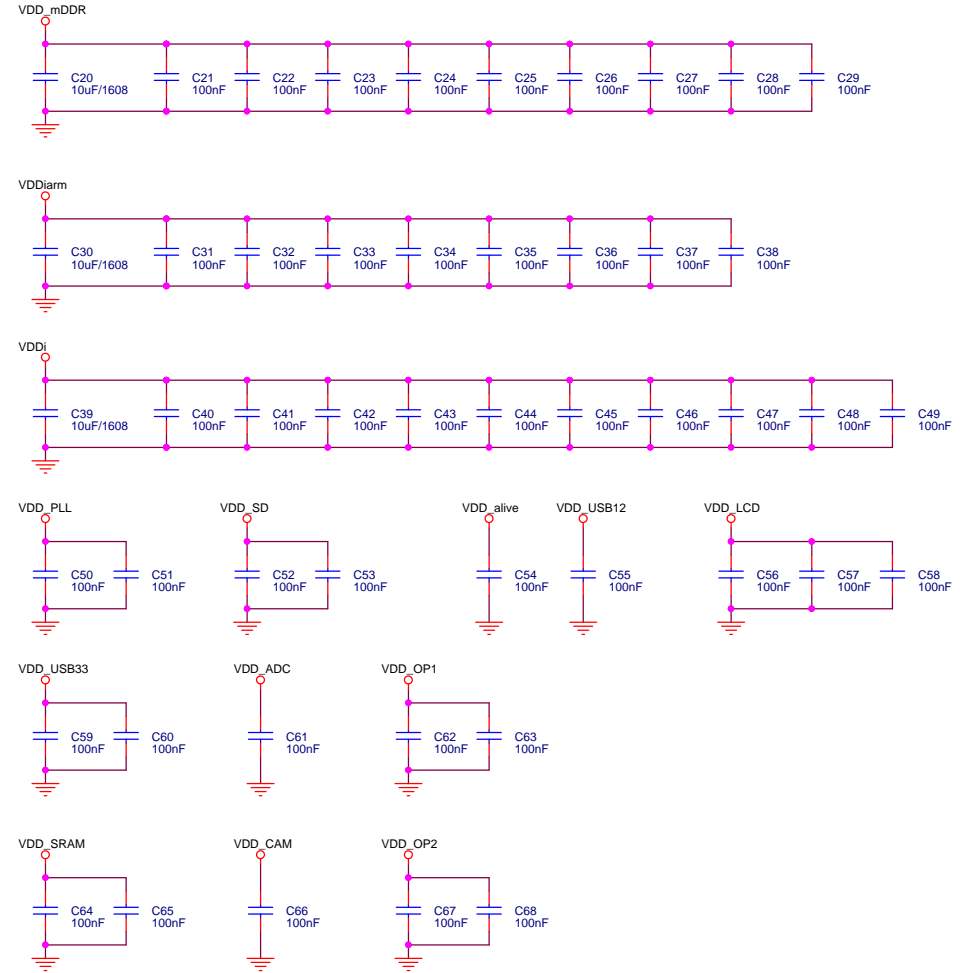
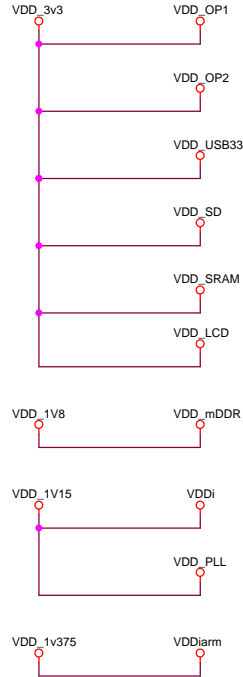
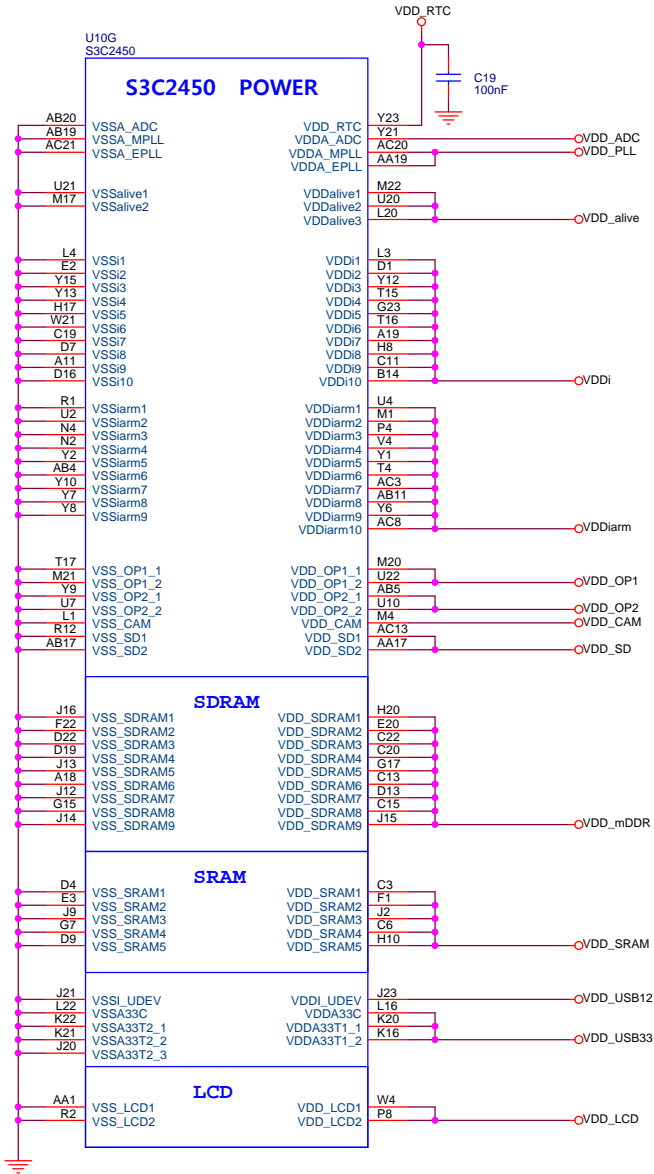


REXT RESISTOR



USB HOST RESISTOR

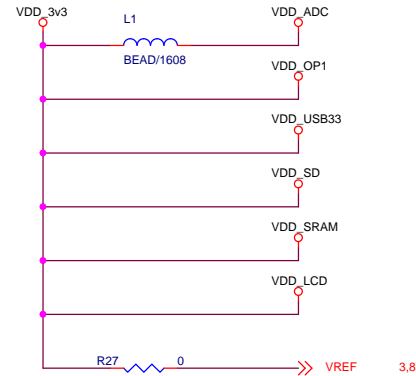
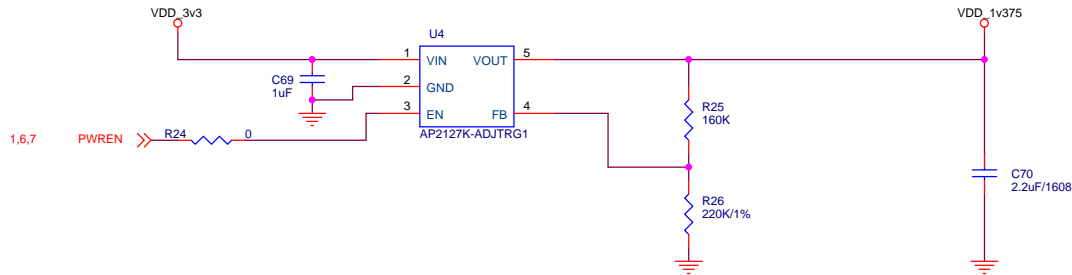




VDDarm

1.375V

1.4V

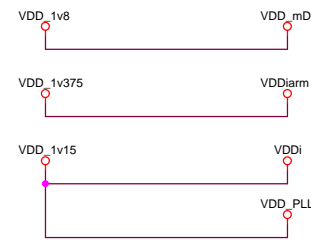
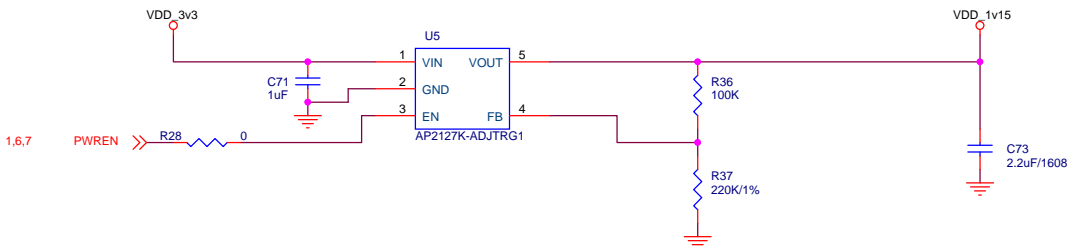


VDD_ADC	3.3V
VDD_OP1	3.3V
VDD_OP2	3.3V
VDD_USB33	3.3V
VDD_CAM	3.3V
VDD_SD	3.3V
VDD_SRAM	3.3V
VDD_LCD	3.3V
VDD_RTC	3.3V
VDD_USB33	3.3V
VDD_mDDR	1.8V
VDDarm	1.375V
VDDalive	1.2V
VDD-_USB12	1.2V
VDDi	1.15V
VDD_PLL	1.15V

VDDi / VDD_MPLL / VDD_EPLL

1.15V

1.2V

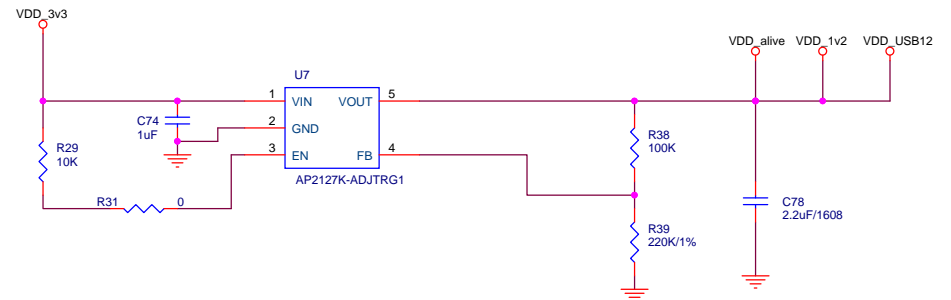
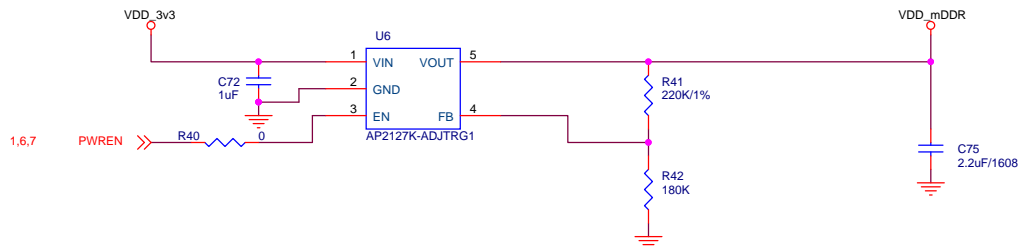


Memory

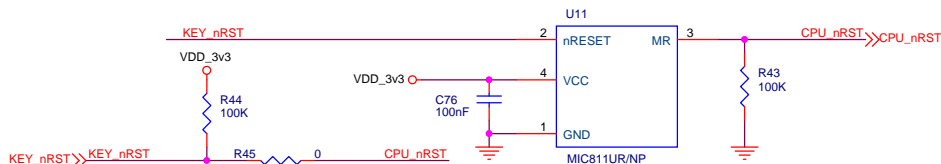
1.8V

ALIVE-BLOCK POWER

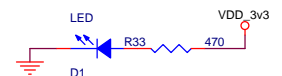
1.2V



Reset IC

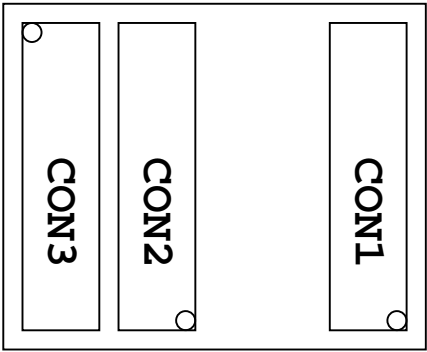
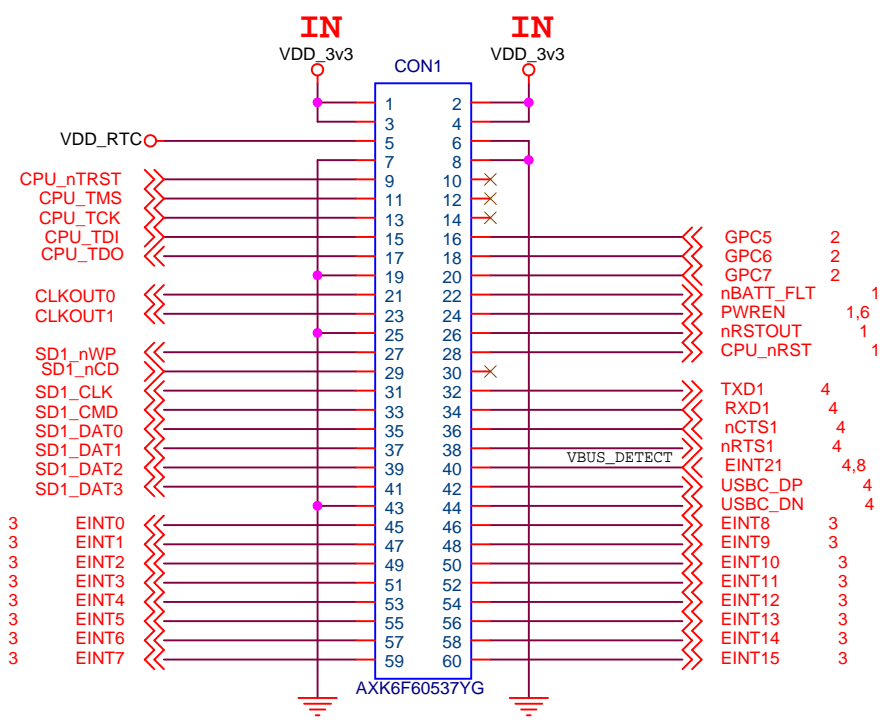


Main POWER 3.3V

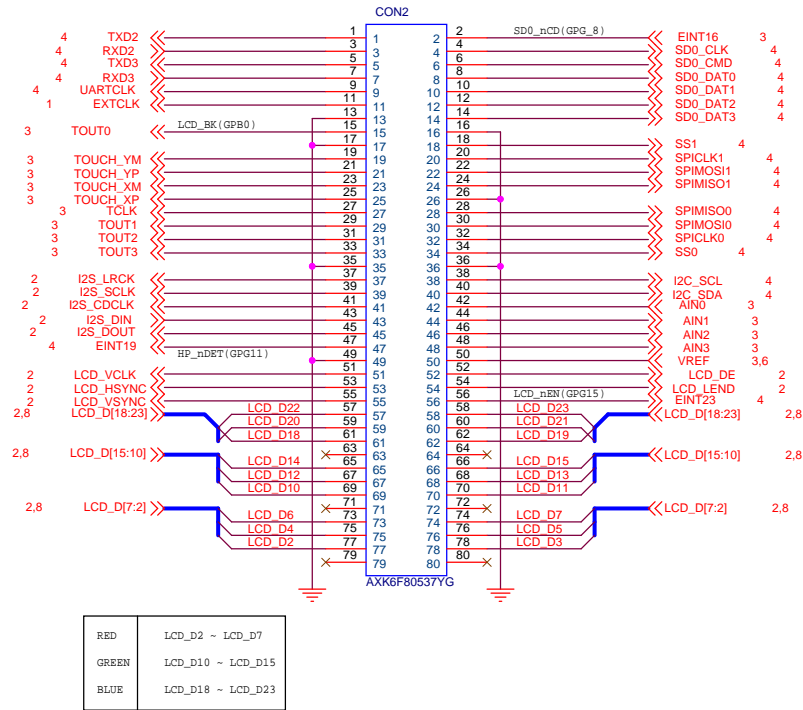


Title	CPU MODULE		
Size	Document Number	Rev	
A3	POWER	0.3	
Date:	Tuesday, February 18, 2014	Sheet	6 of 8

EXPANSION CONNECTOR 1



EXPANSION CONNECTOR 3



EXPANSION CONNECTOR 2

