Q7

msm8939-pinctrl.dtsi

spi5\_active {

/\* MOSI, MISO, CLK \*/

qcom,pins = <&gp 19>;

qcom,num-grp-pins = <1>;

qcom,pin-func = <1>;

label = "spi5-active";

/\* active state \*/

spi5\_default: spi5\_default {

drive-strength = <16>; /\* 16 MA \*/

bias-disable = <0>; /\* No PULL \*/

};

};

spi5\_suspend {

/\* MOSI, MISO, CLK \*/

qcom,pins = <&gp 19>;

qcom,num-grp-pins = <1>;

qcom,pin-func = <0>;

label = "spi5-suspend";

/\* suspended state \*/

spi5\_sleep: spi5\_sleep {

drive-strength = <2>; /\* 2 MA \*/

bias-disable = <0>; /\* No PULL \*/

};

};// huansen by add qimon 2016.11.30

127行增加

backboard\_uart\_sleep {

qcom,pins = <&gp 0>, <&gp 1>;

qcom,num-grp-pins = <2>;

qcom,pin-func = <2>;

label = "uart-backboard";

backboard\_uart\_sleep: uart-backboard {

drive-strength = <2>;

bias-pull-down;

};

}; //huansen by add qimon 2016.11.30

1516行

docking\_detect\_pin {

qcom,pins = <&gp 17>;

qcom,num-grp-pins = <1>;

qcom,pin-func = <0>;

label = "docking\_detect";

docking\_detect\_pin: default {

drive-strength = <16>;

input-high;

bias-pull-up;

};

};//huansen by add qimon 2016.11.30

msm8939-common.dtsi

增加：1178行

blsp1\_uart1: serial@78af000 {

compatible = "qcom,msm-lsuart-v14";

reg = <0x78af000 0x200>;

interrupts = <0 107 0>;

clocks = <&clock\_gcc clk\_gcc\_blsp1\_uart1\_apps\_clk>,

<&clock\_gcc clk\_gcc\_blsp1\_ahb\_clk>;

clock-names = "core\_clk", "iface\_clk";

status = "disabled";

qcom,gpio\_ldo\_pin = <&msm\_gpio 17 0>;

};//huansen by add qimon

msm8939-mtp.dtsi

增加 282行

&blsp1\_uart1 {

status = "ok";

pinctrl-names = "default";

pinctrl-0 = <&backboard\_uart\_sleep>;

}; //huansen by add qimon

msm\_serial\_hs\_lite.c

增加 1825行

#if 1 //huansen by add qimon

gpio\_ldo\_pin = of\_get\_named\_gpio(pdev->dev.of\_node, "qcom,gpio\_ldo\_pin", 0);

if (gpio\_ldo\_pin < 0)

printk("xcz gpio\_ldo\_pin is not available \n");

gpio\_request(gpio\_ldo\_pin, "gpio\_ldo\_pin");

gpio\_direction\_output(gpio\_ldo\_pin, 1);

gpio\_set\_value(gpio\_ldo\_pin, 1);

#endif

qimon\q7\original\m939s550\_2144\_ap\device\qcom\sepolicy\common\file\_contexts

增加25行

/dev/ttyHSL1 u:object\_r:serial\_device:s0

qimon\q7\original\m939s550\_2144\_ap\device\qcom\common\rootdir\etc\ueventd.qcom.rc

增加156行

/dev/ttyHSL1 0777 root root

MSM8939-MTD.dtsi

20161223

最后加

&usb\_otg {

qcom,hsusb-otg-mode = <3>;

qcom,usbid-gpio = <&msm\_gpio 110 0>;

qcom,usbdpdn-switch = <&msm\_gpio 16 0>;

simcom,backboard-int = <&msm\_gpio 28 0>;

simcom,backboard-5v = <&msm\_gpio 84 0>;

pinctrl-names = "default";

pinctrl-0 = <&usbid\_default>;

vbus\_otg-supply = <&smb1360\_otg\_supply>;

};