

HUIHUI HUANG

Southern University of Science and Technology – Shenzhen – China

☎ +86 18028793998 • ✉ huanghh2020@mail.sustech.edu.cn

🌐 github.com/huanghuihui0904

Education

Undergraduate Education

Southern University of Science and Technology
B.E. in Computer Science and Engineering
Member of ARiSE, supervised by [Prof. Yuqun Zhang](#)

Sept. 2020 - Present
Shenzhen, Guangdong

Honors

The Second Class Scholarship of the University	Nov. 2021 & Nov. 2022
Bronze Award of Huawei Kunpeng Innovation Application Competition	Nov. 2022
The Second prize in China Undergraduate Mathematical Contest in Modeling	Sept. 2022
Outstanding Student Leaders of the University	May. 2022

Academic Experience

Binary Code Similarity Detection Based on Natural Language Processing

Keywords: Disassembly, NLP, Function Call Graph, Binary Similarity Feb. 2023 - Present

- Studied state-of-the-art binary similarity detection techniques and binary diffing methods.
- Evaluating state-of-the-art baseline techniques with our testing sets.
- Exploring innovation points for further research and contributing a publication.

An Extensive Study on Large-scale Software Composition Analysis System

Keywords: SCA, Software Security, Semantic Extraction, Static Analysis Sept. 2022 - Jan. 2023

- Studied state-of-the-art software composition analysis systems extensively.
- Conducted experiments to evaluate the accuracy of third-party library dependency extraction of [CENTRIS](#).
- Proposed a new algorithm to improve CENTRIS performance.

Key Projects

Full-stack Chain Hotel Website

Techniques: Spring Boot, Redis, RabbitMQ, PostgreSQL, JMeter, Huawei Cloud Sept. 2022 - Jan. 2023

- A full-stack website that mimics the functionalities of a real-life chain hotel, including a management system, a user booking system, and a beautiful user interface.
- Responsible for back-end, implemented a web server with several features, such as flash sales and time-limited login verification.

Single-cycle CPU Supporting Simple MIPS Instructions

Tools: Vivado, Minisys, Assembler, Mars, Uart Tools Feb. 2022 - Jun. 2022

- Implemented a single-cycle CPU with Verilog language running on the Minisys development board.
- Programed and ran MIPS language on the CPU to realize some basic scenes.

Lisp Interpreter in Java

Realizations: Function, Closure, Lambda Operator, Lazy Evaluation Jun. 2022 - Sept. 2022

- Implemented a Lisp interpreter in Java programming language realizing the above features.

Positions of Responsibility

- **Teaching Assistant** of CS202: Computer Organization Principle Feb. 2023 - Present
- **Teaching Assistant** of CS109: Introduction to Java Programming Sept. 2022 - Jan. 2023
- **President of the Student Union** of Zhixin College Sept. 2021 - Jun. 2022

Technical Strengths

- **Programming Languages:** Java, Python, C/C++, SQL, JavaScript, Verilog, MIPS
- **Frameworks:** Spring Boot, MyBatis, JUnit, Node.js, Vue.js
- **Developer Tools:** GitHub, VS Code, PyCharm, IntelliJ IDEA, CLion