TI BLE Stack CC26xx R2 ROM Release Notes

August 19, 2016

# Description

The full TI BLE stack is comprised of the Host plus Controller, and a set of standard Profiles. The interface between the Host and Controller is called the Host Controller Interface (HCI). The Controller includes the HCI, as well as the Link Layer (LL). The LL provides the basic functionality of the Controller, as well as the PHY interface. The Host provides higher level protocol layers, including L2CAP, GATT, GAP, and ATT.

For the CC26xx platform, components of both the Controller and the Host are placed in the ROM, which includes a minimal C runtime for ROM software initialization. Also built with this ROM image is the ECC interface to licensed third party cryptography software, the source of which has been abstracted from the user. All ROM-to-ROM and ROM-to-Flash subroutine calls and certain data accesses are done via a flash jump table in support of future proofing (i.e. patching).

# Release Information

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| --- | --- | --- | --- | --- | --- |
| Target | CC2640 | | | | |
| Path | git\lprf-sd-rom\ble\_rom\_releases\cc26xx\_r2\Final\_Release | | | | |
| Memory Allocation |  | Start | End | Size/Used | Value/Hex |
| ROM | 0x10004940 | 0x10019AFF | 86464/86442 | NA |
| RAM | 0x200044F8 | 0x20004F7F | 2694/2692 | NA |
| ECC RAM | 0x20004F80 | 0x20004FFF | 128/125 | NA |
| ROM Revision | 0x10019AFC | 0x10019AFD | 2 | 2070/0x0816 |
| Checksum | 0x10019AFE | 0x10019AFF | 2 | 33239/0x81D7 |

# Configuration and Build Tools

IAR Version: V7.50.3

Xilinx Version: V14.5 ISE DS Win

Git Version: V2.5.0.windows.1 - July 24, 2015 (6F9504C)

Git Repositories:

[git@gitorious.design.ti.com:lprf-sd-ble/ble\_core.git](mailto:git@gitorious.design.ti.com:lprf-sd-ble/ble_core.git)

[git@gitorious.design.ti.com:lprf-sd-ble/ble\_examples.git](mailto:git@gitorious.design.ti.com:lprf-sd-ble/ble_examples.git)

[git@gitorious.design.ti.com:lprf-sd-common-components/lprf-sd-common-components.git](mailto:git@gitorious.design.ti.com:lprf-sd-common-components/lprf-sd-common-components.git)

[git@gitorious.design.ti.com:lprf-sd-common-components/lprf-sd-rom.git](mailto:git@gitorious.design.ti.com:lprf-sd-common-components/lprf-sd-rom.git)

[git@gitorious.design.ti.com:lprf-sd-tools/lprf-sd-tools.git](mailto:git@gitorious.design.ti.com:lprf-sd-tools/lprf-sd-tools.git)

# Build Software Information

|  |  |
| --- | --- |
| FPGA Bit File | FW: 569-00, SVN 47050 – August 19, 2016  <https://oslosvn.norway.design.ti.com/svn/srf04/cc26xx/trunk> |
| driverLib | R2\_fpga\_pg\_1\_0, SVN 17286 – May 25, 2016  <https://oslosvn.norway.design.ti.com/svn/satsw/projects/cc26xxware/trunk/r2_fpga_pg_1_0> |
| TI RTOS Software | TIRTOS 2\_20\_01\_07 – July 29, 2016  <http://www.sanb.design.ti.com/tisb_releases/TIRTOS/2_20_01_07/>  BIOS 6\_46\_01\_29 – July 22, 2016  <http://www.sanb.design.ti.com/tisb_releases/BIOS/6_46_01_29/>  TI-DRIVERS 2\_20\_01\_09\_eng – July 28, 2016  <http://www.sanb.design.ti.com/tisb_releases/TI-DRIVERS/2_20_01_09/>  XDCtools 3\_32\_00\_06 – December 17, 2015  <http://www.sanb.design.ti.com/tisb_releases/XDCtools/3_32_00_06/> |