

PREPARE YOUR WORKSPACE

Create a directory for your Eclipse workspace

eg: `c:\users\<USER_NAME>\Documents\<MY_WORKSPACE>`

Go into the above create directory

Run/launch *Atollic-True Studio* from windows start menu

Select the above directory as workspace

eg: `c:\users\<USER_NAME>\Documents\<MY_WORKSPACE>`

A new directory called *.metadata* is create by Atollic. It can be ignored but neither be deleted nor edited.

GET SOURCE TEMPLATE AND CONFIGURE GIT

Right click in the worskpace directory and select **Run Git Bash here** from windows explorer

In the Git bash shell enter the below command to clone the souce code template:

~~`git clone "//PC307-21/distri/_docs/3i - E2i/SPI/TP/SPI_STM32_MESURE_BUS/firmware/Polytech-IESE-E2i-SPI.git" "c:\users\<USER_NAME>\Documents\<MY_WORKSPACE>\Polytech-IESE-E2i-SPI"`~~

`git clone //PC307-21/Distri/_docs/3i\ -\ E2i/SPI/TP/SPI_STM32_MESURE_BUS/firmware/Polytech-IESE-E2i-SPI.git/`

A new directory called *Polytech-IESE-E2i-SPI* appears. This directory contains the source template for the lab session.

Go in the created directory with `cd Polytech-IESE-E2i-SPI` command from git bash command line interface.

Git needs a user name and a user email to commit.

As a user name and email may already created as global. Local name and local email for the project can be used to overwrite the global ones and to enable several users to use git locally

For email, a dummy email can be used eg *email@email.com*.

`git config --local user.email email@email.com`

For the user name, use you "*binome*" name

`git config --local user.name <binome_names>`

To check your info use the below command

`git config --local --list`

Create a branch before to start your work.

`git branch <my_branch>`
`git checkout <my_branch>`

OPEN THE SRC TEMPLATE WITH ATOLLIC - TRUE STUDIO IDE

Close *Information Center* tab

File > Import

Select *General | Existing Projects Into Workspace*

In Select Root Directory field, chose the above directory obtained by git

eg `c:\users\<USER_NAME>\Documents\<MY_WORKSPACE>\Polytech-IESE-E2i-SPI`

Click *Finish*

CHECK THE TEMPLATE CAN BE COMPILED AND DOWNLOADED TO THE TARGET

Select the project in the Project Explorer

Project > Build Project

Make sure the Nucleo board is connected to the PC with its USB cable

Run > Debug (F11)

Eclipse switch to debug perspective

Use F6 (step over) to check step by step execution is working well

Click the red square icon to quit Debug eclipse perspective

You are ready to add your code