



Instruction Set
(number indicates ring counter position)

- 0: Add reg A and reg B and write to reg C
- 1: Reset reg B
- 2: Mov reg A to reg B
- 3: Reset reg A
- 4: Mov reg C to reg A
- 5: Reset reg C

Sets the first bit of
the A register high

SW?
SW_Push

Hayley Hughes

Sheet: /
File: fibonacci.sch

Title: 4 bit Fibonacci Sequence Calculator

Size: A4 Date: 2020-07-30
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