



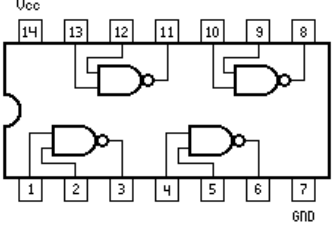
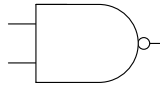
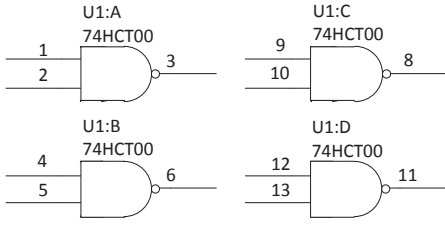
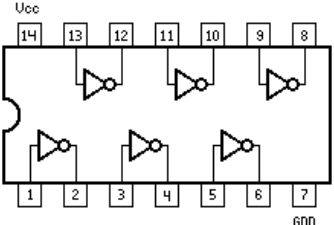
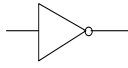
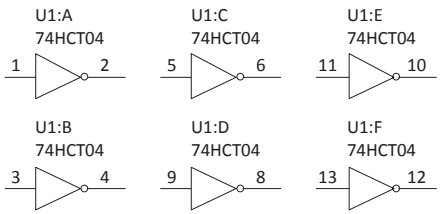
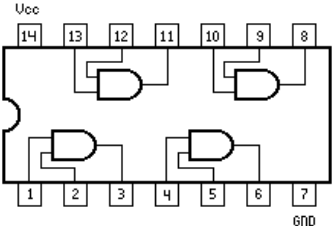
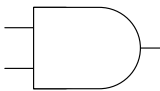
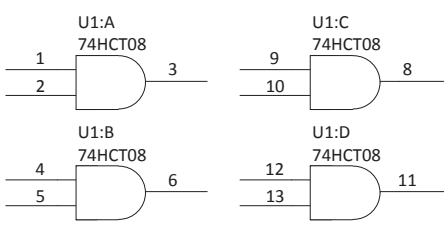
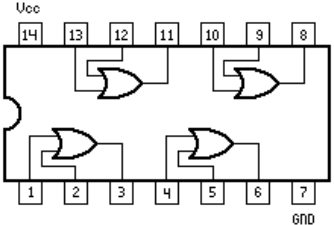

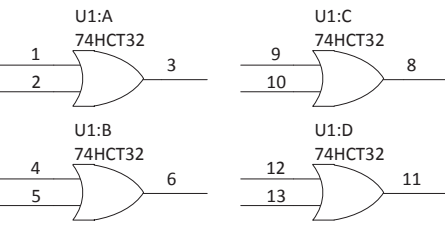
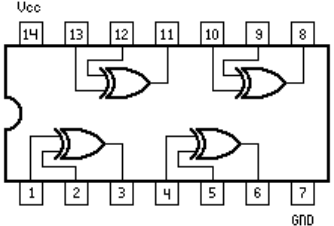

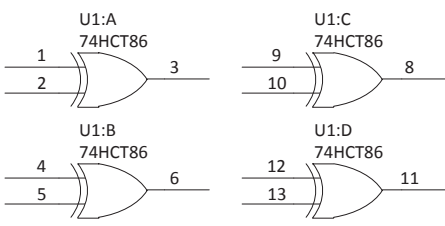
CSSE2010 / CSSE7201 Device Pinouts and Symbols

Power and Ground

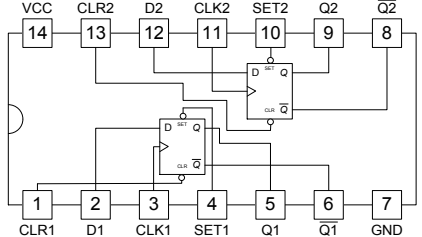
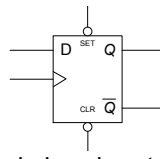
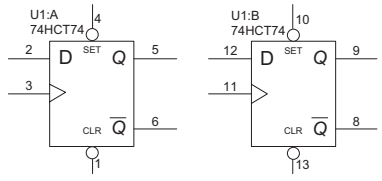
Device ID & Function	Device Pinout	Logic Diagram Symbol	Circuit Schematic Symbol(s)
Logic High / Power		1 ———	VCC └─┘
Logic Low / Ground		0 ———	 or 

Power and ground connections for devices are shown separately. These symbols are used, for example, when connecting a device input to logic low or logic high

Basic Gates

74HCT00 Quad 2-input NAND gate			
74HCT04 Hex Inverter			
74HCT08 Quad 2-input AND gate			
74HCT32 Quad 2-input OR gate			
74HCT86 Quad 2-input XOR gate			

Flip-Flops and MSI Devices

74HCT74 Dual D-type Flip-flop		 (Set and clear inputs can be omitted if not needed)	
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Device ID & Function	Device Pinout	Logic Diagram Symbol	Circuit Schematic Symbol(s)
74HCT157 Quad 2-input Multiplexer		(Several options possible) (Add enable input if needed)	
74HCT175 Quad D-Type Flip-flop			
74HCT283 4 bit full adder			

Inputs and Outputs			
IO Board		<p>Input: A _____</p> <p>Output: _____ X0</p> <p>(Use sensible names)</p>	<p>IO1 IOBOARD</p> <p>B3 12 A 25</p> <p>B2 13 B 26</p> <p>B1 14 C 27</p> <p>B0 15 D 28</p> <p>E 29</p> <p>F 30</p> <p>G 31</p> <p>H 32</p> <p>IO1 IOBOARD</p> <p>17 X0</p> <p>18 X1</p> <p>19 X2</p> <p>20 X3</p> <p>21 X4</p> <p>22 X5</p> <p>23 COUT</p> <p>24 OFLOW</p>