# 基于 RISC-V 的 Type-1 hypervisor 的设计与实现

毕业设计答辩报告

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2023年6月6日





- 1 背景
- 2 设计与实现
- 3 系统运行
- 4 项目进程 & 展望
- **5** Q&A





1 背景

背景 ●000

- 3 系统运行
- 4 项目进程 & 展望



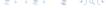


背景

# • 为什么需要虚拟化?

- 数据中心的数量越来越庞大,需要使用虚拟化技术实现数据中心统一管理。
- 物联网技术的发展,需要虚拟化技术来对物联网系统做安全保障。
- 为什么要在 RISC-V ISA 上做虚拟化?
  - RISC-V 目前是最流行的开源 ISA, 在不到十年时间里全世界 芯片出货量超过 100 亿颗。
  - RISC-V 为国内突破卡脖子技术的关键技术,国内学术界和工业界都在积极构建基于 RISC-V 架构的产品与社区。





# RISC-V: the ISA

背景

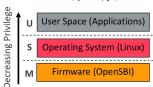
- RISC-V 是一个开放标准 ISA
  - 在 2010 年首次被伯克利开发
  - 与商业/私有标准相区别: x86/ARM
- RISC-V 传统特权级
  - M(Machine)/S(Supervisor)/U(User)
- RISC-V 虚拟化扩展
  - 在 2021 年 11 月正式被批准
  - S(Supervisor) -> HS(Hypervisor Supervisor)/VS(Virtual Supervisor)





#### RISC-V: the ISA

#### RISC-V 传统特权级



# RISC-V Hypervisor Extension 特权级







- ② 设计与实现 Hypocaust 设计与实现 Hypocaust-2 设计与实现
- 3 系统运行
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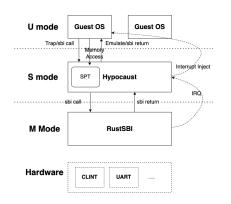
Hypocaust 设计与实现

- 1 背景
- 2 设计与实现 Hypocaust 设计与实现





# Hypocaust: Design



设计与实现

- CPU 虚拟化: S mode 陷入 与模拟
- 内存虚拟化:影子页表技术
- 中断虚拟化: PLIC 模拟与 中断转发
- IO 虚拟化:设备透传
- 系统运行: minikernel





项目进程 & 展望

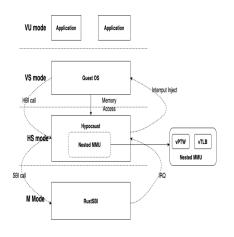
- 1 背景
- 2 设计与实现 Hypocaust-2 设计与实现





Hypocaust-2 设计与实现

# Hypocaust-2: Design



- 基干设备树的配置
- RISC-V Hypervisor Extension 辅助虚拟化
- 两阶段页表翻译
- 异常代理与中断转发(PLIC 模拟)
- 设备透传
- 可运行 rCore-Tutorial-v3, RT-Thread 以及 Linux mainline





- 1) 背景
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# Capable of booting system

hypocaust-2 启动 RT-Thread

```
ustsbi] Implementation
                               RustSBI-QEMU Version 0.2.0-alpha.2
 rustsbi] Platforn Name
                              riscv-virtio,genu
rustsbil Platform SMP
 rustsbij Platform Memory
                            : 0x80800800..0x148608608
 rustsbi] Boot HART
 rustsbil Device Tree Region : 0xbfe00000..0xbfe00f85
rustsbil Firmware Address : 8x88888888
 rustsbi] Supervisor Address : 0x80200000
 rustsbi] pmp01: 0x00000000..0x80000000 (-wr)
 rustsb1] pmp82: 0x80080080..0x80208080 (---)
 rustsbi] pmp03: 0x80200000..0x140000000 (xwr)
rustsbil pmp84: 0x140800800..0x80880080 (-wr)
Hypervisor] Hello Hypocaust-2!
Hypervisor] hart id: 0, dtb: 0xbfe80080
Hypervisor| Hypocaust-2 > running with hardware RISC-V H ISA acceration!
Hypervisor| Heap initialize finished!
Hypervisor] test addr: 0x100000, size: 0x1000
Hypervisor] UART addr: 0x10000000, size: 0x100
Hypervisor | CLINT addr: 0x2000000, size: 0x10000
Hypervisor] PLIC addr: 0xc000000, size: 0x600000
Hypervisor] Initialize hypervisor environment
Hypervisor| Guest root page table: 0x8082c
Hypervisor] va: [0x80200000: 0x802829d0)
Hypervisor] quest va -> [0x80200000: 0x80200000), quest pa -> [0x80200000: 0x90200000)
Hypervisor| Hypervisor enable paging!
Hypervisor] remap test_passed!
Hypervisor] allocated hstack: [0xffffffffffffa800: 0xffffffffffff6000)
Hypervisor] Switch to guest.....
Tracking] write PLIC threshold reg, addr: 0xc201000, value: 0x0
Tracking | write PLIC threshold reg, addr: 0xc201000, value: 0x0
neap: [0x802829d0 - 0x842829d0]
          Thread Operating System
         5.8.8 bulld Mar 4 2823 15:19:41
2086 - 2022 Copyright by RT-Thread team
lwIP-2.0.3 initialized!
Hello RISC-V
msh />
```

hypocaust-2 启动主线 Linux

```
0.460370] remote fence extension is not available in SBI v1.0
     0.460518] remote fence extension is not available in SBI v1.0
     0.460631] remote fence extension is not available in SBI v1.0
     0.460739] remote fence extension is not available in SBI v1.0
     0.4608487 remote fence extension is not available in SBI v1.0
     0.4609587 remote fence extension is not available in SBI v1.0
     0.461067] remote fence extension is not available in SBI v1.0
     0.461175] remote fence extension is not available in SBI v1.0
     0.4612837 remote fence extension is not available in SBI v1.0
     0.461391] remote fence extension is not available in SBI v1.0
    0.461499] remote fence extension is not available in SBI v1.0
     0.461607] remote fence extension is not available in SRI v1.0
    0.461714] remote fence extension is not available in SBI v1.0
    0.461822] remote fence extension is not available in SBI v1.0
     0.461929] remote fence extension is not available in SBI v1.0
Please press Enter to activate this console.
 # 1s
     4.1643737 remote fence extension is not available in SBI v1.0
     4.164625] remote fence extension is not available in SBI v1.0
     4.167375] remote fence extension is not available in SBI v1.0
     4.167555] remote fence extension is not available in SBI v1.0
     4.167715] remote fence extension is not available in SBI v1.0
     4.1678727 remote fence extension is not available in SBI v1.0
     4.1680297 remote fence extension is not available in SBI v1.0
     4.1681867 remote fence extension is not available in SBI v1.0
     4.1683427 remote fence extension is not available in SBI v1.0
     4.168497] remote fence extension is not available in SBI v1.0
     4.168651] remote fence extension is not available in SBI v1.0
     4.168805] remote fence extension is not available in SBI v1.0
     4.168962] remote fence extension is not available in SBI v1.0
     4.169119] remote fence extension is not available in SBI v1.0
     4.1692777 remote fence extension is not available in SBI v1.0
     4.1694327 remote fence extension is not available in SBI v1.0
                                                test
           linuxrc
 # echo Hello World
Hello World
```





- 1) 背景

- 4 项目进程 & 展望





#### Status&Future Works

#### 项目进程

- 基于 hypocaust/hypocasut-2 的经验, 开发了 hypercraft(https://github.com/KuangjuX/hypercraft), 一个 VMM 库, 目前适配了由清华大学开发的 ArceOS 并可以类 似 KVM 作为 Type-2 hypervisor 启动
- 搭建了一个基于 rocket-chip 带 H Extension 的软核 RISC-V SoC, 并正在尝试将 hypocasut-2/hypercraft 跑在 FPGA 上。

#### 未来展望

- 扩展为多核/多 guest
- 实现 RISC-V AIA/IOMMU 驱动以提高性能和安全性
- 等待真实支持硬件虚拟化的 RISC-V 的芯片并移植



Q&A



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Q&A

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Thanks!

