Huize Li

+1-407-590-0278 | huize.li@ucf.edu | Personal Homepage

LinkedIn Page | Github | Google Scholar | ResearchGate | ORCID

Orlando, FL - 32825, USA

Research Interests

I am currently working on machine learning accelerators, Transformer and sparse attention, in-memory computing, in-situ computing, and domain specific accelerators. I am also desired to explore photonic computing and hyper-dimensional computing.

Education

- **Huazhong University of Science and Technology []** *Sept 2017 - Dec 2022*
  Ph.D. in Engineering Wuhan, China
  - Major: Computer Architecture
  - Mentors: Professor Hai Jin, IEEE Fellow (email: hjin@hust.edu.cn).
- **Huazhong University of Science and Technology []** *Sept 2013 - Jun 2017*
  B.S. in Engineering Wuhan, China
  - Major: Software Engineering.
  - GPA: 3.5/4.0, School number: 027-87541114.

Teaching Experience

- **Huazhong University of Science and Technology []** *Fall 2017*
  Teaching Assistant, Graduate Programs: Parallel Processing Wuhan, China
  - Duty: Preparing, guiding, and evaluating semester research projects.
- **Huazhong University of Science and Technology []** *Fall 2019*
  Teaching Assistant, Graduate Programs: Advanced Computer Architecture Wuhan, China
  - Duty: Preparing, guiding, and evaluating semester research projects.

Employment

- **Department of Electrical and Computer Engineering in University of Central Florida []** *Feb 2025 - ongoing*
  Postdoctoral Researcher Orlando, FL, USA
  - Mentors: Professor Xin Xin (email: xin.xin@ucf.edu).
  - Duty: I do researches and projects in memory reliability and processing in memory architecture.
- **School of Computing in National University of Singapore []** *Feb 2023 - Feb 2025*
  Postdoctoral Research Fellow Singapore
  - Mentors: Professor Tulika Mitra (email: tulika@comp.nus.edu.sg).
  - Duty: I do researches and projects in design high performance and energy-efficient accelerator for sparse Transformer.

Publications

C=Conference, J=Journal, P=Patent, S=In Submission, T=Thesis

[T.1] **Huize Li**. (2022). Processing-in-Memory Architecture Based Structured Query Accelerators. Ph.D. Thesis.

[C.5] **Huize Li**, Dan Chen, and Tulika Mitra. (2025). HyAtten: Hybrid Photonic-digital Architecture for Accelerating Attention Mechanism. *Design, Automation, and Test in Europe (DATE)*, Just Accepted.

[C.4] **Huize Li**, Zhaoying Li, Zhenyu Bai, and Tulika Mitra. (2024). ASADI: Accelerating Sparse Attention using Diagonal-based In-situ Computing. In *Proceedings of the 30th IEEE International Symposium on High-Performance Computer Architecture (HPCA)*, pp. 774-787.

[C.3] Zhenyu Bai, Pranav Dangi, **Huize Li**, and Tulika Mitra. (2024). SWAT: Scalable and Efficient Window Attention-based Transformers Acceleration on FPGAs. In *Proceedings of the 61th ACM/IEEE Design Automation Conference (DAC)*, pp. 93:1-93:6.

[C.2] **Huize Li**, Hai Jin, Long Zheng, Yu Huang, Xiaofei Liao, Zhuohui Duan, Dan Chen, and Chuangyi Gui. (2022). ReSMA: accelerating approximate string matching using ReRAM-based content addressable memory. In *Proceedings of the 59th ACM/IEEE Design Automation Conference (DAC)*, pp. 991-996.

[C.1] Cong Liu, Haikun Liu, Hai Jin, Xiaofei Liao, Yu Zhang, Zhuohui Duan, Jiahong Xu, and **Huize Li**. (2022). ReGNN: a ReRAM-based heterogeneous architecture for general graph neural networks. In *Proceedings of the 59th ACM/IEEE Design Automation Conference (DAC)*, pp. 469-474.

[J.7] **Huize Li**, Dan Chen, and Tulika Mitra. (2025). SPLIM: Bridging the Gap Between Unstructured SpGEMM and Structured In-situ Computing. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (IEEE TCAD)*, Just Accepted.

[J.6] **Huize Li**, Dan Chen, and Tulika Mitra. (2024). SADIMM: Accelerating Sparse Attention using DIMM-based Near-memory Processing. *IEEE Transactions on Computers (IEEE TC)*, Just Accepted.

[J.5] **Huize Li**, Hai Jin, Long Zheng, Yu Huang, Xiaofei Liao, Dan Chen, Zhuohui Duan, Cong Liu, Jiahong Xu, and Chuanyi Gui. (2024). CPSAA: Accelerating Sparse Attention using Crossbar-based Processing-In-Memory Architecture. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (IEEE TCAD)*, 43 (6), pp. 1741-1754.

[J.4] Jiahong Xu, Haikun Liu, Zhuohui Duan, Xiaofei Liao, Hai Jin, Xiaokang Yang, **Huize Li**, Cong Liu, Fubing Mao, and Yu Zhang. (2024). ReHarvest: an ADC Resource-Harvesting Crossbar Architecture for ReRAM-Based DNN Accelerators. *ACM Trans. Archit. Code Optim. (ACM TACO)*, 21 (3), pp. 1-26.

[J.3] Cong Liu, Kaibo Wu, Haikun Liu, Hai Jin, Xiaofei Liao, Zhuohui Duan, Jiahong Xu, **Huize Li**, Yu Zhang, and Jing Yang. (2024). A ReRAM-Based Processing-In-Memory Architecture for Hyperdimensional Computing. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (IEEE TCAD)*, Just Accepted.

[J.2] **Huize Li**, Hai Jin, Long Zheng, Yu Huang, and Xiaofei Liao. (2022). ReCSA: a dedicated sort accelerator using ReRAM-based content addressable memory. *Frontiers of Computer Science (FCS)*, 17: 172103.

[J.1] **Huize Li**, Hai Jin, Long Zheng, and Xiaofei Liao. (2020). ReSQM: Accelerating Database Operations Using ReRAM-Based Content Addressable Memory. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (IEEE TCAD)*, 39 (11), pp. 4030-4041.

Professional Services

Reviewer
Reviewer of IEEE TC.

Services

Talks

ASADI: Accelerating Sparse Attention using Diagonal-based In-situ Computing. *HPCA 2024*.
ReSMA: accelerating approximate string matching using ReRAM-based content addressable memory. *DAC 2022*.
ReSQM: Accelerating Database Operations Using ReRAM-Based Content Addressable Memory. *CODES+ISSS 2020*.

References

1. **Hai Jin**
   Professor, Department of Computer Science
   Huazhong University of Science and Technology
   Email: hjin@hust.edu.cn
   Relationship: [Ph.D. Advisor]

2. **Tulika Mitra**
   Professor, School of Computing
   National University of Singapore
   Email: tulika@comp.nus.edu.sg
   Relationship: [My Postdoc Mentor]