

EASYLIFE.RESOURCES
COMPUTING

```
graph LR; A[Input A] --> AND[AND Gate]; B[Input B] --> AND; AND --> C[Output C]; C --> OR[OR Gate]; OR --> P[Output P];
```

A	B	C	P

The diagram shows a logic circuit. On the left, there are two input lines labeled "Input A" and "Input B". These lines enter an AND gate, which is a blue D-shaped symbol. A single line labeled "Output C" exits the right side of the AND gate. This line then enters a NOT gate, which is a blue triangle with a small circle at its tip. A single line labeled "Output P" exits the right side of the NOT gate.

A	B	C	P

A	B	D	C	P
			0	
			0	
			0	
			0	
			1	
			1	
			1	
			1	

```

graph LR
    A[Input A] --> AND1[AND Gate 1]
    B[Input B] --> AND1
    AND1 --> D[Output D]
    D --> AND2[AND Gate 2]
    C[Input C] --> AND2
    AND2 --> P[Output P]
  
```

[illegible]

In a Word document you need to copy one of your logic gates and tables above and explain HOW you calculated the table. Imagine you are trying to teach a non geek how to complete the truth table