

Hunter Ellis

Electrical & Computer Engineer

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Engineer with an interest in control theory and embedded technology.
↔ Currently, working on my Master's Thesis in Dr.Thinh Doan's Research Group.

github.com/hunterwellis
ellishw.tech

Skills

Languages: C++, Python, MATLAB, Embedded C, LaTeX, Verilog

Tools: Simulink, Git, GNU/Linux, ROS, Gazebo, PyTorch, OpenCV, SciKit-Learn, Make, CMake, LabView, Qt, KiCAD, FreeRTOS, Autodesk Inventor, SolidWorks, Rhino

Education

Master of Science in Computer Engineering May 2025
Virginia Tech – Software & Machine Intelligence Blacksburg, Virginia

Bachelor of Science in Electrical & Computer Engineering (double major) May 2024
Virginia Tech – Controls and Machine Learning Blacksburg, Virginia

Experience

Reinforcement Learning Research | M.S. Thesis Aug 2023 – Present
Virginia Tech · Graduate Researcher Blacksburg, Virginia

- Undergraduate and graduate research developing neuro-symbolic RL algorithms
- Developing and merging symbolic programming with deep RL for multi-task agents
- Building ROS workspaces and packages for training RL agents
- Implemented and designed symbolic RL methods to beat Atari games
- Simulated drone flight plans utilizing ROS, Ardupilot, and Gazebo

Thrust Vector Control | Mars Ascent Vehicle May 2024 – Aug 2024
Jacobs Space Exploration Group · TVC Intern Huntsville, Alabama
(Merrit Island, Florida)

- TVC for Mars Sample Return Mission and EUS at the NASA Marshall Space Flight Center
- Developed software and hardware systems for NASA's Active Inertial Load Simulator
- Derived a non-linear model and control architecture for a load simulating actuator
- Characterized dynamic systems for the Mars Ascent Vehicle's TVC test stand
- Traveled to Kennedy Space Center for the Space Launch System's (Booster) TVC Testing

Control Systems Research | Microgrid Inverters June 2023 – Aug 2023
Grenoble Electrical Engineering Laboratory · Research Intern Grenoble, France

- Designed and tested inverter control methods for decentralized grid applications
- Investigated NPC inverters with unbalanced network conditions for islanding events
- Simulated neutral point balancing control methods using 4-leg inverters

Naval Concept Design Research | Hospital Sea Trains June 2022 – Aug 2022
Naval Surface Warfare Center (Carderock Division) · Concept Research Intern West Bethesda, Maryland

- Developed concept hospital sea-train design at the Center for Innovative Ship Design
- Conducted electrical power loading analysis on concept surface ships
- Estimated fuel consumption of concept hospital ships

Graduate Teaching Assistant | Continuous & Discrete Systems Aug 2024 – Present
Virginia Tech · Teaching Assistant Blacksburg, Virginia

- Assisting ECE Professors, teaching fundamental concepts in linear systems theory
- Holding office hours and preparing recitation sessions for students

Projects

FOC Stepper Motor (github.com/hunterwellis) Dec 2023 – Present

- Widely applicable stepper motor driver using FOC and a magnetic encoder for feedback
- 4-layer PCB mounts to the back of stepper with CAN and power connection

Computer Vision | OCR Capstone Project (capstone_brochure.pdf) Aug 2023 – May 2024

- IOS application capable of detecting coins of interest/value
- Trained OCR and ResNet-50 models on dataset of real and augmented coin images

Design Teams | Solar Car & Human Powered Submarine (solarcaratvt.org) Oct 2020 – Mar 2023

- Overall E/E architecture of the Solar Car
- Single board computer and LCD to display relevant data to the submarine pilot