

CmpE 443 Final Project Design Document

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1 System Level Structural Diagram

This is where system level structural diagram will be put. Do not forget to describe functionality of the components in the scope of the given problem.

2 Task Graph (System Level Functional Diagram)

Draw the task graph here. Briefly describe the tasks of the functions and ISRs.

3 Sequence Diagram

Sequence Diagram would be here. You should show the relationships between modules (by sending variables, commands etc.) clearly and thoroughly. Explain each module as it is explained in Bike Computers lab description.(Lab 4)

4 Scheduling

Necessary codes with ISRs and the shared variable vs. ISR table will go here.

5 Timing Diagram

By using your scheduling, draw a timing diagram. The table (including WCET, ACET) goes here also.

6 Hardware Block Diagram

Here is an example figure for you to use the LaTeX format.

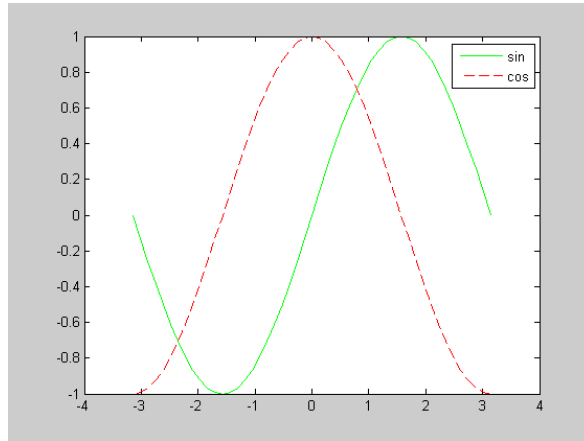


Figure 1: Sin and Cosine.

7 Board Pin Table

Here is an example table for you to use the LaTeX format.

Algorithm	Feature 1	Feature 2	Feature 3	Feature n
Alg 1	1	2	23	68.7%
Alg 2	9	2	37	75.68%
Alg 3	10	3	51	96.34%

Table 1: Should be a caption