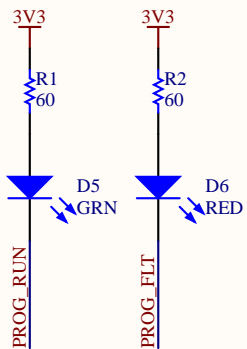
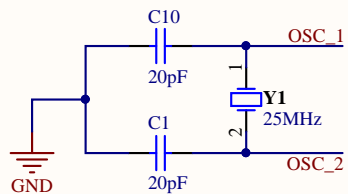


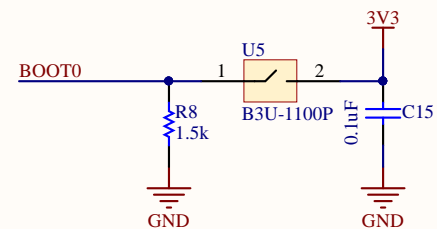
A



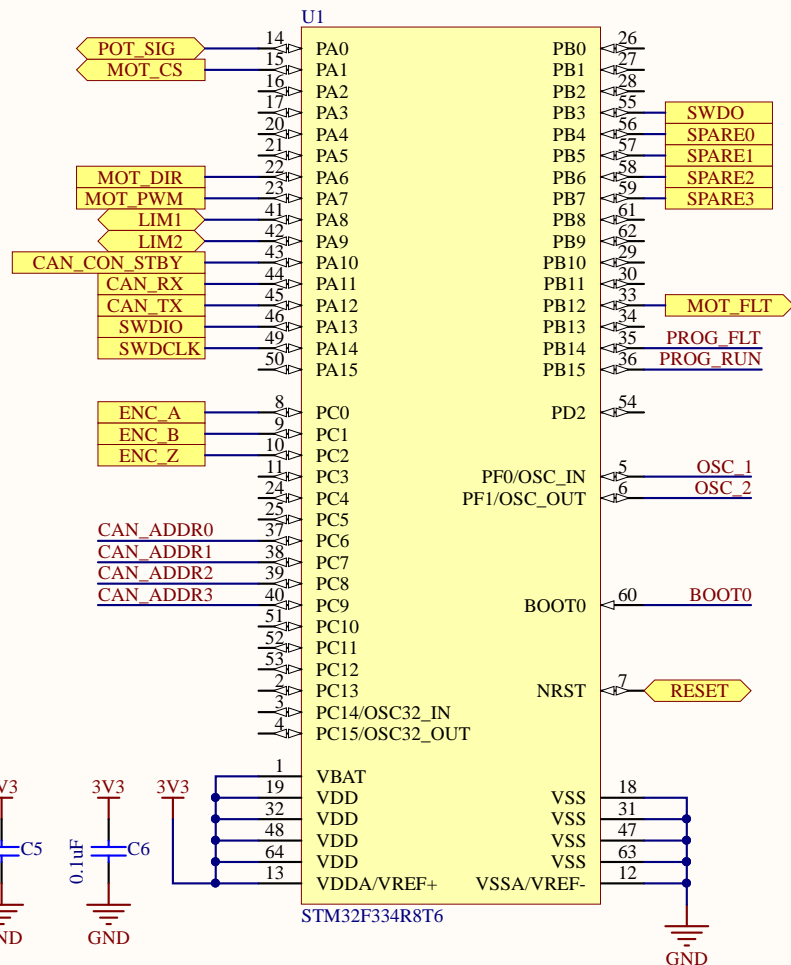
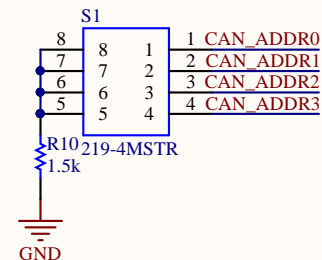
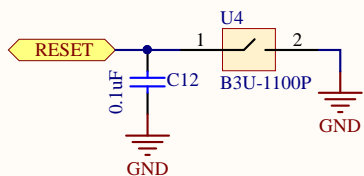
B



C



D



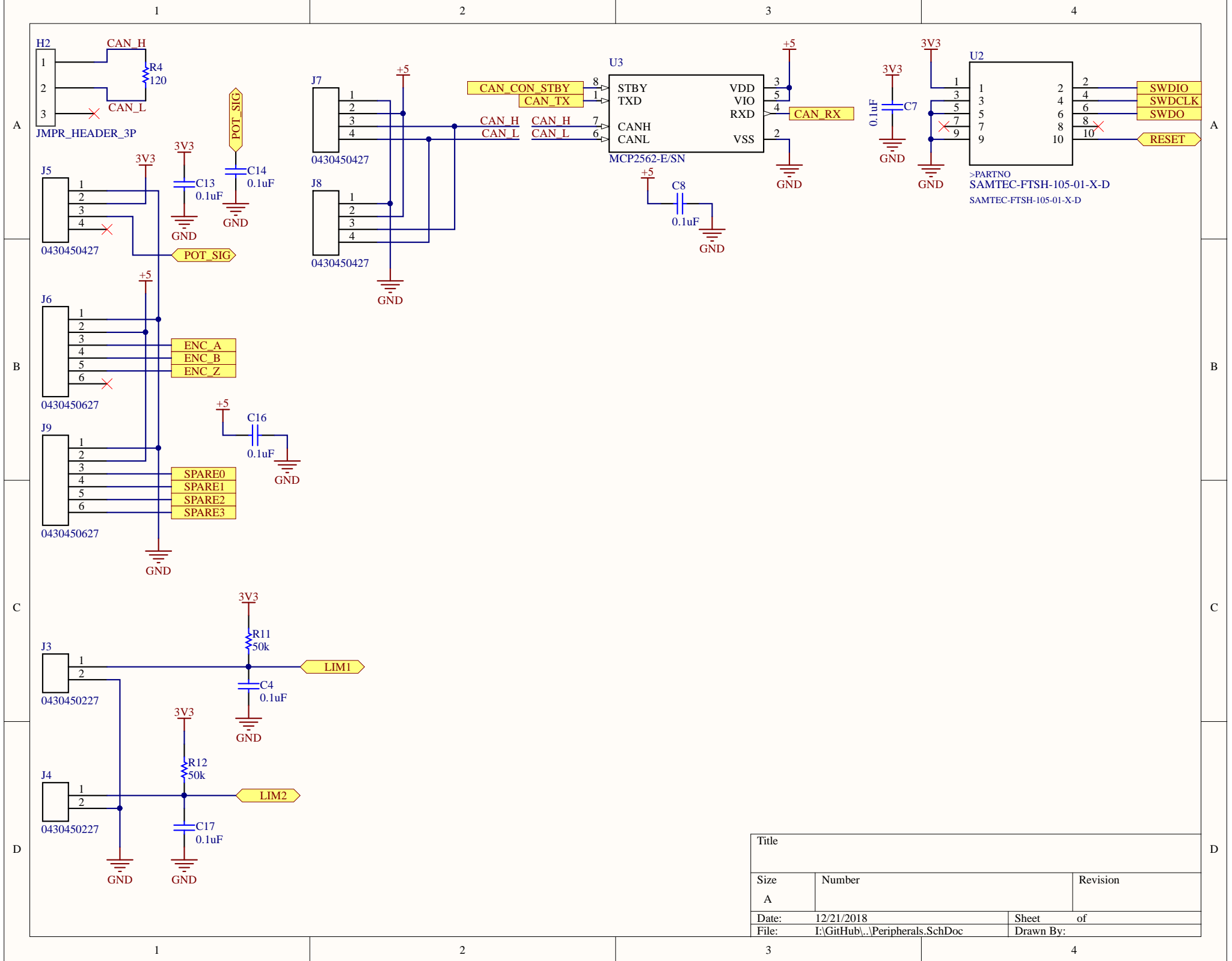
A

B

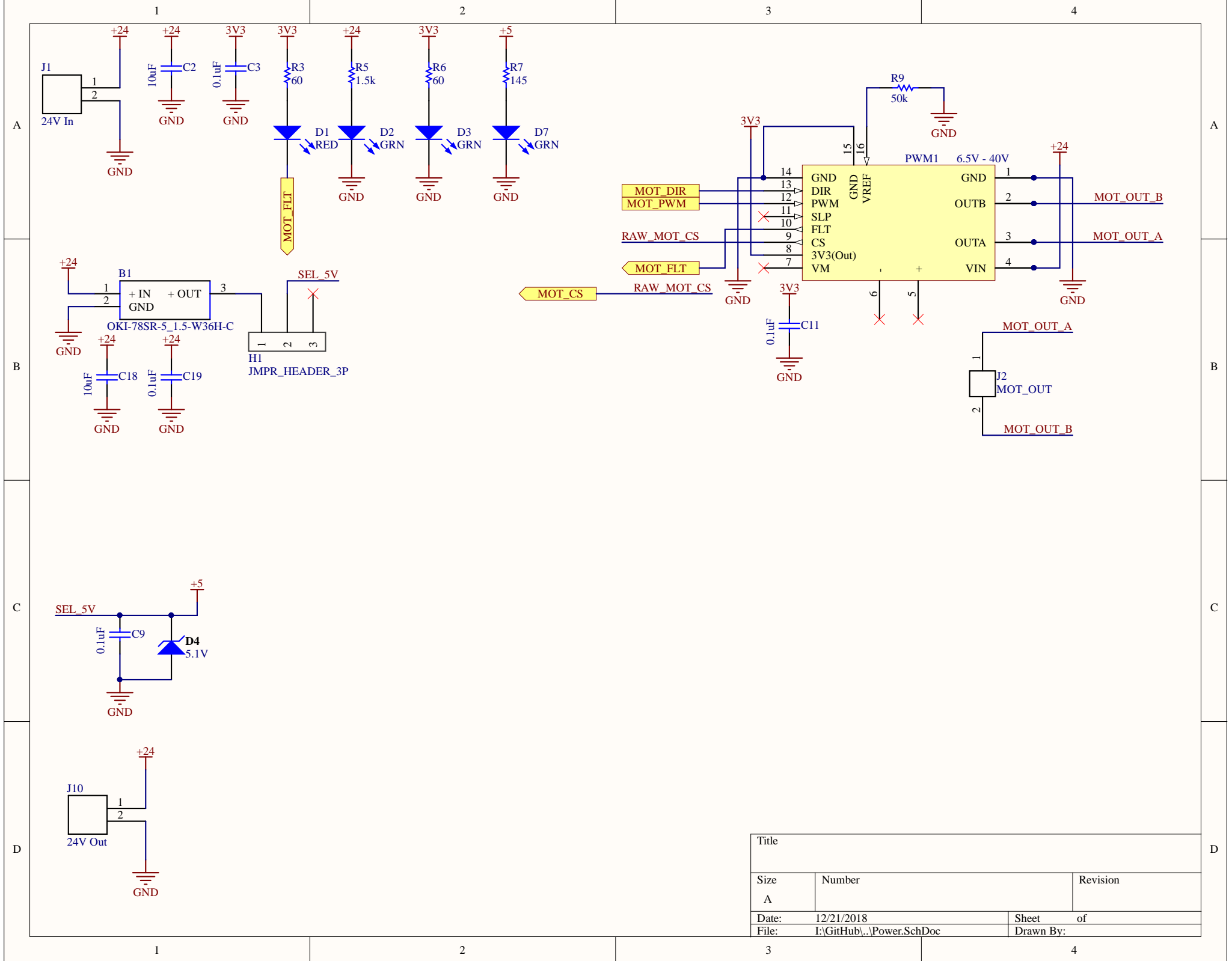
C

D

Title		
Size	Number	Revision
A		
Date:	12/21/2018	Sheet of
File:	I:\(GitHub)\...\Microcontroller.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	12/21/2018	Sheet of
File:	I:\GitHub\...\Peripherals.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	12/21/2018	Sheet of
File:	I:\GitHub\...\Power.SchDoc	Drawn By:

