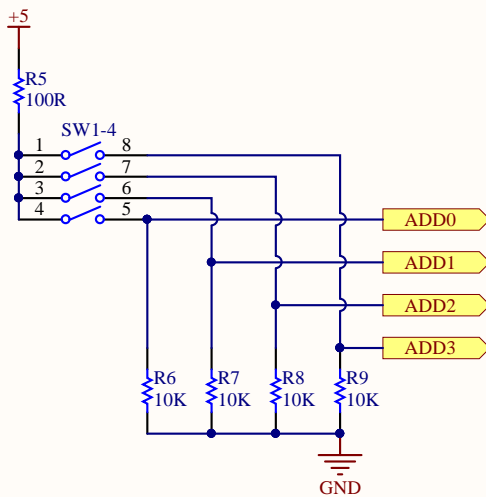
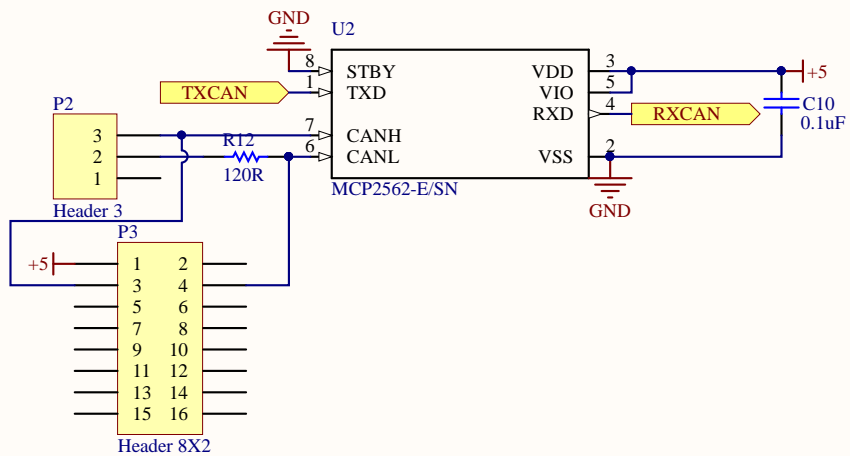


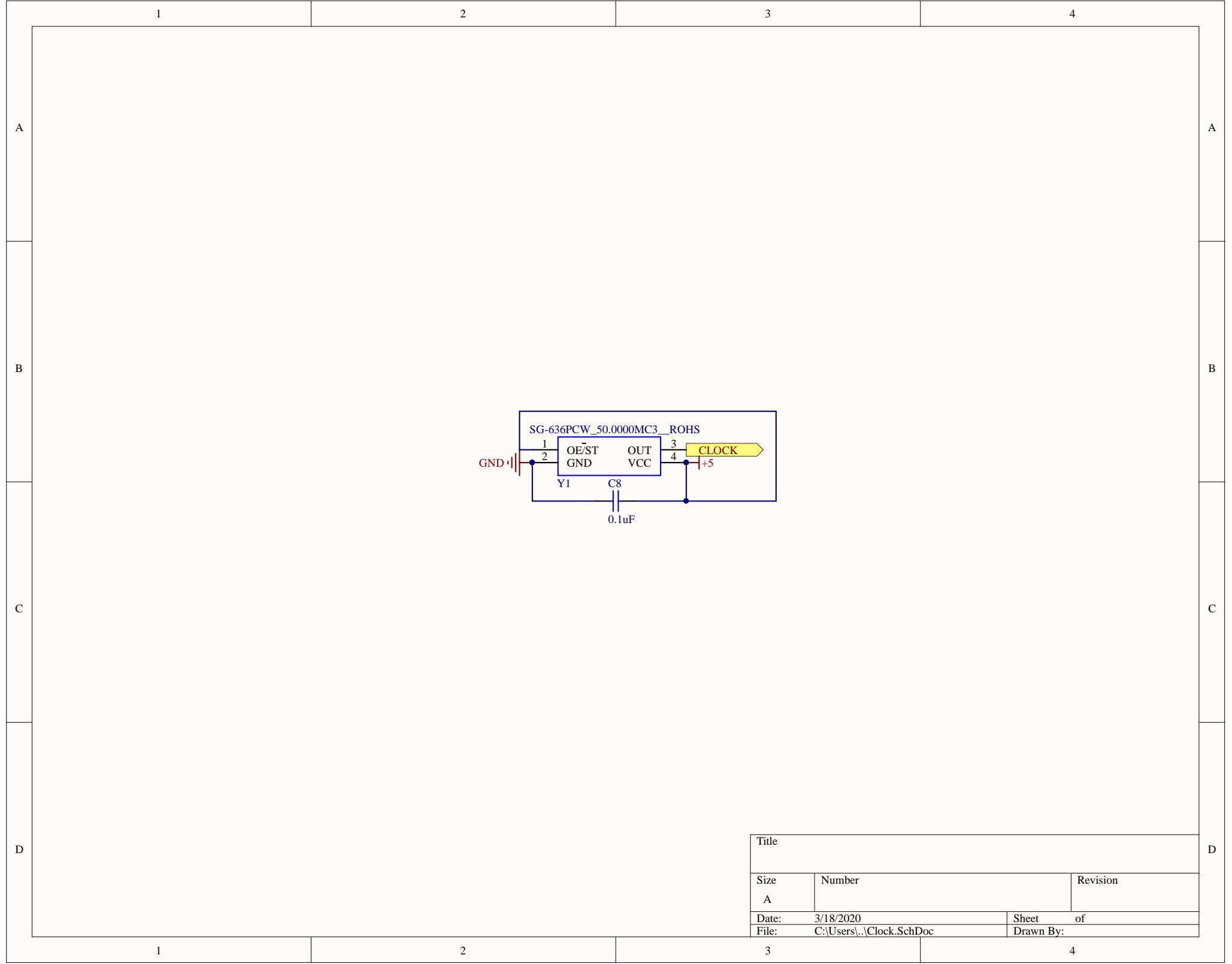
Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\Analog Input Noise Canceller.SchDoc	
	Drawn By:	

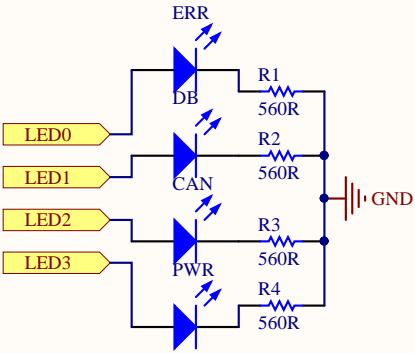


Title			
Size	Number		Revision
A			
Date:	3/18/2020	Sheet	of
File:	C:\Users\...\CAN Address Switch Array.SchDoc		
Drawn By:			

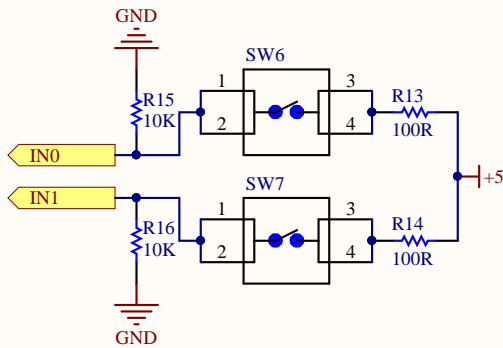


Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\CAN System.SchDoc	Drawn By:

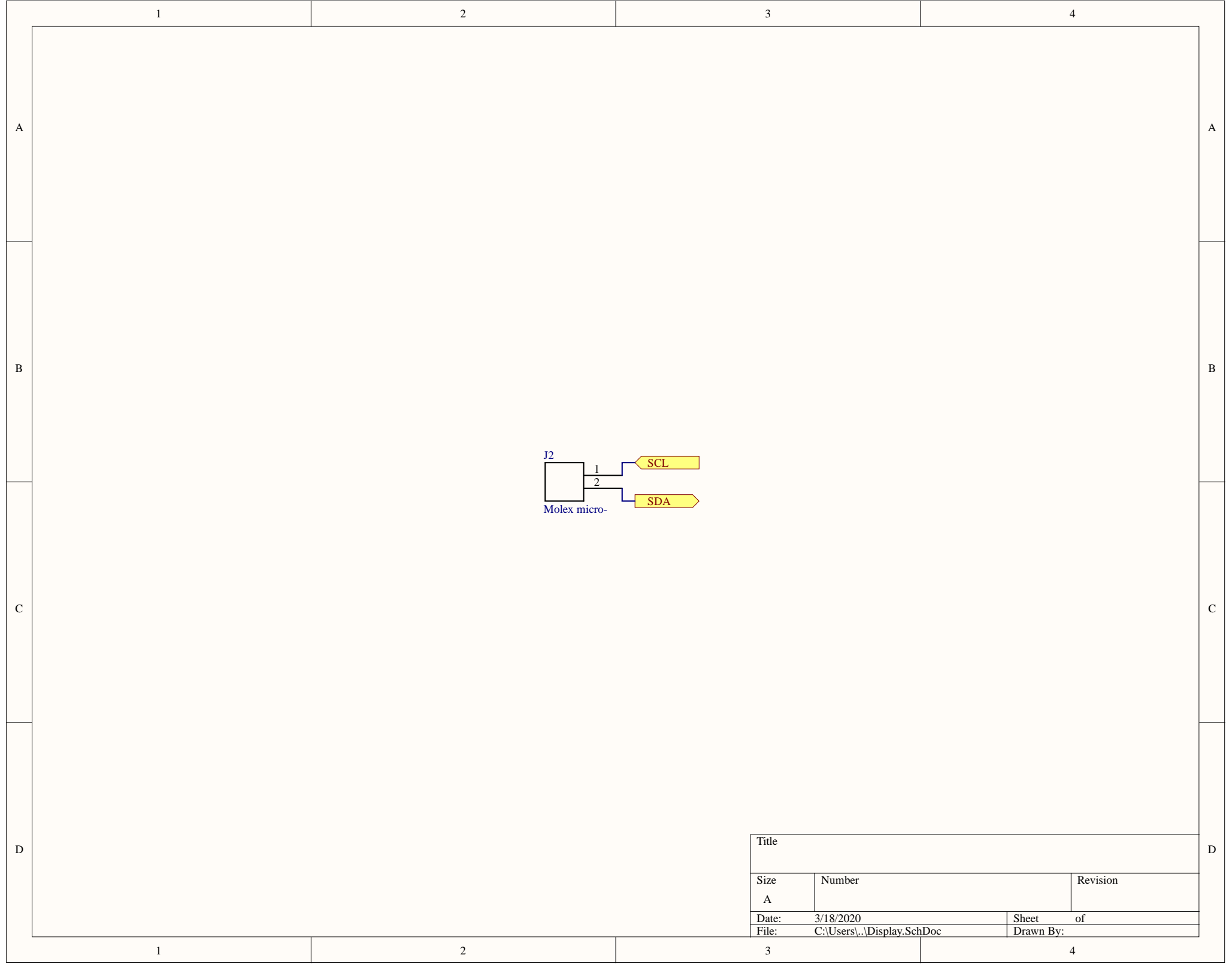


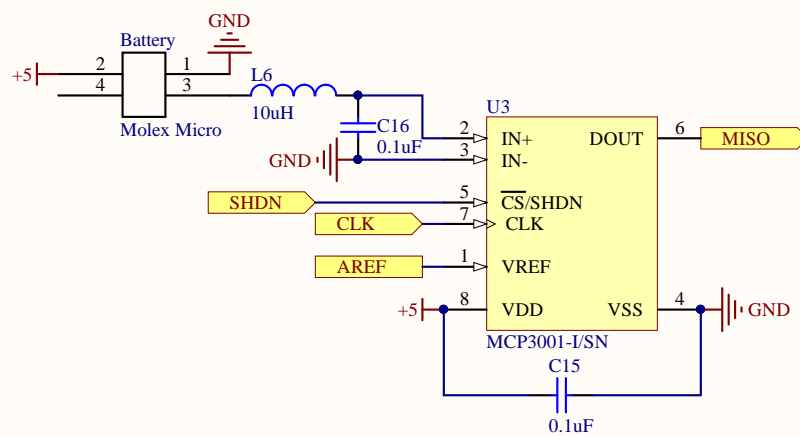


Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\Debug LEDs.SchDoc	Drawn By:

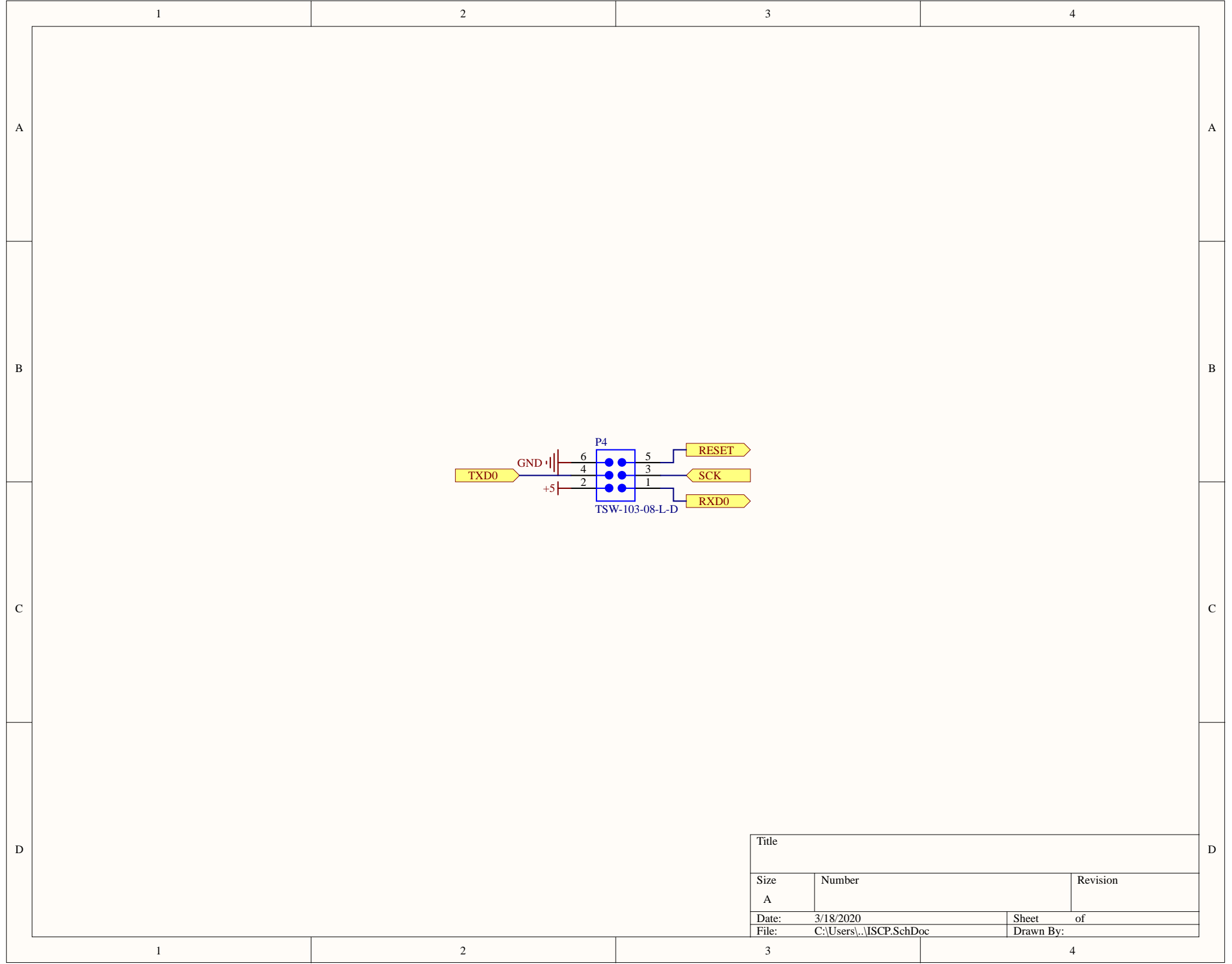


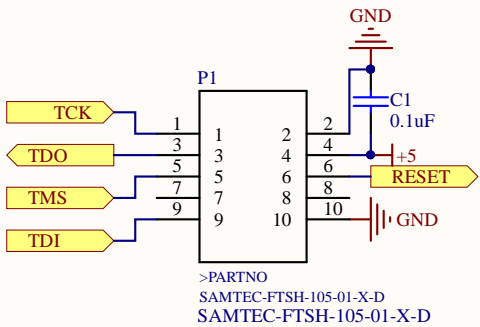
Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\Debugger.SchDoc	Drawn By:



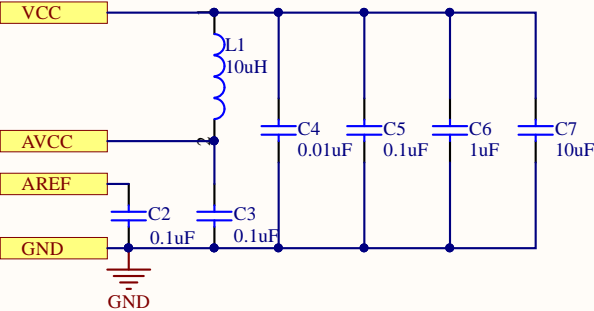


Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\External ADC.SchDoc	Drawn By:

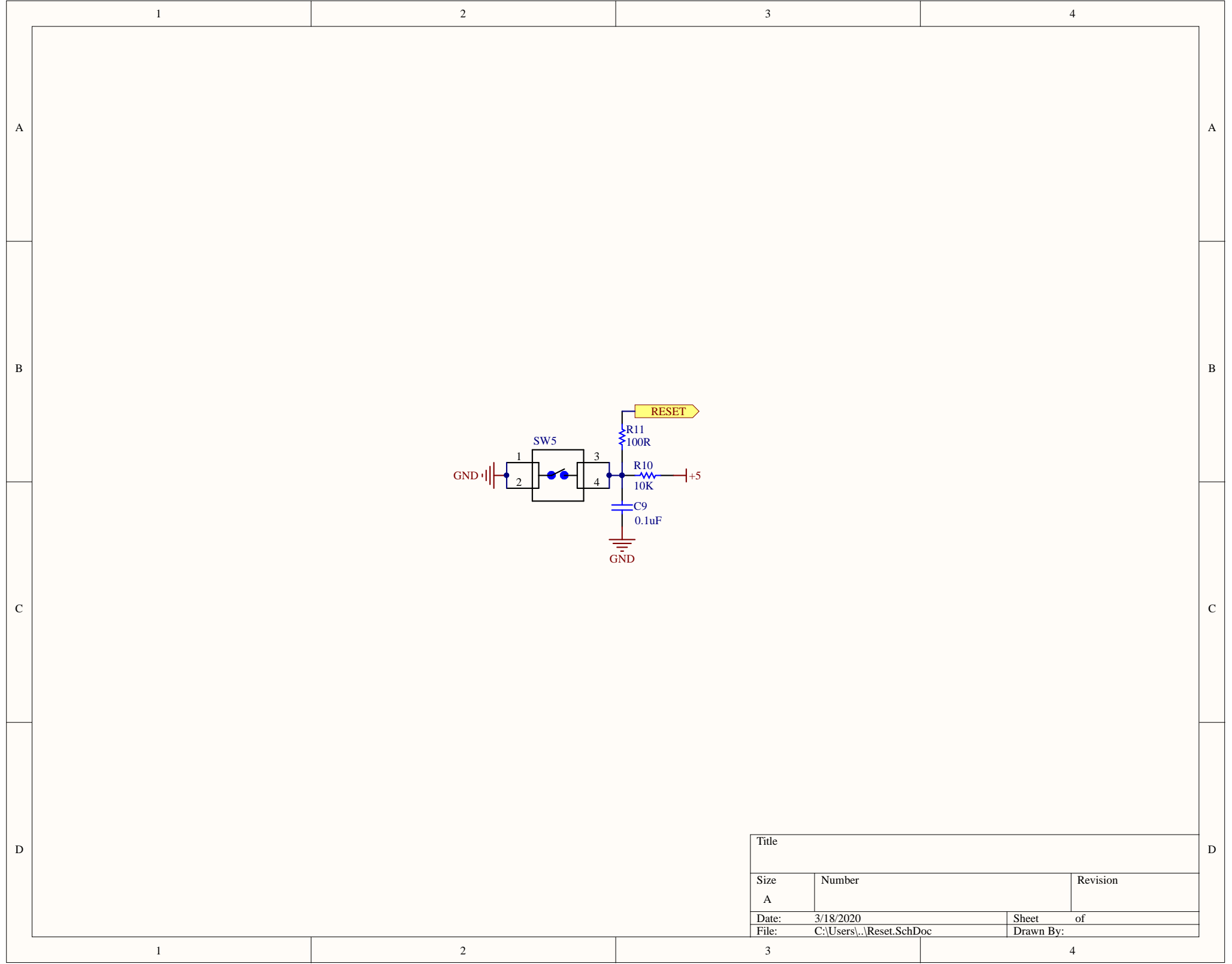




Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\JTAG.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	3/18/2020	Sheet of
File:	C:\Users\...\Power Supply Noise Canceller.SchDoc By:	



Title		
Size A	Number	Revision
Date:	3/18/2020	Sheet of
File:	C:\Users\...\Reset.SchDoc	Drawn By:

A

A

B

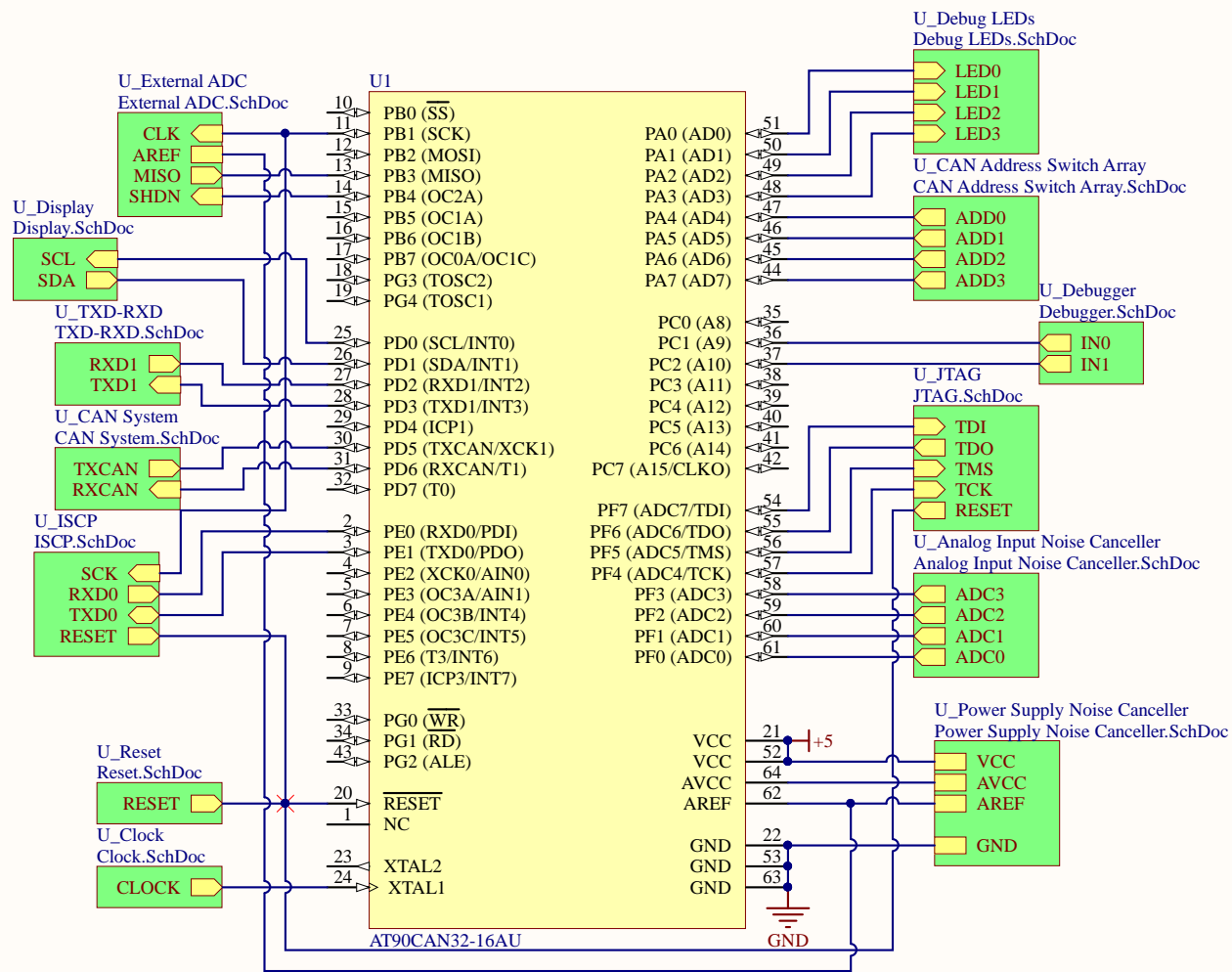
B

C

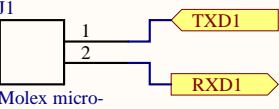
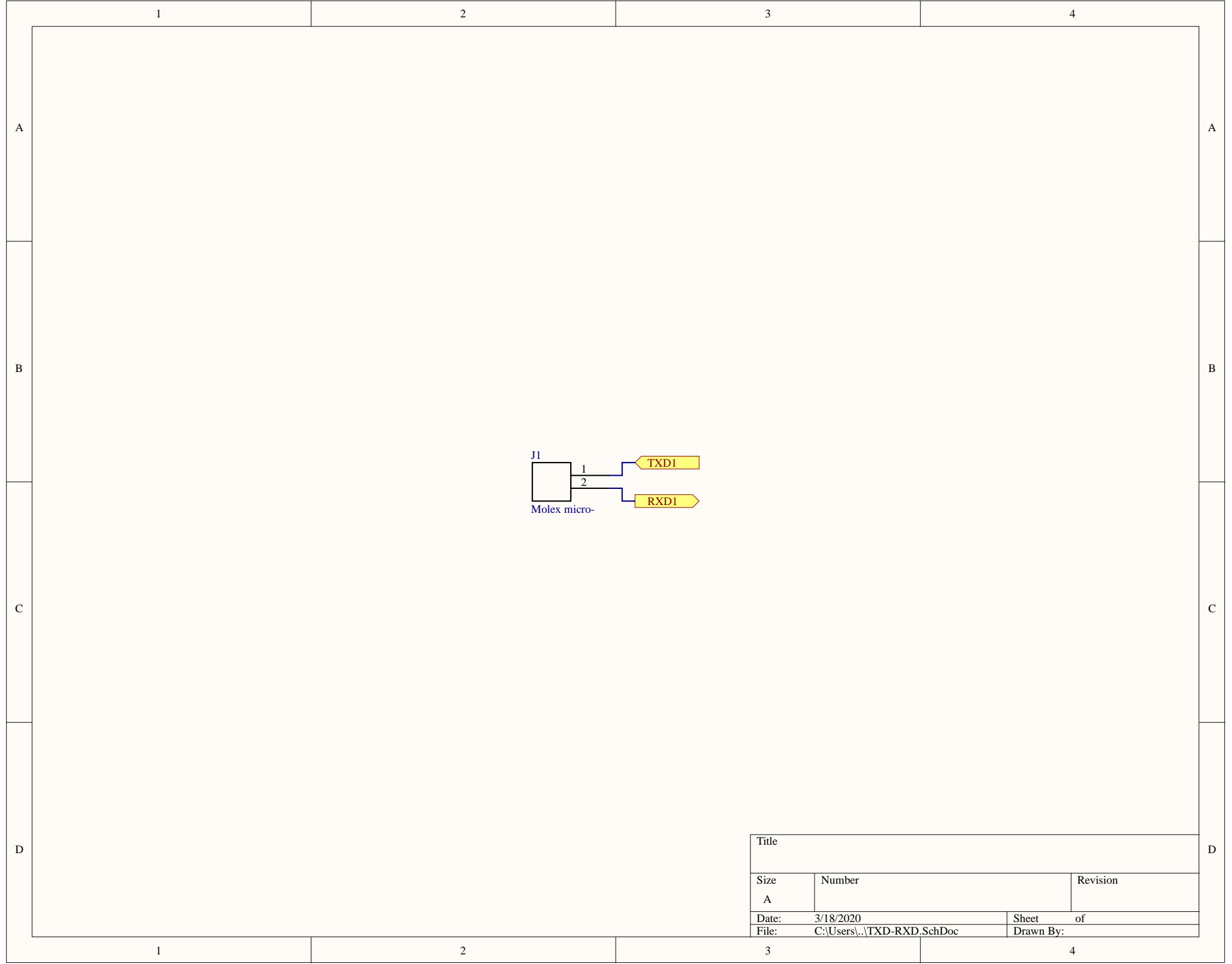
C

D

D



Title			
Size	Number		Revision
A			
Date:	3/18/2020		Sheet of
File:	C:\Users\...\Top Level.SchDoc		Drawn By:

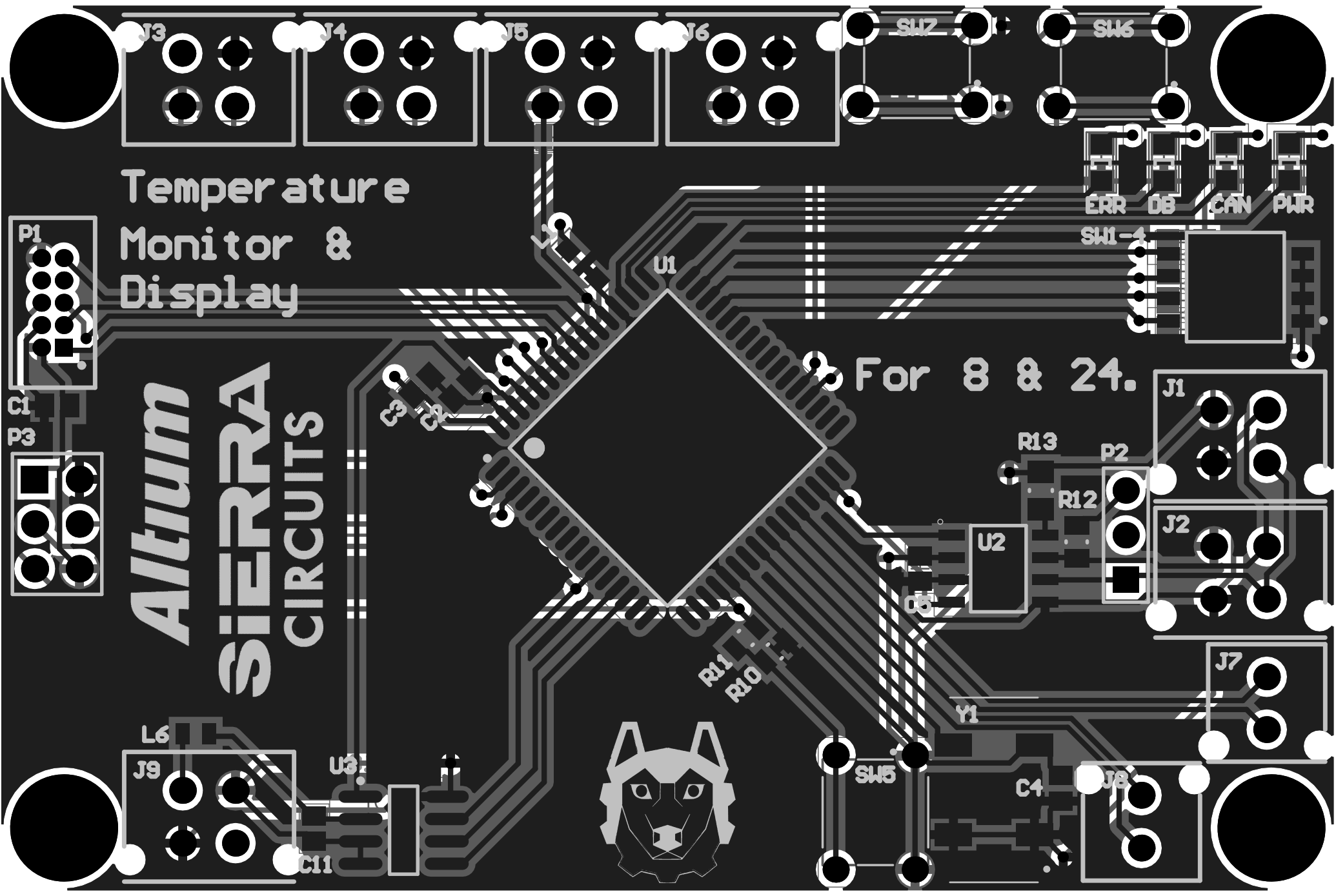


Title			
Size	Number		Revision
A			
Date:	3/18/2020	Sheet	of
File:	C:\Users\...\TXD-RXD.SchDoc	Drawn By:	

Temperature
Monitor &
Display

Altium
SIERRA
CIRCUITS

For 8 & 24.



Board Stac