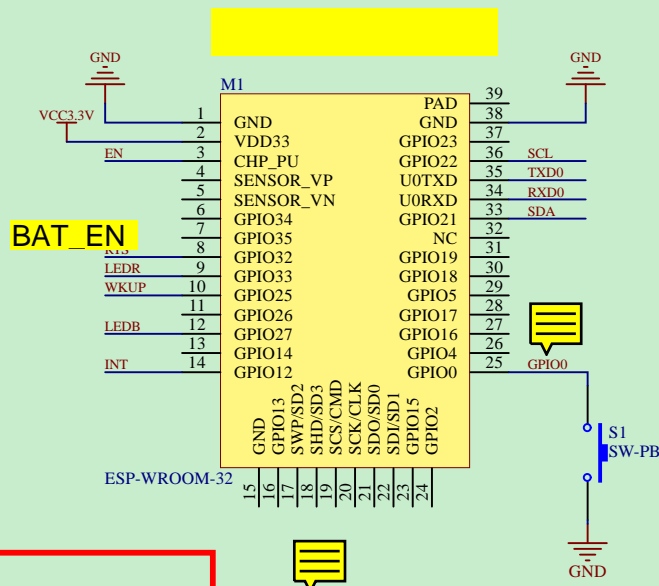
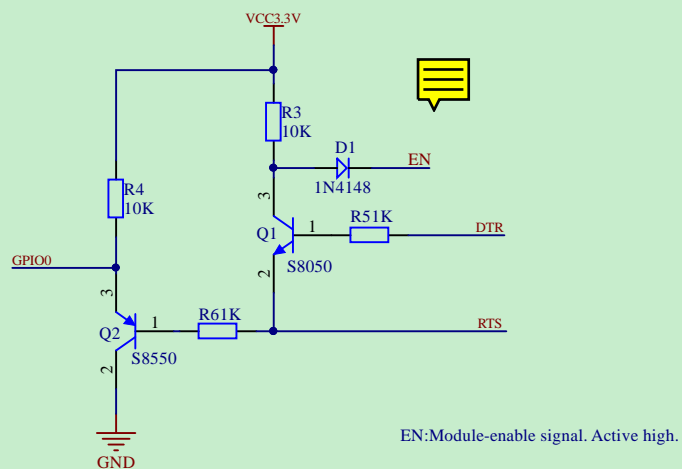
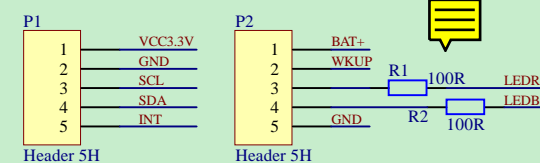


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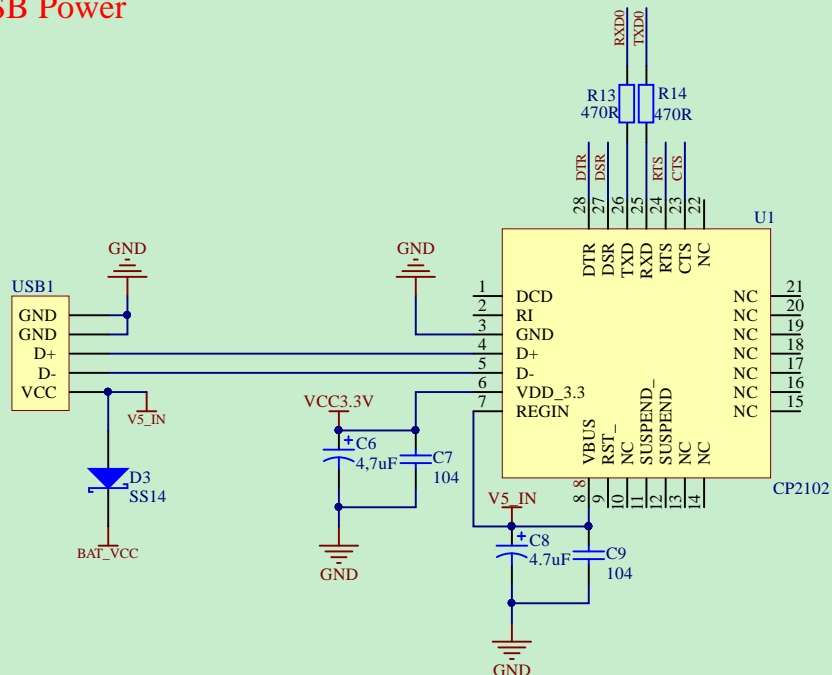
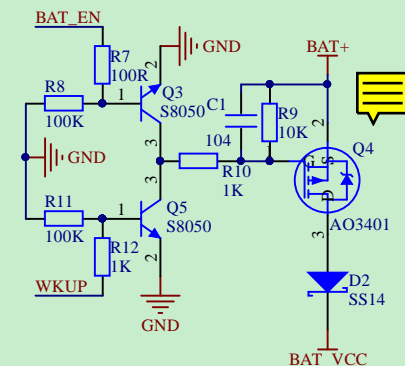
EN:Module-enable signal. Active high.



Interface




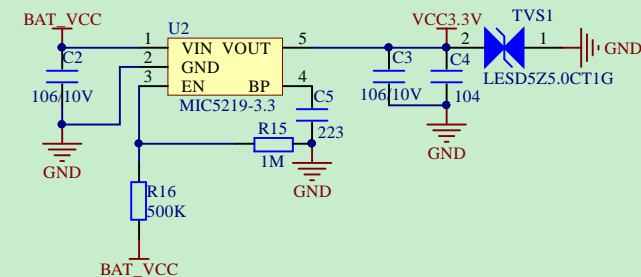
Power



DC-DC

The diagram illustrates a DC-DC converter circuit using the MIC5219-3.3 voltage regulator. The input voltage is BAT_VCC, which is filtered by capacitor C2 (106 10V). The regulator U2 (MIC5219-3.3) has its VIN connected to BAT_VCC, GND to ground, EN to ground via R16 (500K), and BP to ground via R15 (1M). The output VOUT is connected to the load through capacitor C3 (106 10V) and C4 (106 10V). The output voltage is 3.3V (VCC3.3V). A TVS diode (TVS1, LESD5Z5.0CT1G) is connected in parallel with the load to protect against voltage spikes. The load is represented by a resistor connected to GND.

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