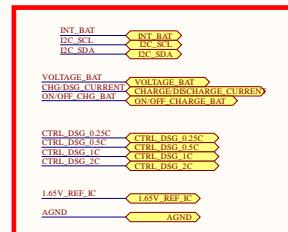
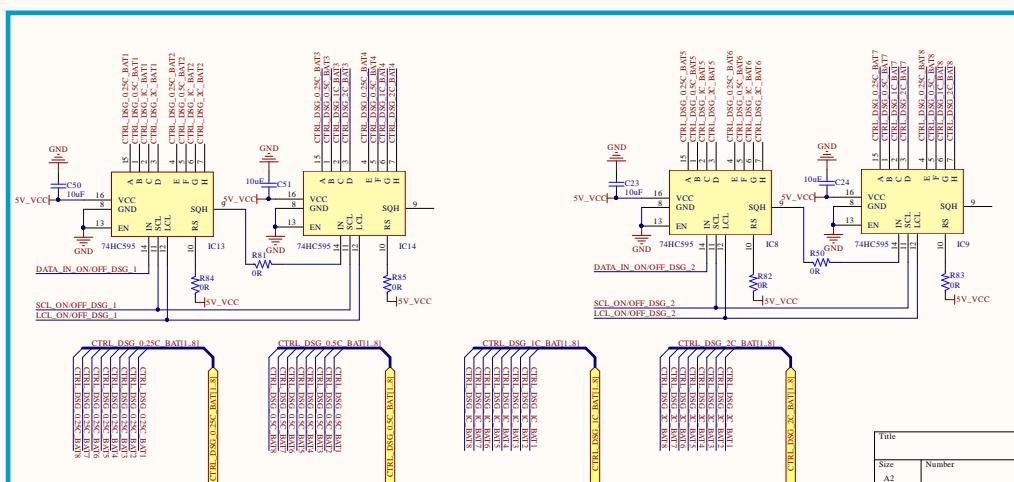
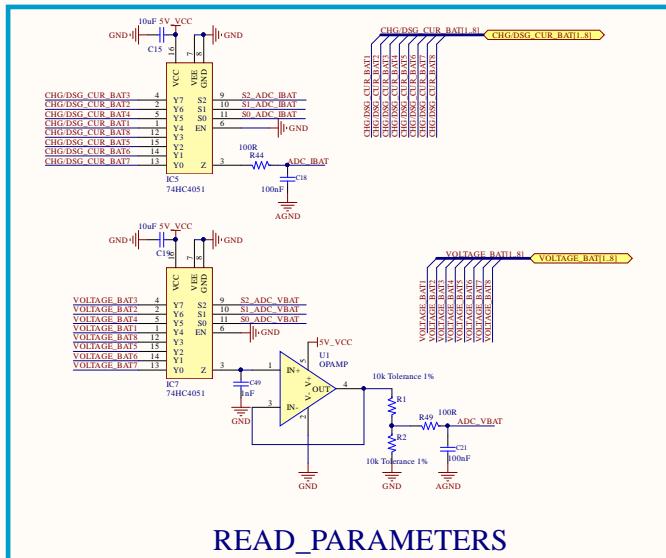
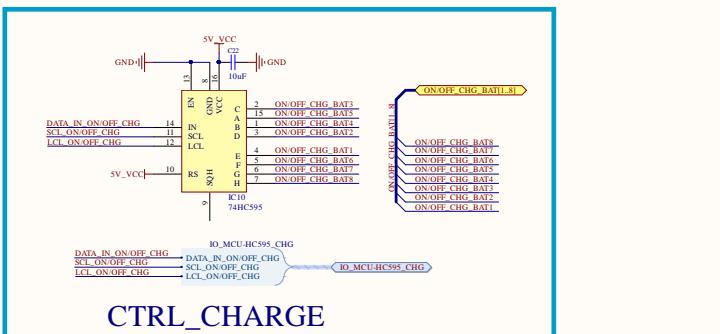
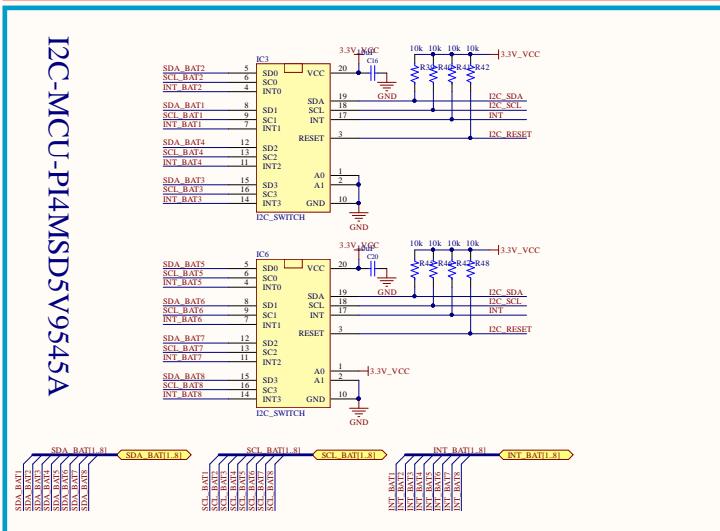
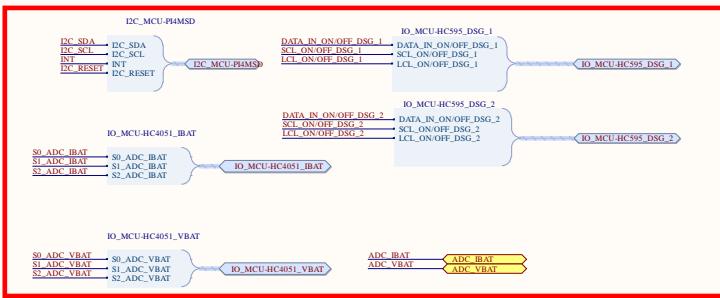


PORT OUT





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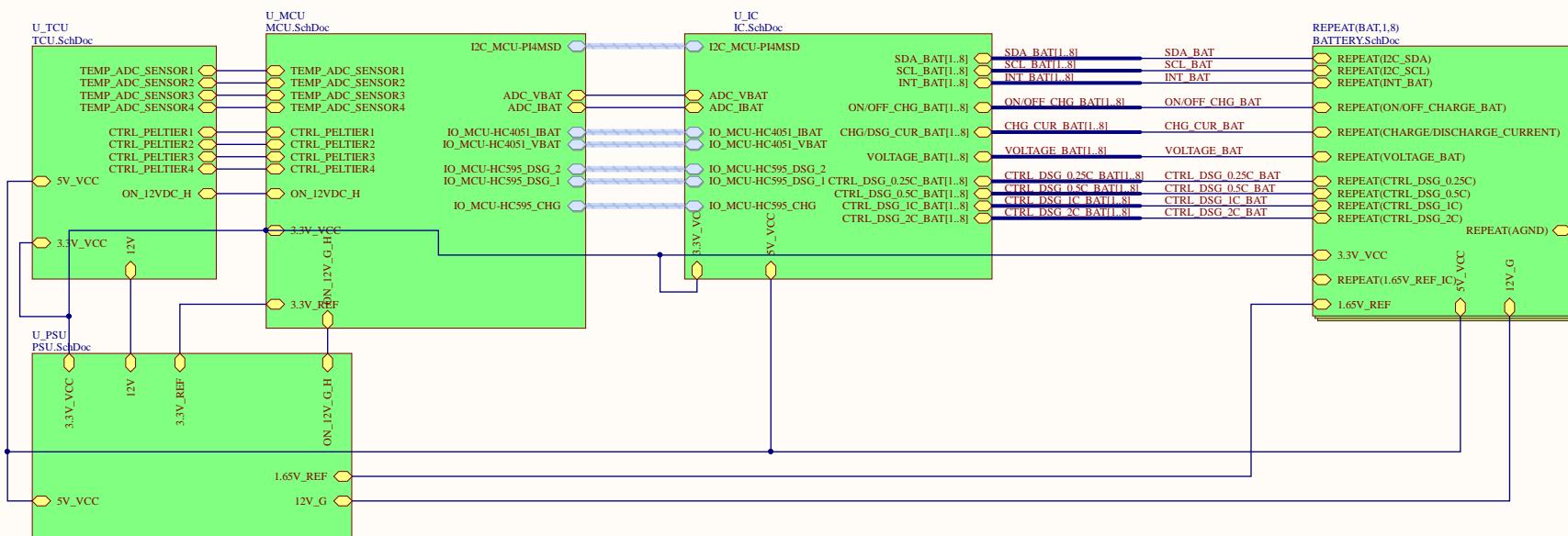
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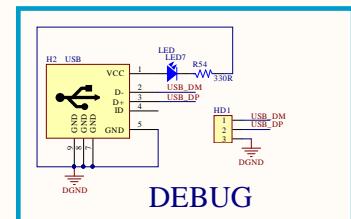
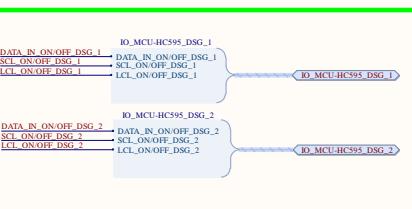
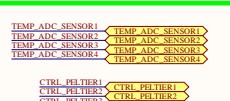


The diagram illustrates the connection between the I2C pins of the STM32F407VG microcontroller and the I2C_MCU-PI4MSD module. The connections are as follows:

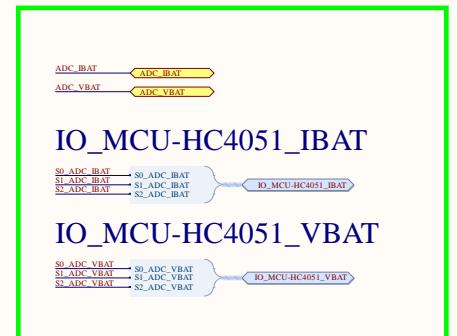
- I2C_SDA: STM32F407VG pin 10 → I2C_MCU-PI4MSD
- I2C_SCL: STM32F407VG pin 11 → I2C_MCU-PI4MSD
- INT: STM32F407VG pin 12 → I2C_MCU-PI4MSD
- RESET: STM32F407VG pin 13 → I2C_MCU-PI4MSD



DATA_IN_ON/OFF_CHG → **DATA_IN_ON/OFF_CHG**
SCL_ON/OFF_CHG → **SCL_ON/OFF_CHG**

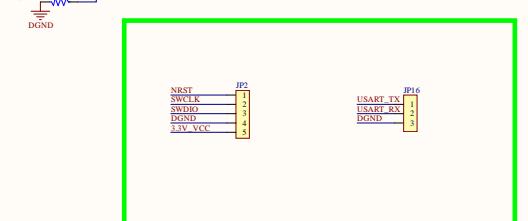
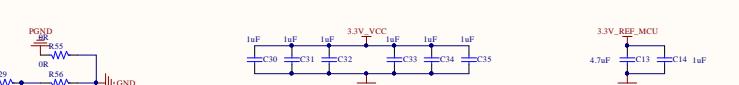


DEBUG



IO MCU-HC4051_IBAT

The diagram illustrates a connection from four individual ADC_IBAT signals (S0_ADC_IBAT, S1_ADC_IBAT, S2_ADC_IBAT) to a single integrated block labeled IO MCU-HC4051_IBA. The connections are represented by blue lines originating from the left side of the diagram and merging into a single blue line that points to the right towards the IO MCU block.

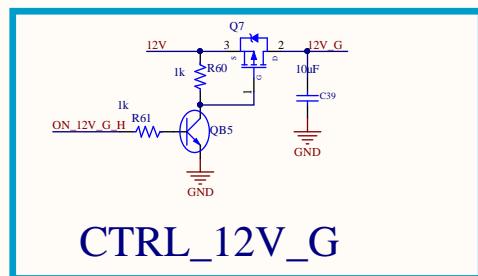
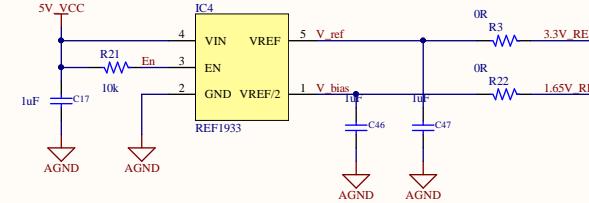
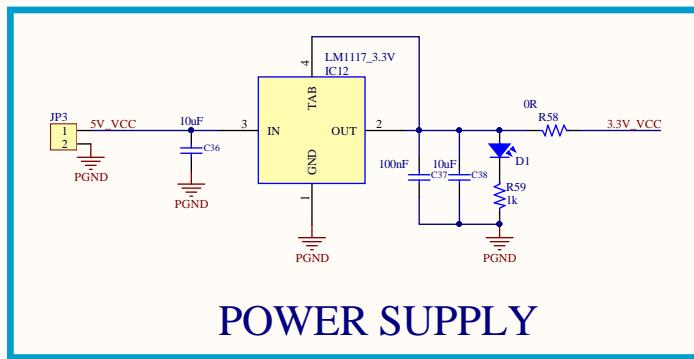
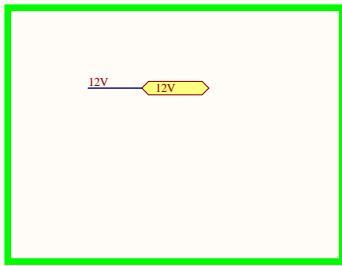
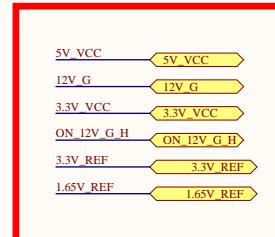


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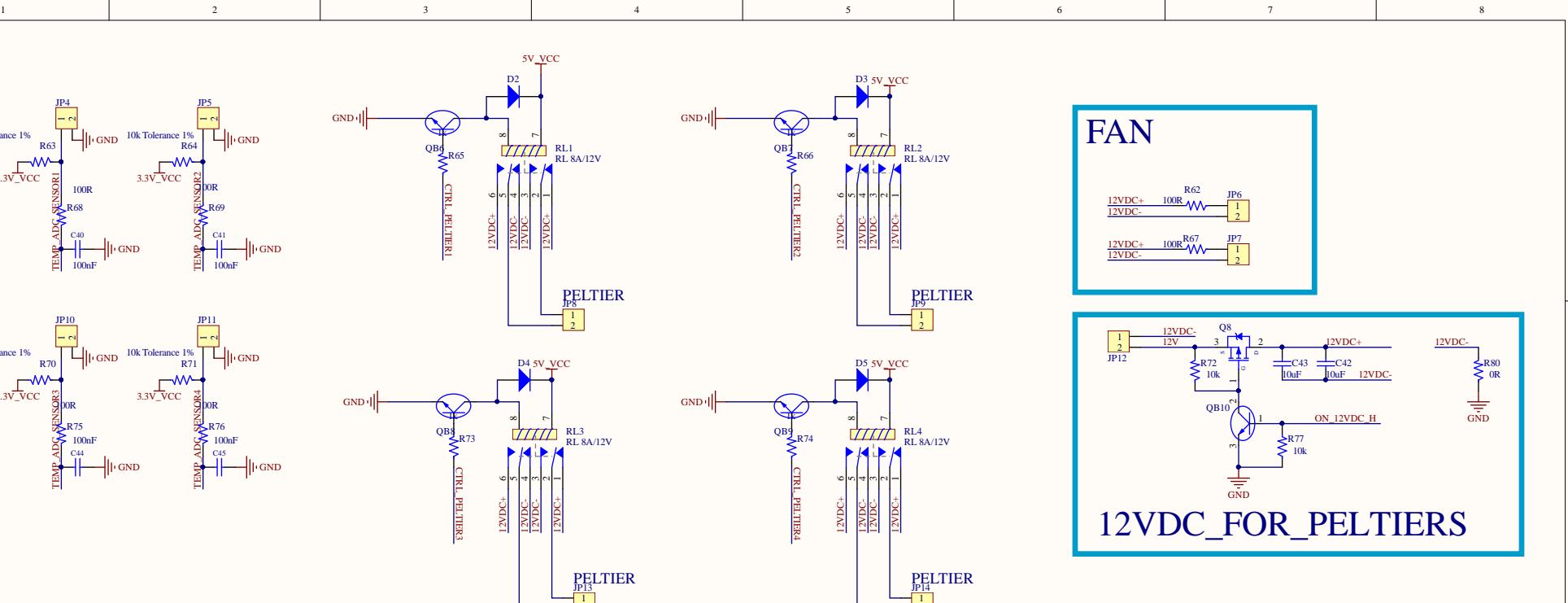
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