

Huy Dinh Tran

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EDUCATION

University of Kansas Ph.D Computer Science Advisor: Prof. Mohammad Alian (Cornell University)	08/2023 — Present
University of California, Riverside M.S. Computer Engineering Advisor: Prof. Daniel Wong	09/2021 — 03/2023
Pennsylvania State University B.S. Electrical Engineering	08/2017 — 05/2021

EXPERIENCE

University of Kansas <i>Graduate Teaching Assistant</i> <ul style="list-style-type: none">Assisted in teaching, grading, and providing feedback for Software Engineering II (EECS 581)Supervised and offered mentorship to 7 senior undergraduate teams on complex design projectsConducted weekly meetings to provide guidance, resolve challenges, and ensure project success	Lawrence, KS 08/2024 — Present
<i>Graduate Research Assistant</i> <ul style="list-style-type: none">Researched processor microarchitecture designs aimed at improving datacenter performance and efficiencyDeveloped custom applications and microbenchmarks for thorough evaluation of architectural designsImplemented new features in a full-system simulator to support research experiments	08/2023 — 08/2024
Futurewei Technologies <i>Research Intern</i> <ul style="list-style-type: none">Simulated RISC-V CPUs in Linux Full-System simulation mode using gem5Cross-compiled binaries of SPEC CPU 2017 benchmarks to RISC-V for measuring the performance of CPU designsIntegrated SimPoint to create checkpoints at ROIs for speeding up the simulation while still representing the workloads	San Jose, CA 08/2022 — 09/2022

PUBLICATIONS

- Amin Mamandipoor, **Huy Dinh Tran**, Mohammad Alian, “*SDT: Cutting Datacenter Tax Through Simultaneous Data-Delivery Threads*” (Under Review, ISCA 2025)
- Amin Mamandipoor, **Huy Dinh Tran**, Mohammad Alian, “*Simultaneous Multithreading in gem5 Full System Simulation*” (In Preparation, to be submitted to ISPASS 2025)

PROJECTS

Building custom GPU power models with AccelWattch Implemented a GPU power model using AccelWattch; profiled GPUs to analyze power usage and performance counters; simulated benchmarks on GPGPU-Sim, achieved an average MAPE of 63.42% between simulated and real power data	Spring 2022 — Spring 2023
Sparse matrix-vector multiplication (SpMV) Developed sequential and parallel Sparse Matrix-Vector Multiplication in C with OpenMP; converted matrix formats from COO to CSR and CSC; achieved 3.93x speedup using 8 threads	Fall 2022
Soccer matches prediction Implemented multiple ML classification models in Python using scikit-learn and from scratch using NumPy and Pandas; achieved prediction accuracy of 81.25% using K-Nearest Neighbors	Spring 2022

SKILLS

Language: C/C++, Python, Bash, LaTeX, MATLAB
Software & Tools: Git, gem5, Intel VTune, Intel CAT, GDB, Docker, OpenMP, GPGPU-Sim