


REVISION		DESCRIPTION	DATE	APPROVED

The diagram illustrates a two-stage DC-DC conversion process. The first stage uses an LM7805 voltage regulator to convert a 9V input from a JDC (Jack DC) connector into a stable 5V output. This 5V output is then used to power a second stage, which is an LM1117-3.3V adjustable voltage regulator. The LM1117-3.3V is configured with its input to the 5V line and its output to a 3.3V line. Both the 5V and 3.3V lines are connected to a 10-pin header (JP1). The 3.3V line is also connected to two LEDs (D1 and D2) through current-limiting resistors (104 ohms and 104 ohms, respectively). The ground connections for all components are shown as a common ground.

APPROVALS		DATE		PROJECT			
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Board Stack Report