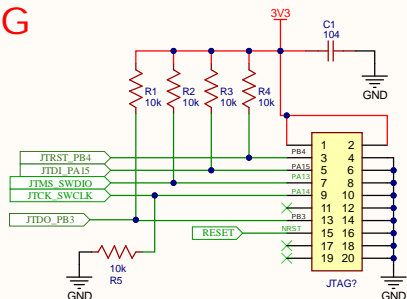
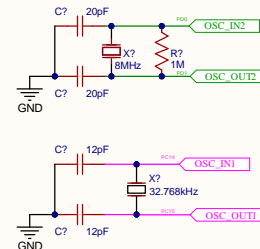


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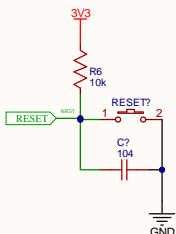
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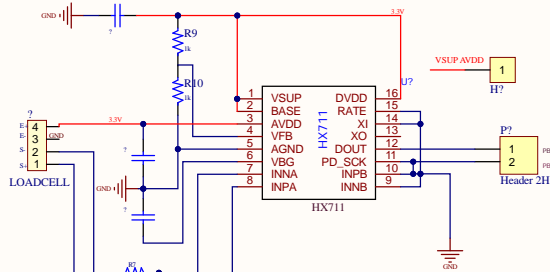
## CRISTALS



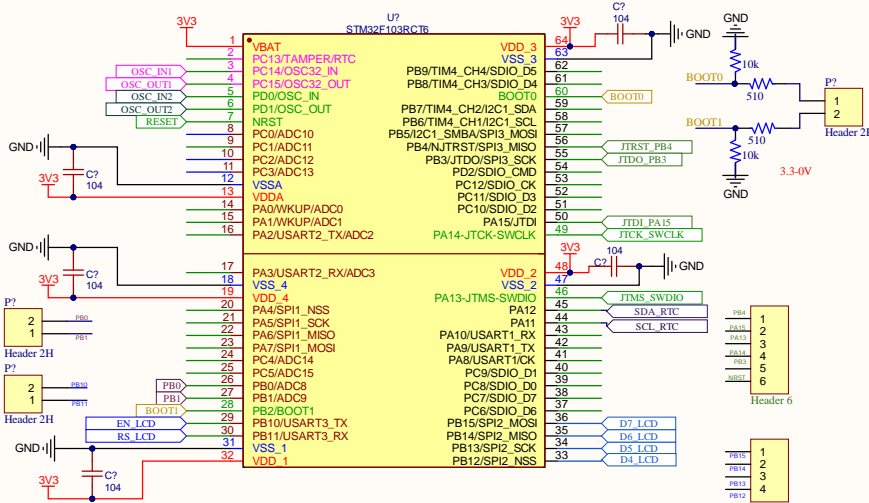
## RESET



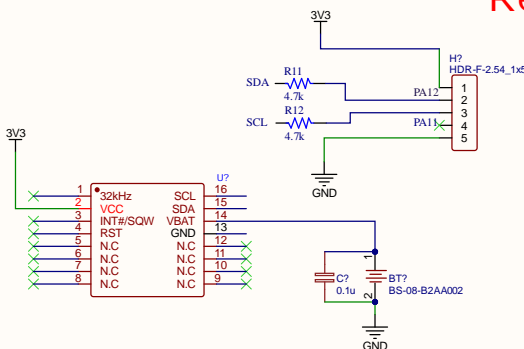
## LOADCELL + HX711



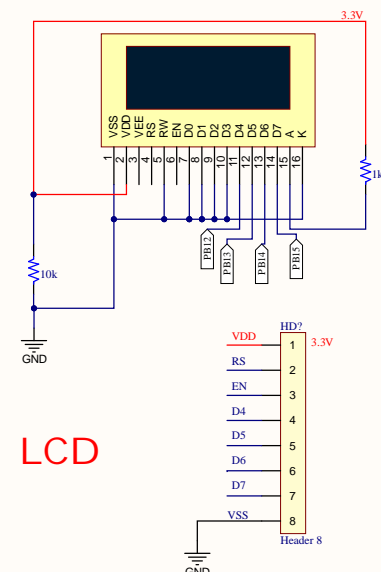
## STM32-CPU STM32F103RCT6 @72MHz



## Real-Time Clock



## LCD



|                     |       |                     |                        |
|---------------------|-------|---------------------|------------------------|
| APPROVALS           | DATE  | PROJECT             | Altium                 |
| ENG: .              |       | PROJECT REVISION: . | DOCUMENT REVISION: .   |
| DSN: .              |       | TITLE               | Not in version control |
| CHK: .              |       | SCALE               | Not in version control |
| REFERENCE DOCUMENTS |       | * .                 |                        |
| ASSY DWG:           | SIZE  | CAGE CODE           | DWG NO.                |
| FAB DWG:            | A3    |                     |                        |
| PCB DWG:            | SCALE | FILE NAME           | CONNECTER.SchDoc       |
|                     |       | SHEET               | OF                     |

# Board Stack Report