

ADSP TDM Renderer/Capture Plugin RCG3AHPLN0101ZNO

User's Manual

RCG3AHPLN0101ZNOE

All information contained in these materials, including products and product specifications, represents information on the product at the time of publication and is subject to change by Renesas Electronics Corp. without notice. Please review the latest information published by Renesas Electronics Corp. through various means, including the Renesas Electronics Corp. website (http://www.renesas.com).

Rev. 1.00 Jul, 2017

Notice

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
- 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other disputes involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawing, chart, program, algorithm, application examples.
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 4. You shall not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics products.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.
 - Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (space and undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by Renesas Electronics.
- 6. When using the Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat radiation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions or failure or accident arising out of the use of Renesas Electronics products beyond such specified ranges.
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please ensure to implement safety measures to guard them against the possibility of bodily injury, injury or damage caused by fire, and social damage in the event of failure or malfunction of Renesas Electronics products, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures by your own responsibility as warranty for your products/system. Because the evaluation of microcomputer software alone is very difficult and not practical, please evaluate the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please investigate applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive carefully and sufficiently and use Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall not use Renesas Electronics products or technologies for (1) any purpose relating to the development, design, manufacture, use, stockpiling, etc., of weapons of mass destruction, such as nuclear weapons, chemical weapons, or biological weapons, or missiles (including unmanned aerial vehicles (UAVs)) for delivering such weapons, (2) any purpose relating to the development, design, manufacture, or use of conventional weapons, or (3) any other purpose of disturbing international peace and security, and you shall not sell, export, lease, transfer, or release Renesas Electronics products or technologies to any third party whether directly or indirectly with knowledge or reason to know that the third party or any other party will engage in the activities described above. When exporting, selling, transferring, etc., Renesas Electronics products or technologies, you shall comply with any applicable export control laws and regulations promulgated and administered by the governments of the countries asserting jurisdiction over the parties or transactions.
- 10. Please acknowledge and agree that you shall bear all the losses and damages which are incurred from the misuse or violation of the terms and conditions described in this document, including this notice, and hold Renesas Electronics harmless, if such misuse or violation results from your resale or making Renesas Electronics products available any third party.
- 11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

Trademarks

- Linux® is the registered trademark of Linus Torvalds in the U.S. and other countries.
- ARM® is a registered trademark of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved.
- Windows and Windows Media are registered trademarks of Microsoft Corporation in the United States and other countries.
- Android is a trademark of Google Inc. Use of this trademark is subject to Google permissions.
- All other company names and product names mentioned in this manual are registered trademarks or trademarks of their respective companies.
- The registered trademark symbol (®) and trademark symbol (™) are omitted in this manual.

How to Use This Manual

Purpose and Target Reader

This manual is designed to provide the user with an understanding of the interface specifications of the Software product. It is intended for users designing application systems incorporating the Software product. Please refer to the related documents with this product.

Use this Software after carefully reading the precautions. The precautions are stated in the main text of each section, at the end of each section, and in the usage precaution section.

The revision history summarizes major corrections and additions to the previous version. It does not cover all the changes. For details, refer to this manual.

Restrictions on the Use of this Software

This software is MIT license. The certificates from the licensor do not provide any assurances to users that the product performs reliably, intellectual property rights are protected, disputes are resolved by contract, and specifications are not subject to major changes. The user should use this software at his or her own risk.

Permission is hereby granted, free of charge, to any person obtaining a copy of this software and associated documentation files (the "Software"), to deal in the Software without restriction, including without limitation the rights to use, copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the Software, and to permit persons to whom the Software is furnished to do so, subject to the following conditions:

The above copyright notice and this permission notice shall be included in all copies or substantial portions of the Software.

THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE AUTHORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.

| 3. Related Manuals |
|---|
| 4. Technical Terms and Abbreviation |
| |
| |
| |
| |
| |
| |
| |
| |
| |
| All trademarks and registered trademarks are the property of their respective owners. |

- Table of Contents -

| Ι. | OVERVIEW | / | 3 |
|----|--|---|--------------------------|
| | | cations Outline | |
| | • | uration | |
| 2. | SOFTWARE | E SPECIFICATIONS | 8 |
| | 2.1 API spe | ecifications | 8 |
| | 2.2 Comma | and | 9 |
| | 2.2.1 Co | mmand list | 10 |
| | | etail of Command Specifications | |
| | | ures | |
| | | RelTDMrdr type structure | |
| | | RelTDMcap type structure | |
| | | ry Specifications | |
| | | rsistent Area | |
| | | ack Area | |
| | | put Bufferput Buffer | |
| | | itput Buffer | |
| | | Processing | |
| | | ror codes | |
| _ | | | |
| 3. | PROCESSIN | NG FLOW | . 93 |
| | Figure 1-1 Figure 1-2 Figure 2-1 Figure 2-2 Figure 2-3 Figure 2-4 Figure 3-1 | Example of the ADSP System Configuration for TDM renderer function Example of the ADSP System Configuration for capture function API command sequence overview | 6 9 86 86 87 |
| | Table 1-1 Table 1-2 Table 1-3 Table 1-4 Table 2-1 | - List of Tables - | 3 |
| | Table 2-2 Table 2-3 Table 2-4 | Supported TDM Renderer function Specifications Memory Size Requirements Version Information API Functions of TDM Renderer API Functions of TDM Capture List of supported none supported command, subcommand List of Initialization Commands | 4 8 8 10 |

ADSP TDM Renderer/Capture Plugin User's Manual

1 Overview

| Table 2-10 | List of Get commands for capture | 18 |
|------------|--|----|
| Table 2-11 | List of execution commands | 19 |
| Table 2-12 | Structures | 81 |
| Table 2-13 | XARelTDMrdr type structure information | 82 |
| Table 2-14 | XARelTDMcap type structure information | 83 |
| Table 2-15 | Persistent Area Description | 84 |
| | Input Buffer Description | |
| Table 2-17 | Output Buffer Description | 85 |
| Table 2-18 | Error Codes for TDM Renderer | 89 |
| Table 2-19 | Error Codes for TDM Capture | 91 |

1. Overview

This section provides an overview of the Time-Division Multiplexing (TDM) Renderer plugin. It contains TDM renderer and capture function.

1.1 Specifications Outline

TDM Renderer function plays the multiplexing audio signal based on the parameter that was set.

TDM Capture function capture/record the multiplexing audio signal based on the parameter that was set.

Basic Specification Table 1-1

| Item | Description |
|----------|--|
| DSP | Cadence Design Systems, Inc. HiFi2 |
| Compiler | Xtensa C and C++ Compiler (version 12.0.4) |
| Endian | Little Endian |

Table 1-2 Supported TDM Renderer function Specifications

| Table 1-2 Supported 1DM Renderer function Specifications | | | | | |
|--|---|----------------|--------|---------------------------|--|
| Item Description | | | | | |
| Input data format | Cha | Channel number | | PCM bit-width (fix-point) | |
| | | | 16-bit | 24-bit | |
| | 6ch | 3 * 2ch | 0 | 0 | |
| | | 1 * 6ch | 0 | 0 | |
| | 8ch | 4 * 2ch | 0 | 0 | |
| | | 1 * 8ch | 0 | 0 | |
| Output data format | Time-division Multiplexing 16-bit/24-bit linear PCM (fixed point) | | | | |
| Sampling frequency 48000 / 44100 / 32000 | | | | | |
| (Hz) supported | | | | | |
| Number of channels TDM format channel (6 / 8) | | | | | |
| supported | | | | | |
| Reentrant | Supported | | | | |
| Other | Other | | | | |
| Restrictions | | | | | |

Rev. 1.00 Page 3 of 94 Table 1-3 Memory Size Requirements

| Table 1-5 Memory Size Requirements | | | | | |
|------------------------------------|------------------|--|--------------------------|-----------|------------|
| Memory type | Location | Mem | ory area name | Size | (in bytes) |
| Instruction | Instruction area | | | | |
| | ROM | Constant table area Other area(Depended on the compiler) | | | 53599 |
| | | | | | |
| | | Software work | area | _ | 198284 |
| | | Area | Persistent area | Size | 67208 |
| | | breakdown | Scratch area | breakdown | 65536 |
| | | | DTCM area |] [| 65536 |
| | RAM | | Built-in descriptor area | | 4 |
| | (TDM Capture) | User work are | a | _ | 35064 |
| | | Area | Output buffer | Size | 32768 |
| | | breakdown | Structure | breakdown | 1448 |
| Data | | Stack area | | | 848 |
| Data | | Other area(Depended on the compiler) | | | 0 |
| | | Software work | area | _ | 165516 |
| | | Area | Persistent area | Size | 67208 |
| | | breakdown | Scratch area | breakdown | 32768 |
| | | | DTCM area | _ | 65536 |
| | RAM | | Built-in descriptor area | | 4 |
| | (TDM Renderer) | User work area | | _ | 35128 |
| | | Area | Input buffer | Size | 32768 |
| | | breakdown | Structure | breakdown | 1464 |
| | | Stack area | | | 896 |
| | | Other area(De | pended on the compiler) | | 0 |

[Note] Area whose location is shown as ROM in the location column can be included in RAM or ROM.

[Note] Area whose location is shown as RAM in the location column can be included in RAM only.

[Note] Built-in is a memory area to allocate descriptor memory, which need in the DMAC transfer type of plugin.

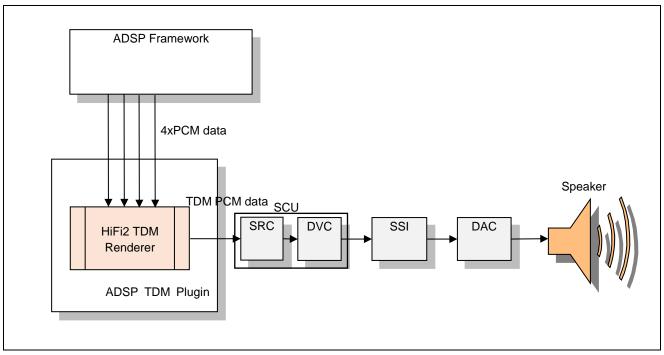
Table 1-4 Version Information

| Item | Description |
|-----------------------------|---------------|
| Library Version information | Version 1.0.0 |
| API Version information | Version 1.0.0 |

Rev. 1.00 Page 4 of 94

Configuration 1.2

Figure 1-1 shows an example of the ADSP system configuration which uses renderer function.



Example of the ADSP System Configuration for TDM renderer function Figure 1-1

Rev. 1.00 Page 5 of 94

Figure 1-1 shows an example of the ADSP system configuration which uses capture function.

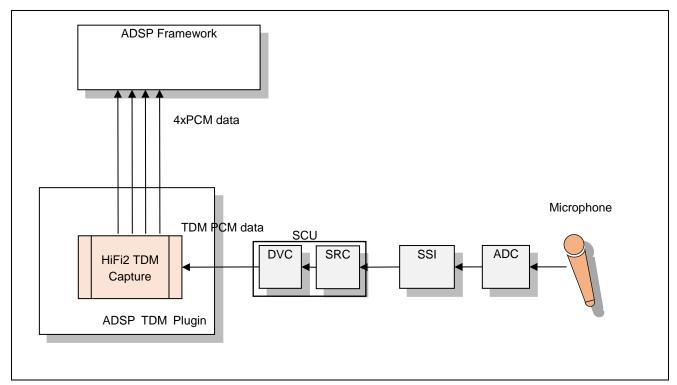


Figure 1-2 Example of the ADSP System Configuration for capture function

1. ADSP Framework

It controls ADSP Plugin. It is software provided separately as Framework.

2. HiFi2 TDM Renderer (ADSP TDM Plugin)

It performs merge multiple input PCM data and output to other audio device. It is this software set up as ADSP TDM Plugin.

3. HiFi2 TDM Capture (ADSP TDM Plugin)

It performs split multiple output PCM data from TDM input received from other audio device. It is this software set up as ADSP TDM Plugin.

4. PCM data

16-bit / 24-bit linear PCM data which is a processing by this software.

5. SCU

It performs sampling rate converters (SRC) and volume control (DVC).

6. SSI (*)

Send or receive audio data interfacing with a variety devices of offering I2C format.

ADSP TDM Renderer/Capture Plugin User's Manual

1 Overview

7. DAC/ADC

The DAC/ADC converts a digital 16-bit/24-bit linear PCM data into analog signal and vice versa.

Note:

(*) Due to development environment (Salvator-X board) does not supports TDM format for input/output hardware. TDM input/output changes to 2 channels.

Rev. 1.00 Page 7 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

Software Specifications 2.

API specifications 2.1

A single interface function is used to access the plugin, with operation specified by command codes. Each library has a single C API call. The parameter definition for every library are same and is specified as below:

In TDM renderer case

Table 2-1 API Functions of TDM Renderer

| xa_rel_tdm_rdr | | | |
|---|--|--|--|
| Description | This API is the only access function to the TDM renderer. | | |
| Syntax | | | |
| | xa_codec_handle_t p_xa_module_obj, | | |
| | WORD32 i_cmd, | | |
| | WORD32 i_idx, | | |
| | pVOID pv_value); | | |
| Parameters p_xa_module_obj : Pointer to opaque API structure. i_cmd : Command. (defined in the supplied header files as) | | | |
| | | | |
| | pv_value : Pointer to the variable used to pass in, or get out properties, from state | | |
| | structure. | | |
| Returns | Error Code based on the success or failure of API command (defined in the supplied header files) | | |

In TDM capture case

API Functions of TDM Capture Table 2-2

| xa_rel_tdm_cap | | | |
|--|---|--|--|
| Description | Scription This API is the only access function to the capture. | | |
| Syntax XA_ERRORCODE xa_rel_tdm_cap(| | | |
| | xa_codec_handle_t p_xa_module_obj, | | |
| | WORD32 i_cmd, | | |
| | WORD32 i_idx, | | |
| | pVOID pv_value); | | |
| Parameters p_xa_module_obj : Pointer to opaque API structure. | | | |
| | i_cmd : Command. (defined in the supplied header files as) | | |
| | i_idx : Command subtype or index. (defined in the supplied header files as) | | |
| pv_value : Pointer to the variable used to pass in, or get out properties, from structure. | | | |
| Returns | Error Code based on the success or failure of API command (defined in the supplied header | | |
| | files) | | |

Rev. 1.00

Page 8 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2 Command

Using API functions of the Table 2-1 and Table 2-2, it performs each processing by a combination of Command/Subcommand.

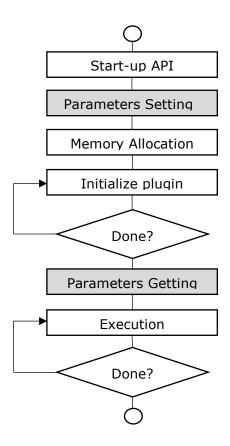


Figure 2-1 API command sequence overview

Rev. 1.00 Page 9 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.1 Command list

Below table presents commands used in renderer and capture case.

Table 2-3 List of supported none supported command, subcommand

| Table 2-3 List of supported none supported command, subcommand | | | | |
|--|--|---|---|--|
| Command | Sub command | | | |
| VA ADI CMD CET LIB ID CTDINCC | XA_CMD_TYPE_LIB_VERSION | 0 | 0 | |
| XA_API_CMD_GET_LIB_ID_STRINGS | XA_CMD_TYPE_API_VERSION | 0 | 0 | |
| XA_API_CMD_GET_API_SIZE | - | 0 | 0 | |
| | XA_CMD_TYPE_INIT_API_PRE_CONFIG_PARAMS | 0 | 0 | |
| VA ADI CAD INIT | XA_CMD_TYPE_INIT_API_POST_CONFIG_PARAMS | 0 | 0 | |
| XA_API_CMD_INIT | XA_CMD_TYPE_INIT_PROCESS | 0 | 0 | |
| | XA_CMD_TYPE_INIT_DONE_QUERY | 0 | 0 | |
| | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | 0 | - | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | 0 | - | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT3* | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL3* | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE | 0 | _ | |
| XA_API_CMD_SET_CONFIG_PARAM | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | _ | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | _ | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | - | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | - | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | _ | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | _ | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | _ | Ō | |
| | XA TDM CAP CONFIG PARAM DMACHANNEL2 | _ | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT3* | _ | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL3* | _ | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | _ | 0 | |
| | XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | - | 0 | |
| | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | 0 | _ | |
| | XA TDM RDR CONFIG PARAM CHANNEL MODE | Ō | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | Ō | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | 0 | _ | |
| | XA TDM RDR CONFIG PARAM DMACHANNEL2 | 0 | _ | |
| | XA TDM RDR CONFIG PARAM OUTPUT3* | 0 | _ | |
| XA_API_CMD_GET_CONFIG_PARAM | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL3* | 0 | _ | |
| | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | 0 | _ | |
| | XA TDM RDR CONFIG PARAM VOLUME RATE | 0 | _ | |
| | XA TDM CAP CONFIG PARAM PCM WIDTH | _ | 0 | |
| | XA TDM CAP CONFIG PARAM CHANNEL MODE | - | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | - | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | _ | Ō | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | _ | Ö | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | _ | 0 | |
| | The Control of the Co | 1 | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | _ | 0 |
|-----------------------------------|---|---|---|
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | - | 0 |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT3* | - | 0 |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL3* | _ | 0 |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | - | 0 |
| | XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | - | 0 |
| XA_API_CMD_GET_MEMTABS_SIZE | - | 0 | 0 |
| XA_API_CMD_SET_MEMTABS_PTR | - | 0 | 0 |
| XA_API_CMD_GET_N_MEMTABS | - | 0 | 0 |
| XA_API_CMD_GET_MEM_INFO_SIZE | - | 0 | 0 |
| XA_API_CMD_GET_MEM_INFO_ALIGNMENT | - | 0 | 0 |
| XA_API_CMD_GET_MEM_INFO_TYPE | - | 0 | 0 |
| XA_API_CMD_SET_MEM_PTR | - | 0 | 0 |
| XA_API_CMD_SET_INPUT_BYTES | - | 0 | 0 |
| XA_API_CMD_INPUT_OVER | - | 0 | 0 |
| XA_API_CMD_GET_CURIDX_INPUT_BUF | - | 0 | - |
| VA ADI CMD EVECLITE | XA_CMD_TYPE_DO_EXECUTE | 0 | 0 |
| XA_API_CMD_EXECUTE | XA_CMD_TYPE_DONE_QUERY | 0 | 0 |
| XA_API_CMD_GET_OUTPUT_BYTES | - | - | 0 |

: Available- : Omitted

* : Not applicable in current library version

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.1.1 Start-up API

Table 2-4 List of Initialization Commands

| up | per stage : Command / lower step : Subcommand | Description | |
|----|---|---|--|
| 1 | XA_API_CMD_GET_LIB_ID_STRINGS | Cat the version of the library | |
| 1 | XA_CMD_TYPE_LIB_VERSION | Get the version of the library | |
| 2 | XA_API_CMD_GET_LIB_ID_STRINGS | Get the version of the API | |
| | XA_CMD_TYPE_API_VERSION | | |
| 3 | XA_API_CMD_GET_API_SIZE | Get the size of the API structure | |
| ٥ | (NULL) | Get the Size of the Art structure | |
| 4 | XA_API_CMD_INIT | Set the default values of all the configuration | |
| 4 | XA_CMD_TYPE_INIT_API_PRE_CONFIG_PARAMS | parameters | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.1.2 Parameters setting

Table 2-5 List of Set Commands for renderer

| | Table 2.5 List of Set Commands for refluerer | | |
|---|--|---|--|
| upper stage : Command / lower step : Subcommand | | Description | |
| 1 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM PCM sample bit width to 16 or 24 | |
| _ | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | Set the input 10M FCM Sample bit width to 10 of 24 | |
| 2 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM PCM channel mode | |
| | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | Set the input 1514 Fell channel mode | |
| 3 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM PCM sampling frequency (supported | |
| | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | 32000/44100/48000 kHz) | |
| 4 | XA_API_CMD_SET_CONFIG_PARAM | Set the input/output frame size | |
| 4 | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | Set the input/output frame size | |
| 5 | XA_API_CMD_SET_CONFIG_PARAM | Set the output destination Audio device 1st for TDM | |
| 3 | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | Renderer | |
| 6 | XA_API_CMD_SET_CONFIG_PARAM | Set ADMA channel number usage for Audio device 1s | |
| 0 | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | (supported Audio-DMAC, Audio-DMAC-pp) | |
| 7 | XA_API_CMD_SET_CONFIG_PARAM | Set the output destination Audio device 2 nd for TDM | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | Renderer | |
| 8 | XA_API_CMD_SET_CONFIG_PARAM | Set ADMA channel number usage for Audio device 2 nd | |
| 0 | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | (supported Audio-DMAC, Audio-DMAC-pp) | |
| 9 | XA_API_CMD_SET_CONFIG_PARAM | Set the output PCM sampling frequency (supported | |
| 9 | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | 32000/44100/48000 kHz) | |
| 1.0 | XA_API_CMD_SET_CONFIG_PARAM | Set the output PCM volume rate compare with input | |
| 10 | XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE | PCM (supported from 0 – 8 times) | |
| | | | |

Rev. 1.00 Page 13 of 94

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

Table 2-6 List of Set Commands for capture

| Table 2-0 List of Set Commands for Capture | | | |
|--|---|---|--|
| | upper stage: Command / lower step: Subcommand | Description | |
| 1 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM DCM comple bit width to 16 or 24 | |
| 1 | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | Set the input TDM PCM sample bit width to 16 or 24 | |
| 2 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM DCM channel made | |
| | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | Set the input TDM PCM channel mode | |
| 3 | XA_API_CMD_SET_CONFIG_PARAM | Set the input TDM PCM sampling frequency (supported | |
| 3 | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | 32000/44100/48000 kHz) | |
| 4 | XA_API_CMD_SET_CONFIG_PARAM | | |
| 4 | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | Set the input/output frame size | |
| 5 | XA_API_CMD_SET_CONFIG_PARAM | Catalla invata anno Andia dania 1st Ca TDM Cantaga | |
| 5 | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | Set the input source Audio device 1st for TDM Captur | |
| 6 | XA_API_CMD_SET_CONFIG_PARAM | Set ADMA channel number usage for Audio device 1s | |
| 0 | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | (supported Audio-DMAC, Audio-DMAC-pp) | |
| 7 | XA_API_CMD_SET_CONFIG_PARAM | Set the input source Audia device 2nd for TDM Conture | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | Set the input source Audio device 2 nd for TDM Capture | |
| 8 | XA_API_CMD_SET_CONFIG_PARAM | Set ADMA channel number usage for Audio device 2nd | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | (supported Audio-DMAC, Audio-DMAC-pp) | |
| 9 | XA_API_CMD_SET_CONFIG_PARAM | Set the output PCM sampling frequency (supported | |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | 32000/44100/48000 kHz) | |
| 10 | XA_API_CMD_SET_CONFIG_PARAM | Set the output PCM volume rate compare with input | |
| | XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | PCM (supported from 0 – 8 times) | |
| | | | |

Rev. 1.00 Page 14 of 94

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

2.2.1.3 Memory allocation

Table 2-7 List of Memory allocation Commands

| upper stage : Command / lower step : Subcommand | | Description | |
|---|---|---|--|
| - | XA_API_CMD_GET_MEMTABS_SIZE | Get the size of the memory structures to be allocated for the plugin tables | |
| 1 | (NULL) | | |
| 2 | XA_API_CMD_SET_MEMTABS_PTR | Pass the memory structure pointer allocated for the | |
| | (NULL) | tables | |
| 3 | XA_API_CMD_INIT | Calculate the required sizes for all the memory | |
| 3 | XA_CMD_TYPE_INIT_API_POST_CONFIG_PARAMS | blocks based on the setting specific parameters | |
| 4 | XA_API_CMD_GET_N_MEMTABS | Obtain the number of memory blocks required by | |
| _ | (NULL) | plugin | |
| 5 | XA_API_CMD_GET_MEM_INFO_SIZE | Get the size of the memory type being referred to | |
| | (NULL) | by the index | |
| 6 | XA_API_CMD_GET_MEM_INFO_ALIGNMENT | Get the alignment information of the memory type being referred to by the index | |
| | (NULL) | | |
| 7 | XA_API_CMD_GET_MEM_INFO_TYPE | Get the type of memory being referred to by the | |
| | (NULL) | index | |
| 8 | XA_API_CMD_SET_MEM_PTR | Set the pointer to the memory allocated for the | |
| 0 | (NULL) | referred index to the input value | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

Initialize plugin 2.2.1.4

List of initialize commands

| | upper stage : Command / lower step : Subcommand | Description | |
|--|--|--|--|
| | XA_API_CMD_SET_INPUT_BYTES | Set the number of bytes available in the input buffer | |
| 1 | (NULL) | | |
| 2 XA_API_CMD_INPUT_OVER Signal to the case | Signal to the plugin the end of the bit stream in renderer | | |
| | (NULL) | case | |
| 3 | XA_API_CMD_INIT | Setup for the HW operation, and initialize state and configuration structure | |
| | XA_CMD_TYPE_INIT_PROCESS | | |
| 4 | XA_API_CMD_INIT | Check if the initialization process has completed | |
| 4 | XA_CMD_TYPE_INIT_DONE_QUERY | | |
| 5 | XA_API_CMD_GET_CURIDX_INPUT_BUF | Cat the number of input buffer bytes consumed | |
| 5 | (NULL) | Get the number of input buffer bytes consumed | |

Rev. 1.00 Page 16 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.1.5 Parameters getting

Table 2-9 List of Get commands for renderer

| upper stage: Command / lower step: Subcommand | | Description | |
|---|---|---|--|
| 1 | XA_API_CMD_GET_CONFIG_PARAM | Cat the input TDM DCM cample hit width | |
| 1 | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | Get the input TDM PCM sample bit width | |
| 2 | XA_API_CMD_GET_CONFIG_PARAM | Cat the input TDM DCM shannel made | |
| 2 | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | Get the input TDM PCM channel mode | |
| 3 | XA_API_CMD_GET_CONFIG_PARAM | Get the input TDM PCM sampling frequency | |
| 3 | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | Get the input 10M PCM sampling frequency | |
| 4 | XA_API_CMD_GET_CONFIG_PARAM | Cot the input/output frame cize | |
| 4 | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | Get the input/output frame size | |
| 5 | XA_API_CMD_GET_CONFIG_PARAM | Cot TDM Dondover output destination Audia device 1st | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | Get TDM Renderer output destination Audio device 1s | |
| 6 | XA_API_CMD_GET_CONFIG_PARAM | Get ADMA channel number usage for Audio device 1 st | |
| U | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | Get ADMA channel number usage for Addio device 1-4 | |
| 7 | XA_API_CMD_GET_CONFIG_PARAM | Cat TDM Dandauar authorit dastination Audia davice 200 | |
| / | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | Get TDM Renderer output destination Audio device 2 ⁿ | |
| 8 | XA_API_CMD_GET_CONFIG_PARAM | Cat ADMA shared growth agreement for Avid's device 20 | |
| 0 | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | Get ADMA channel number usage for Audio device 2 nd | |
| 9 | XA_API_CMD_GET_CONFIG_PARAM | Cat the output DCM compling frequency | |
| 9 | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | Get the output PCM sampling frequency | |
| 1.0 | XA_API_CMD_GET_CONFIG_PARAM | Get the output PCM volume rate compare with input | |
| 10 | XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE | PCM | |

Rev. 1.00 Page 17 of 94

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

Table 2-10 List of Get commands for capture

| | upper stage : Command / lower step : Subcommand | Description | |
|----|---|--|--|
| | | Description | |
| 1 | XA_API_CMD_GET_CONFIG_PARAM | Get the input TDM PCM sample bit width | |
| | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | oct the input 1011 ren sample bit width | |
| 2 | XA_API_CMD_GET_CONFIG_PARAM | Get the input TDM PCM channel mode | |
| | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | Get the input 10M FCM channel mode | |
| 3 | XA_API_CMD_GET_CONFIG_PARAM | Get the input TDM PCM sampling frequency | |
| 5 | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | Get the input 10M FCM sampling frequency | |
| 4 | XA_API_CMD_GET_CONFIG_PARAM | Cat the input/output from a size | |
| 4 | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | Get the input/output frame size | |
| 5 | XA_API_CMD_GET_CONFIG_PARAM | Cot TDM Conture input course Audio device 1st | |
| 5 | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | Get TDM Capture input source Audio device 1 st | |
| 6 | XA_API_CMD_GET_CONFIG_PARAM | Get ADMA channel number usage for Audio device 1 st | |
| 0 | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | Get ADMA channel number usage for Addio device 1 | |
| 7 | XA_API_CMD_GET_CONFIG_PARAM | Get TDM Capture input destination Audio device 2 nd | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | Get 10M Capture input destination Addio device 2 | |
| 8 | XA_API_CMD_GET_CONFIG_PARAM | Get ADMA channel number usage for Audio device 2 nd | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | Get ADMA channel number usage for Addio device 2 | |
| 9 | XA_API_CMD_GET_CONFIG_PARAM | Get the output PCM sampling frequency | |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | Get the output PCM sampling frequency | |
| 10 | XA_API_CMD_GET_CONFIG_PARAM | Get the output PCM volume rate compare with input | |
| | XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | PCM | |

Rev. 1.00 Page 18 of 94

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

2.2.1.6 Execution

Table 2-11 List of execution commands

| upper stage : Command / lower step : Subcommand | | Description | |
|---|---------------------------------|--|--|
| | XA_API_CMD_INPUT_OVER | Signal TDM Renderer/Capture the input data is over | |
| 1 | (NULL) | | |
| 2 | XA_API_CMD_SET_INPUT_BYTES | Set the number of bytes available in the input buffer (only available in TDM Renderer) | |
| - | (NULL) | | |
| 3 | XA_API_CMD_EXECUTE | Everyte TDM Denderer/Centure plugin | |
| ٥ | XA_CMD_TYPE_DO_EXECUTE | Execute TDM Renderer/Capture plugin | |
| 4 | XA_API_CMD_EXECUTE | Check if the execution process has completed | |
| 4 | XA_CMD_TYPE_DONE_QUERY | | |
| 5 | XA_API_CMD_GET_OUTPUT_BYTES | Get the number of bytes output by the plugin in the | |
| | (NULL) | last frame (only available in TDM Capture) | |
| 6 | XA_API_CMD_GET_CURIDX_INPUT_BUF | Get the number of input buffer bytes consumed | |
| 6 | (NULL) | (only available in TDM Renderer) | |

Rev. 1.00 Page 19 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2 Detail of Command Specifications

The next sections describe this library command functions by using the description format below.

| Subcommand | Name of subcommand | |
|--|---|--|
| Synopsis | Outlines the function. | |
| Arguments | guments Describes the arguments for the function. | |
| Restrictions Provides information such as precautions in using the function. | | |

[[]Note] This syntax format complies with ANSI-C.

Rev. 1.00 Page 20 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.1 XA_API_CMD_GET_LIB_ID_STRINGS command

| Subcommand | XA_CMD_TYPE_LIB_VERSION | |
|---|---|---|
| Description | This command obtains the version of the library in the form of a string. The maximum length of the string that the library will provide is 30 bytes. Therefore the application shall pass a pointer to a buffer of a minimum size of 30 bytes. This command is optional | |
| Arguments | p_xa_module_obj | |
| | NULL | |
| | | |
| | i_cmd | |
| | XA_API_CMD_GET_LIB_ID_S | TRINGS |
| | | |
| i_idx | | |
| XA_CMD_TYPE_LIB_VERSION | | |
| | | |
| | pv_value | |
| Pointer to a character buffer in which the version of t | | which the version of the library is returned. |
| Return value | XA_NO_ERROR N | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC p | ov_value is NULL. |
| Restrictions | - | |

Example:

char lib_version[30];
res = (*api_func)(NULL,

XA_API_CMD_GET_LIB_ID_STRINGS, XA_CMD_TYPE_LIB_VERSION, (pVOID) lib_version);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_CMD_TYPE_API_VERSION | |
|--------------|--|-------------------|
| Description | This command obtains the version of the API in the form of a string. The maximum length of the string that the library will provide is 30 bytes. Therefore the application shall pass a pointer to a buffer of a minimum size of 30 bytes. This command is optional. | |
| Arguments | p_xa_module_obj | |
| | NULL | |
| | i_cmd | |
| | XA_API_CMD_GET_LIB_ID_ | STRINGS |
| | | |
| | i_idx | |
| | XA_CMD_TYPE_API_VERSION | |
| | | |
| | pv_value | |
| | Pointer to a character buffer in which the version of the API is returned. | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | pv_value is NULL. |
| Restrictions | ns - | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.2 XA_API_CMD_GET_API_SIZE command

| Subcommand | (None) | |
|--------------|--|--|
| Description | This command is used to obtain the size of the API structure, in order to allocate memory for the API structure. | |
| Arguments | p_xa_module_obj | |
| | NULL | |
| | i_cmd | |
| | XA_API_CMD_GET_API_SIZE | |
| | | |
| | i_idx | |
| | NULL | |
| | pv_value | |
| | Pointer to API size variable. | |
| Return value | XA_NO_ERROR Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC pv_value is NULL. | |
| Restrictions | rictions The application shall allocate memory with an alignment of 4 bytes. | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.3 XA_API_CMD_INIT command

| Subcommand | XA_CMD_TYPE_INIT_API_PRE_CONFIG_PARAMS | | |
|----------------|--|--|--|
| Description | This command is used to set the default value of the configuration parameters. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_INIT | | |
| | i_idx | | |
| | XA_CMD_TYPE_INIT_API_PRE_CONFIG_PARAMS | | |
| | pv_value | | |
| | NULL | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| Restrictions - | | | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_CMD_TYPE_INIT_API_POST_CONFIG_PARAMS | |
|--------------|---|--|
| Description | This command is used to calculate the sizes of all the memory blocks required by the application. It should occur after the plugin specific parameters have been set. | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i_cmd | |
| | XA_API_CMD_INIT | |
| | i_idx | |
| | XA_CMD_TYPE_INIT_API_POST_CONFIG_PARAMS | |
| | pv_value | |
| | NULL | |
| Return value | XA_NO_ERROR | Normally ends. |
| Restrictions | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call before set memory table step) |
| | (in TDM Renderer) XA_TDM_CAP_EXEC_FATAL_INTERNAL (in TDM Capture) XA_TDM_RDR_EXEC_FATAL_INTERNAL (in TDM Renderer) | Invalid connection device setting path (i.e. setting SRC module for both device1 and device2), or lack of memory resource. |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

| Subcommand | XA_CMD_TYPE_INIT_PROCESS | |
|--------------|--|---|
| Description | Setup and start HW operation, and initialize state and configuration structure. No output data is created during initialization. In this state, plugin will check all hardware modules. If a module is busy, plugin will try to establish connection with next available one. If all module are busy, plugin will return error code. | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i_cmd | |
| | XA_API_CMD_INIT | |
| | i_idx | |
| | XA_CMD_TYPE_INIT_PROCESS | |
| | pv_value | |
| | NULL | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_EXEC_FATAL_STATE (in TDM Capture) (XA_TDM_RDR_EXEC_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before post-configuration step or without persistent/scratch buffer allocation, or without DTCM/Built-in descriptor memory allocation (in case of DMAC used)). |
| | XA_TDM_CAP_EXEC_FATAL_INTERNAL (in TDM Capture) XA_TDM_RDR_EXEC_FATAL_INTERNAL (in TDM Renderer) | Plugin has some abnormal cases happened from hardware modules (i.e. all hardware resource is busy). |
| Restrictions | - | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual **2 Software** Specifications

| Subcommand | XA_CMD_TYPE_INIT_DONE_QUERY | | |
|--------------|--|--|--|
| Description | This command checks to see if the initialization process has completed. If it has, the flag value is set to one; else, it is set to zero. A pointer to the flag variable is passed as an argument. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_INIT | | |
| | i_idx | | |
| | XA_CMD_TYPE_INIT_DONE_QUERY | | |
| | pv_value | | |
| | Pointer to flag that indicates the completion of initialization process | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_EXEC_FATAL_STATE (in TDM Capture) (XA_TDM_RDR_EXEC_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before post-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.4 XA_API_CMD_GET_MEMTABS_SIZE command

| Subcommand | None | |
|--------------|--|---|
| Description | This command is used to obtain the size of the table used to hold the memory blocks required for the plugin operation. The API returns the total size of the required table. A pointer to the size variable is sent with this API command and the plugin writes the value to the variable. | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i_cmd | |
| | XA_API_CMD_GET_MEMTABS_SIZE | |
| | i_idx | |
| | NULL | |
| | pv_value | |
| | Pointer to memory size variable | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or | Incorrect sequence call (i.e. call before pre-configuration step) |
| | XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | |
| Restrictions | - | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.5 XA_API_CMD_SET_MEMTABS_PTR command

| Subcommand | None | | |
|--------------|---|---|--|
| Description | This command is used to set the memory structure pointer in the library to the allocated value. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_MEMTABS_PTR | | |
| | i_idx | | |
| | NULL | | |
| | pv_value | | |
| | Allocated pointer | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj or pv_value is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or | Incorrect sequence call (i.e. call before pre-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | | |
| Restrictions | - | | |

memtab_ptr);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.6 XA_API_CMD_GET_N_MEMTABS command

| Subcommand | None | |
|--------------|---|--|
| Description | This command is used to obtain the number of memory blocks needed by the plugin. This value is used as the iteration counter for the allocation of the memory blocks. A pointer to each memory block will be placed in the previously allocated memory tables. The pointer to the variable is passed to the API and the plugin writes the value to this variable. | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i_cmd | |
| | XA_API_CMD_GET_N_MEMTAI | BS |
| | i_idx | |
| | NULL | |
| | pv_value | |
| | Pointer to variable of number of memory blocks required to be allocated | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before post-configuration step) |
| Restrictions | pv_value will be changed depend on channel mode and DMAC transfer type (using ADMAC or DMACPP) | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.7 XA_API_CMD_GET_MEM_INFO_SIZE command

| Subcommand | Memory index | | |
|--------------|--|--|--|
| Description | This command obtains the size of the memory type being referred to by the index. The size in bytes is returned in the variable pointed to by the final argument. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_MEM_INFO_SIZE | | |
| | i_idx | | |
| | Index of the memory 0 - 1 st Input Buffer (TDM Renderer) / 1 st Output Buffer (TDM Capture) 1 - 2 nd Input Buffer (TDM Renderer) / 2 nd Output Buffer (TDM Capture) 2 - 3 rd Input Buffer (TDM Renderer) / 3 rd Output Buffer (TDM Capture) 3 - 4 th Input Buffer (TDM Renderer) / 4 th Output Buffer (TDM Capture) 4 - Persistent Area 5 - Scratch Area 6 - DTMC Area 7 - Built-in Area pv_value | | |
| | | | |
| | Pointer to memory size. | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned 4 bytes | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before post-configuration step) | |
| | XA_API_FATAL_INVALID_CMD_TYPE | Incorrect index | |
| Restrictions | The index of DTCM and built-in area are only used in case of using ADMAC module to transfer data. And the index of input buffer will be affected by channel mode. So it may also affect to the other index memory. | | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.8 XA_API_CMD_GET_MEM_INFO_ALIGNMENT command

| Subcommand | Memory index | | |
|--------------|--|---|--|
| Description | This command gets the alignment information of the memory-type being referred to by the index. The alignment required in bytes is returned to the application. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_MEM_INFO | _ALIGNMENT | |
| | i_idx | | |
| | Index of the memory 0 - 1 st Input Buffer (TDM Renderer) / 1 st Output Buffer (TDM Capture) 1 - 2 nd Input Buffer (TDM Renderer) / 2 nd Output Buffer (TDM Capture) 2 - 3 rd Input Buffer (TDM Renderer) / 3 rd Output Buffer (TDM Capture) 3 - 4 th Input Buffer (TDM Renderer) / 4 th Output Buffer (TDM Capture) 4 - Persistent Area 5 - Scratch Area 6 - DTMC Area 7 - Built-in Area | | |
| | pv_value | | |
| | Pointer to the alignment info variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned 4 bytes | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before post-configuration step) Incorrect index | |
| | XA_API_FATAL_INVALID_CMD_TYPE | | |
| Restrictions | The index of DTCM and built-in area are only used in case of using ADMAC module to transfer data. And the index of input buffer will be affected by channel mode. So it may also affect to the other index memory. | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.9 XA_API_CMD_GET_MEM_INFO_TYPE command

| Subcommand | Memory index | | |
|--------------|--|---|--|
| Description | This command gets the alignment information of the memory-type being referred to by the index. The alignment required in bytes is returned to the application. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_MEM_INFO | _TYPE | |
| | i_idx | | |
| | Index of the memory 0 - 1 st Input Buffer (TDM Renderer) / 1 st Output Buffer (TDM Capture) 1 - 2 nd Input Buffer (TDM Renderer) / 2 nd Output Buffer (TDM Capture) 2 - 3 rd Input Buffer (TDM Renderer) / 3 rd Output Buffer (TDM Capture) 3 - 4 th Input Buffer (TDM Renderer) / 4 th Output Buffer (TDM Capture) 4 - Persistent Area 5 - Scratch Area 6 - DTMC Area 7 - Built-in Area | | |
| | pv_value | | |
| | Pointer to the memory type variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned 4 bytes | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) XA_API_FATAL_INVALID_CMD_TYPE | Incorrect sequence call (i.e. call before post-configuration step) Incorrect index | |
| Restrictions | The index of DTCM and built-in area are only used in case of using ADMAC module to transfer data. And the index of input buffer will be affected by channel mode. So it may also affect to the other index memory. | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.10 XA_API_CMD_SET_MEM_PTR command

| Subcommand | Memory index | | |
|--------------|--|--|--|
| Description | This command passes to the plugin the pointer to the allocated memory. This is then stored in the memory tables structure allocated earlier. For the input and output buffers, it is legitimate to execute this command during the main plugin loop. | | |
| Arguments | p_xa_module_obj Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_MEM_PTR | | |
| | i_idx | | |
| | Index of the memory 0 - 1 st Input Buffer (TDM Renderer) / 1 st Output Buffer (TDM Capture) 1 - 2 nd Input Buffer (TDM Renderer) / 2 nd Output Buffer (TDM Capture) 2 - 3 rd Input Buffer (TDM Renderer) / 3 rd Output Buffer (TDM Capture) 3 - 4 th Input Buffer (TDM Renderer) / 4 th Output Buffer (TDM Capture) 4 - Persistent Area 5 - Scratch Area 6 - DTMC Area 7 - Built-in Area | | |
| | pv_value Pointer to the memory block | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. pv_value is not aligned to required alignment for the requested memory block. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE (in TDM Capture) Or | Incorrect sequence call (i.e. call before post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE (in TDM Renderer) | | |
| | XA_API_FATAL_INVALID_CMD_TYPE | Incorrect index | |
| Restrictions | The index of DTCM and built-in area are only used in case of using ADMAC module to transfer data. And the index of input buffer will be affected by channel mode. So it may also affect to the other index memory. | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.11 XA_API_CMD_INPUT_OVER command

| Subcommand | None | | |
|--------------|---|---|--|
| Description | This command is used to tell the plugin that the input signal is over. The execution or initialization step will continue in loop until it all the remaining input data is processed. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_INPUT_OVER | | |
| | i_idx | | |
| | NULL | | |
| | pv_value | | |
| | NULL | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_EXEC_FATAL_STATE (in TDM Capture) Or | Incorrect sequence call (i.e. call before initialization step – init process) | |
| | XA_TDM_RDR_EXEC_FATAL_STATE (in TDM Renderer) | | |
| Restrictions | - | | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.12 XA_API_CMD_SET_INPUT_BYTES command

| Subcommand | None | | |
|--------------|---|---|--|
| Description | In TDM Capture this command will do nothing. The purpose of this command is filled the full list of standard API. In TDM Renderer this command will set number of bytes available in the input buffer. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_INPUT_BYTES | | |
| | i_idx The index of input buffer (only for TDM Renderer) pv_value | | |
| | | | |
| | | | |
| | Pointer to the input byte variable (Any value is OK with TDM Capture) | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes | |
| | XA_TDM_RDR_EXEC_FATAL_STATE (only for TDM Renderer) | Input buffer is not ready, and have not init done | |
| | XA_API_FATAL_INVALID_CMD_TYPE (only for TDM Renderer) | Incorrect index of input buffer | |
| | XA_TDM_RDR_EXEC_FATAL_INPUT (only for TDM Renderer) | Invalid input buffer size (i.e. minus buffer size or buffer size is not align with sample size) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.13 XA_API_CMD_GET_CURIDX_INPUT_BUF command

| Subcommand | None | | |
|--------------|--|--|--|
| Description | In TDM Capture, this command will return value 0 each time it's called In TDM Renderer, this command will return number of input buffer bytes consumed | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CURIDX_INPUT_ | BUF | |
| | i_idx | | |
| | The index of input buffer (only for TDM Renderer) pv_value | | |
| | | | |
| | Pointer to number variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_EXEC_FATAL_STATE (only for TDM Renderer) | Input buffer is not ready | |
| | XA_API_FATAL_INVALID_CMD_TYPE (only for TDM Renderer) | Invalid index of input buffer | |
| Restrictions | - | | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.14 XA_API_CMD_EXECUTE command

| Subcommand | XA_CMD_TYPE_DO_EXECUTE | | |
|--------------|---|--|--|
| Description | This command execute the TDM Renderer/Capture plugin. | | |
| Arguments | p_xa_module_obj Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_EXECUTE | | |
| | i_idx | | |
| | XA_CMD_TYPE_DO_EXECUTE | | |
| | pv_value | | |
| | NULL | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_EXEC_FATAL_STATE (in TDM Capture) Or XA_TDM_RDR_EXEC_FATAL_STATE (in TDM Renderer) | Incorrect sequence call (i.e. call before initialization step) Or input / output buffer is not ready | |
| | XA_TDM_CAP_EXEC_FATAL_INTERNAL (in TDM Capture) Or XA_TDM_RDR_EXEC_FATAL_INTERNAL (in TDM Renderer) | Hardware does not stop successfully | |
| Restrictions | - | <u> </u> | |

Example:

| Subcommand | XA_CMD_TYPE_DONE_QUERY | | |
|--------------|---|--|--|
| Description | This command checks to see if the end of processing has been reached. If it is, the flag value is set to 1; else, it is set to zero. The pointer to the flag is passed as an argument. Processing by the plugin can continue for several invocations of the DO_EXECUTE command after the last input data has been passed to the plugin, so the application should not assume that the plugin has finished generating all its output until so indicated by this command. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_EXECUTE | | |
| | | | |
| | i_idx | | |
| | XA_CMD_TYPE_DONE_QUERY | | |
| | | | |
| | pv_value | | |
| | Pointer to the flag variable | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_EXEC_FATAL_STATE (in TDM Capture) Or | Incorrect sequence call (i.e. call before initialization step) | |
| | XA_TDM_RDR_EXEC_FATAL_STATE (in TDM Renderer) | | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.15 XA_API_CMD_GET_OUTPUT_BYTES command

| Subcommand | None | | |
|--------------|--|---|--|
| Description | In TDM Renderer, this command will do nothing. The purpose of this command is fulfilled the standard APIs list. In TDM Capture, this command obtains the number of bytes output by the plugin during the last execution. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_OUTPUT_BYTES | | |
| | i_idx | | |
| | The index of output buffer (only for TDM Capture) | | |
| | pv_value Pointer to the flag variable | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj or pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_EXEC_FATAL_STATE (only for TDM Capture) XA_API_FATAL_INVALID_CMD_TYPE (only for TDM Capture) | Incorrect sequence call (i.e. call before initialization step) Or output buffer is not ready Invalid index of output buffer | |
| Restrictions | - | • | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.16 XA_API_CMD_SET_CONFIG_PARAM command

2.2.2.16.1 Set configuration command for TDM Renderer

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | | |
|--------------|--|---|--|
| Description | Set the TDM PCM sample bit width to 16 or 24 bits | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i and | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | | |
| | | | |
| | pv_value | | |
| | Pointer to the sample bit width variable (valid value: 16 or 24) | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_PCM_WIDTH | TDM PCM sample bit width is invalid | |
| Restrictions | - | • | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | | |
|--------------------------------------|--|---|--|
| Description | Set TDM PCM channels mode | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx | | |
| XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | | | |
| | pv_value | | |
| | Pointer to the TDM channels mode variable XA_TDM_RDR_CHANNEL_MODE_2X4: 4 stereo TDM data XA_TDM_RDR_CHANNEL_MODE_1X8: 1 eight-channel TDM data | | |
| | XA_TDM_RDR_CHANNEL_MODE_2X3: 3 stereo TDM data XA_TDM_RDR_CHANNEL_MODE_1X6: 1 six-channel TDM data | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_CHANNEL_MODE | Invalid TDM format | |
| Restrictions | - | | |

```
Example:
WORD32 ch_mode;
res = (*api_func)(api_obj,
               XA_API_CMD_SET_CONFIG_PARAM,
```

XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE, &ch_mode);

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RA | ATE |
|--------------|---|---|
| Description | Set input TDM PCM sampling frequency | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | | |
| | i_cmd | |
| | XA_API_CMD_SET_CONFIG_PARAM | |
| | | |
| | i_idx | |
| | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | |
| | | |
| | pv_value | |
| | Pointer to the input sampling frequency variable (valid value: 32,000 / 44,100 / 48,000 Hz) | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_RDR_CONFIG_FATAL_SAMPLE_RATE | Input TDM PCM sampling frequency is out of range. |
| Restrictions | - | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | | |
|--------------|---|---|--|
| Description | Set input/output TDM PCM frame size in sample | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE pv_value | | |
| | | | |
| | | | |
| | Pointer to frame size in sample variable (valid value: 512 / 1024 / 2048) | | |
| | , , | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_FRAME_SIZE | TDM PCM frame size value is out of range. | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | | |
|--------------|--|----------------------|---|
| Description | Set 1 st output destination device for TDM Renderer. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CON | NFIG_PARAM | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG | _PARAM_OUTPUT1 | |
| | | | |
| | pv_value | | |
| | Pointer to output destin | ation value variable | |
| Return value | XA_NO_ERROR | | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALI | GN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_RDR_CONFIG_F | FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_RDR_CONFIG_FATAL_INVALID_OUTPUT TDM PCM output device is out range. | | |
| Restrictions | List of supported module | : | |
| | Macro | Value | |
| | SSI00 | 0 | |
| | SSI10 | 10 | |
| | SSI20 | 20 | - |
| | SSI30 SSI40 | 30 40 | - |
| | SSI90 | 90 | 1 |
| | SCU_SRCI0 | 110 | 1 |
| | SCU_SRCI1 | 111 | 1 |
| | SCU_SRCI3 | 113 | 1 |
| | SCU_SRCI4 | 114 | |

Example:

WORD32 output_dev;
res = (*api_func)(api_obj,

`XA_API_CMD_SET_CONFIG_PARAM,

XA_TDM_RDR_CONFIG_PARAM_OUTPUT1,

&output_dev);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | | |
|--------------|--|---|--|
| Description | Set ADMA channel number usage for 1 st Audio device. | | |
| Arguments | p_xa_module_obj Pointer to API Structure. | | |
| | | | |
| | i cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-perip ADMAC CH[0-31] : Audio-I | heral-peripheral channels number DMAC usage | |
| | | DMACpp usage | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_DMACHANNEL | TDM PCM ADMA channel setting is out of range. | |
| Restrictions | - | | |

Example:

WORD32 dma_channel; res = (*api_func)(api_obj,

XA_API_CMD_SET_CONFIG_PARAM, XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1,

&dma_channel);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | | |
|--------------|--|----------------------|---|
| Description | Set 2 nd output destination device for TDM Renderer. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CON | NFIG_PARAM | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG | _PARAM_OUTPUT2 | |
| | | | |
| | pv_value | | |
| | Pointer to output destin | ation value variable | |
| Return value | XA_NO_ERROR | | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALI | | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_RDR_CONFIG_F | ATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_RDR_CONFIG_FATAL_INVALID_OUTPUT TDM PCM output device is out range. | | |
| Restrictions | List of supported module | : | |
| | Macro | Value | |
| | SSI00 | 0 | |
| | SSI10 | 10 | |
| | SSI20 | 20 | |
| | SSI30 | 30 | - |
| | SSI40 SSI90 | 90 | - |
| | SCU_SRCIO | 110 | - |
| | SCU_SRCI1 | 111 | 1 |
| | SCU SRCI3 | 113 | 1 |
| | SCU_SRCI4 | 114 | 1 |

Example:

WORD32 output_dev;
res = (*api_func)(api_obj,

XA_API_CMD_SET_CONFIG_PARAM,

XA_TDM_RDR_CONFIG_PARAM_OUTPUT2,

&output_dev);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | | |
|--------------|--|---|--|
| Description | Set ADMA channel number usage for 2 nd Audio device. | | |
| Arguments | p_xa_module_obj Pointer to API Structure. | | |
| | | | |
| | i cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-perip ADMAC CH[0-31] : Audio-I | heral-peripheral channels number DMAC usage | |
| | | DMAC-pp usage | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_DMACHANNEL | TDM PCM ADMA channel setting is out of range. | |
| Restrictions | - | - | |

Example:

WORD32 dma_channel; res = (*api_func)(api_obj,

XA_API_CMD_SET_CONFIG_PARAM, XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2,

&dma_channel);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | | |
|--------------|---|---|--|
| Description | Set output sample rate in Sampling Rate Converter (SRC) of Sampling Rate Converter Unit (SCU). If this setting is valid and non-zero value, SRC connection will be enabled even without setting connection device path. And the connection will automatically use the available Audio-DMAC channel. If this setting is zero, SRC module will not be used. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE pv_value Pointer to the output sampling frequency variable. Valid value: 0: disable SRC module 32,000 / 44,100 / 48,000 Hz: setting output sampling rate for SRC module | | |
| | | | |
| | | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_RDR_CONFIG_FATAL_SAMPLE_RATE | TDM PCM output sample rate is out of range. | |
| Restrictions | - | | |

Example:

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA TDM RDR CONFIG PARAM VOLUME RATE | | |
|--------------|---|---|--|
| | | | |
| Description | Set the output PCM volume rate in Digital Volume and Mute Function (DVC) of Sampling Rate Converter Unit (SCU). Any setting values except 0xFFFF FFFF (disable) will enabled DVC of SCU module and the connection will be established even without setting connection path. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE pv_value | | |
| | | | |
| | | | |
| | oint Q3.20): | | |
| Return value | [0, 0x7F FFFF]: setting volume rate value XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | TDM PCM volume rate value is out of range. | | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.16.2 Set configuration command for TDM Capture

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | | | |
|--------------|--|---|--|--|
| Description | Set TDM PCM sample bit width to 16 or 24 bits | | | |
| Arguments | p_xa_module_obj | | | |
| | Pointer to API Structure. | | | |
| | i_cmd | | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | | |
| | i_idx | | | |
| | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH pv_value | | | |
| | | | | |
| | Pointer to the sample bit width variable (valid value: 16 or 24) | | | |
| Return value | XA_NO_ERROR | Normally ends. | | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | | |
| | XA_TDM_CAP_CONFIG_FATAL_PCM_WIDTH | TDM PCM sample width size is out of range. | | |
| Restrictions | - | | | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | | |
|--------------|---|---|--|
| Description | Set TDM PCM channels mode | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | | |
| | pv_value | | |
| | Pointer to the TDM channels mode variable XA_TDM_CAP_CHANNEL_MODE_2X4: 4 stereo TDM data XA_TDM_CAP_CHANNEL_MODE_1X8: 1 eight-channel TDM data XA_TDM_CAP_CHANNEL_MODE_2X3: 3 stereo TDM data XA_TDM_CAP_CHANNEL_MODE_1X6: 1 six-channel TDM data | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_CAP_CONFIG_FATAL_CHANNEL_MODE | Invalid TDM format | |
| Restrictions | - | • | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | | |
|--------------|--|---|--|
| Description | Set input sample rate in Sampling Rate Converter (SRC) of Sampling Rate Converter Unit (SCU). If this setting is valid and non-zero value, SRC connection will be enabled even without setting connection device path. And the connection will automatically use the available Audio-DMAC channel. If this setting is zero, SRC module will not be used. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | | | |
| | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | | |
| | pv_value | | |
| | Pointer to the input sampling frequency variable. Valid value: 0: disable SRC module 32,000 / 44,100 / 48,000 Hz: setting input sampling rate for SRC module | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_CAP_CONFIG_FATAL_SAMPLE_RATE | Input TDM PCM sampling frequency is out of range. | |
| Restrictions | - | | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | | |
|--------------|---|---|--|
| Description | Set input/output TDM PCM frame size in sample | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | | |
| | mu value | | |
| | pv_value | | |
| | Pointer to frame size variable (valid value: 512 / 1024 / 2048) | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_CAP_CONFIG_FATAL_FRAME_SIZE | TDM PCM frame size value is out of range. | |
| Restrictions | - | | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | | |
|--------------|--|------------------|---|
| Description | Set 1 st input source device for TDM Capture | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONF | IG_PARAM | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_F | PARAM_INPUT1 | |
| | | | |
| | pv_value | | |
| | Pointer to the input devic | e value variable | |
| Return value | XA_NO_ERROR | | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_CAP_CONFIG_FATAL_INVALID_INPUT TDM PCM input device is out or | | |
| Restrictions | List of supported module: | | |
| | Macro | Value | |
| | | 0 | |
| | SSI10 10 | | |
| | | 20 | |
| | | 30 40 | |
| | | 90 | |
| | | 110 | |
| | | 111 | |
| | | 113 | |
| | | 114 | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | | |
|--------------|--|---|--|
| Description | Set ADMA channel number usage for 1 st Audio device. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral channels nu ADMAC CH[0-31] : Audio-DMAC usage | | |
| | | io-DMAC-pp usage | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_CAP_CONFIG_FATAL_DMACHANNEL | TDM PCM ADMA channel setting is out of range. | |
| Restrictions | - | | |

| Subcommand | XA_TDM_CAP_CONFIG_PAR | RAM_INPUT2 | |
|--------------|---|------------------|---|
| Description | Set 2 nd input source device | for TDM Capture | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFI | IG_PARAM | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_P/ | ARAM_INPUT2 | |
| | | | |
| | pv_value | | |
| | Pointer to the input device | e value variable | |
| Return value | XA_NO_ERROR | | Normally ends. |
| | XA_API_FATAL_MEM_ALLO | C | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FAT | AL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_CAP_CONFIG_FAT | AL_INVALID_INPUT | TDM PCM input device is out of range. |
| Restrictions | List of supported module: | | L |
| | | /alue | |
| | SSI00 0 | | |
| | | 10 | |
| | | 20 30 | |
| | | 10 | |
| | | 90 | |
| | | 110 | |
| | | 11 | |
| | | .13 | |
| | SCU_SRCI4 1 | 14 | |

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | | |
|--------------|---|---|--|
| Description | Set ADMA channel number usage for 2 nd Audio dev | rice. | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral channels number XA_TDM_CAP_ADMAC_CH[0-31] : Audio-DMAC usage XA_TDM_CAP_ADMACPP_CH[0-28] : Audio-DMAC-pp usage | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_CAP_CONFIG_NONFATAL_ERR_DMACHANNEL | TDM PCM ADMA channel setting is out of range. | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | |
|--------------|--|---|
| Description | Set the PCM sampling frequency. | |
| Arguments | p_xa_module_obj Pointer to API Structure. | |
| | | |
| | i_cmd | |
| | XA_API_CMD_SET_CONFIG_PARAM | |
| | i_idx | |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | |
| | pv_value | |
| | Pointer to the output sampling frequency variable. Valid value: (32,000 / 44,100 / 48,000 Hz) | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) |
| | XA_TDM_CAP_CONFIG_FATAL_SAMPLE_RATE | TDM PCM output sample rate is out of range. |
| Restrictions | - | |

Example:

WORD32 sample_rate; res = (*api_func)(api_obj,

XA_API_CMD_SET_CONFIG_PARAM,

XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE,

&sample_rate);

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | | |
|--------------|---|---|--|
| Description | Set the output PCM volume rate in Digital Volume and Mute Function (DVC) of Sampling Rate Converter Unit (SCU). Any setting values except 0xFFFF FFFF (disable) will enabled DVC of SCU module and the connection will be established even without setting connection path. | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_SET_CONFIG_PARAM | | |
| | i_idx XA_TDM_CAP_CONFIG_PARAM_VOLUME_RATE | | |
| | | | |
| | pv_value | | |
| | Pointer to the volume ratio number (using Fix-point Q3.20): 0xFFFF FFFF : disable DVC module [0, 0x7F FFFF]: setting volume rate value | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step or call after post-configuration step) | |
| | XA_TDM_CAP_CONFIG_FATAL_VOLUME_RATE | TDM PCM volume rate value is out of range. | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.17 XA_API_CMD_GET_CONFIG_PARAM command

2.2.2.16.1 Get configuration command for TDM Renderer

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | |
|--------------|--|---|
| Description | Get the TDM PCM sample bit width setting | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i_cmd | |
| | XA_API_CMD_GET_CONFIG_PARAM | |
| | i idv | |
| i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_PCM_WIDTH | |
| | pv_value | |
| | Pointer to the sample bit width variable | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) |
| Restrictions | - | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MODE | | |
|--------------|--------------------------------------|---|--|
| Description | Get TDM PCM channels mode setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_CHANNEL_MOI | DE | |
| pv_value | | | |
| | Pointer to the TDM channels mode | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RAT | E | |
|--------------|--|---|--|
| Description | Get input TDM PCM sampling frequency setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_IN_SAMPLE_RATE | | |
| | | | |
| | pv_value | | |
| | Pointer to the input sampling frequency variable | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | | |
|--------------|---|--|--|
| Description | Get input/output TDM PCM frame size in sample setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_FRAME_SIZE | | |
| | | | |
| | pv_value Pointer to frame size in sample variable | | |
| | | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call | |
| Restrictions | - | before pre-configuration step) | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | | |
|--------------|---|---|--|
| Description | Get 1 st output destination device for TDM Renderer info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| i_cmd | | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_OUTPUT1 | | |
| | pv_value | | |
| | Pointer to output destination value variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | | |
|--|---|---|--|
| Description | Get ADMA channel number usage for 1 st Audio device info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL1 | | |
| | pv_value | | |
| Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral chann | | pheral-peripheral channels variable | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | | |
|---------------------------------|---|---|--|
| Description | Get 2 nd output destination device for TDM Renderer info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | | | |
| XA_TDM_RDR_CONFIG_PARAM_OUTPUT2 | | 2 | |
| | pv_value Pointer to output destination value variable | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

WORD32 output_dev;

res = (*api_func)(api_obj,

XA_API_CMD_GET_CONFIG_PARAM,

API_CMD_GET_CONFIG_PARAM,

OUT XA_TDM_RDR_CONFIG_PARAM_OUTPUT2,

&output_dev);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | | |
|--------------|--|---|--|
| Description | Get ADMA channel number usage for 2 nd Audio device info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral channels variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

WORD32 dma_channel;

res = (*api_func)(api_obj,

XA_API_CMD_GET_CONFIG_PARAM,

API_CMD_GET_CONFIG_PARAM,

API_CMD_GET_CONFI

XA_TDM_RDR_CONFIG_PARAM_DMACHANNEL2,

&dma_channel);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | | |
|--------------|---|---|--|
| Description | Get output sample rate setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE | | |
| | | | |
| | | | |
| pv_value | | | |
| | Pointer to the output sampling frequency variable | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

WORD32 sample_rate;

res = (*api_func)(api_obj,

XA_API_CMD_GET_CONFIG_PARAM,

A_API_CMD_GET_CONFIG_PARAM,

A_API_CMD_GET_CONFIG_PARAM,

A_API_CMD_GET_CONFIG_PARAM,

A_API_CMD_GET_CONFIG_PARAM,

XA_TDM_RDR_CONFIG_PARAM_OUT_SAMPLE_RATE,

&sample_rate);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE | | |
|--------------|--|---|--|
| Description | Get the output PCM volume rate setting value | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE | | |
| | | | |
| | pv_value | | |
| | Pointer to the volume ratio number (using Fix-point Q3.20) | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_RDR_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

WORD32 vol_rate;

res = (*api_func)(api_obj,

XA_API_CMD_GET_CONFIG_PARAM,

XA_TDM_RDR_CONFIG_PARAM_VOLUME_RATE,

&vol_rate);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.2.2.16.2 Get configuration command for TDM Capture

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | |
|--------------|--|---|
| Description | Get TDM PCM sample bit width setting | |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | i cmd | |
| | XA_API_CMD_GET_CONFIG_PARAM | |
| | | |
| | i_idx XA_TDM_CAP_CONFIG_PARAM_PCM_WIDTH | |
| | pv value | |
| | Pointer to the sample bit width variable | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) |
| Restrictions | - | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE | | |
|--------------|---|---|--|
| Description | Get TDM PCM channels mode setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx XA_TDM_CAP_CONFIG_PARAM_CHANNEL_MODE pv_value | | |
| | | | |
| | | | |
| | Pointer to the TDM channels mode variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | | |
|--------------|--|---|--|
| Description | Get the PCM sampling frequency setting value | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE | | |
| | | | |
| | | | |
| | pv_value Pointer to the input sampling frequency variable | | |
| | | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example

WORD32 sample_rate;

res = (*api_func)(api_obj,

XA_API_CMD_GET_CONFIG_PARAM,

XA_TDM_CAP_CONFIG_PARAM_IN_SAMPLE_RATE,

&sample_rate);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | | |
|--------------|---|---|--|
| Description | Get input/output TDM PCM frame size in sample setting | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx XA_TDM_CAP_CONFIG_PARAM_FRAME_SIZE | | |
| | | | |
| | | | |
| | | | |
| | pv_value Pointer to frame size variable | | |
| | | | |
| | | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | | |
|---|--|---|--|
| Description | Get 1 st input source device for TDM Capture info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT1 | | |
| | pv_value | | |
| Pointer to the input destination value variable | | riable | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | | |
|--------------|--|---|--|
| Description | Get ADMA channel number usage for 1st Audio device info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL1 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral channels variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | | |
|--------------|--|---|--|
| Description | Get 2 nd input source device for TDM Capture info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | : and | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_INPUT2 | | |
| | | | |
| | pv_value | | |
| | Pointer to the input destination value varia | able | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | | |
|--------------|--|---|--|
| Description | Get ADMA channel number usage for 2 nd Audio device info | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd XA_API_CMD_GET_CONFIG_PARAM | | |
| | | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_DMACHANNEL2 | | |
| | pv_value | | |
| | Pointer to the Audio-DMAC / Audio-DMAC-peripheral-peripheral channels variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | | |
|--------------|---|---|--|
| Description | Get output sample rate setting value | | |
| Arguments | p_xa_module_obj | | |
| | Pointer to API Structure. | | |
| | i_cmd | | |
| | XA_API_CMD_GET_CONFIG_PARAM | | |
| | i_idx | | |
| | XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE | | |
| | pv_value | | |
| | Pointer to the output sampling frequency variable | | |
| Return value | XA_NO_ERROR | Normally ends. | |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. | |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. | |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) | |
| Restrictions | - | | |

Example:

WORD32 sample_rate;

res = (*api_func)(api_obj, XA_API_CMD_GET_CONFIG_PARAM, XA_TDM_CAP_CONFIG_PARAM_OUT_SAMPLE_RATE,

&sample_rate);

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| Subcommand | XA_TDM_CAP_CONFIG_PARAM_VOLUME_R | ATE |
|--------------|--|---|
| Description | Get the output PCM volume rate setting val | ue |
| Arguments | p_xa_module_obj | |
| | Pointer to API Structure. | |
| | | |
| | i_cmd | |
| | XA_API_CMD_GET_CONFIG_PARAM | |
| | | |
| | i_idx | |
| | XA_TDM_CAP_CONFIG_PARAM_VOLUME_ | _RATE |
| | | |
| | pv_value | |
| | Pointer to the volume ratio number (using | g Fix-point Q3.20) |
| | | |
| Return value | XA_NO_ERROR | Normally ends. |
| | XA_API_FATAL_MEM_ALLOC | p_xa_module_obj / pv_value is NULL. |
| | XA_API_FATAL_MEM_ALIGN | p_xa_module_obj is not aligned to 4 bytes. |
| | XA_TDM_CAP_CONFIG_FATAL_STATE | Incorrect sequence call (i.e. call before pre-configuration step) |
| Restrictions | - | 1, |

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.3 Structures

Table 2-12 lists the structures for this software. The user should reserve areas required for these structures. For detailed specifications of these input structures, refer to Section 2.3.1.

Table 2-12 Structures

| Structure name | Size | Outline |
|----------------|------------|--|
| XARelTDMrdr | 1464 bytes | API's structure to stores the information of API |
| XARelTDMcap | 1448 bytes | API's structure to stores the information of API |

Rev. 1.00 Page 81 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.3.1 XARelTDMrdr type structure

The XAReITDMrdr type structure is the work area used by the TDM Renderer of TDM plugin. When using this plugin, secure the area with the application program. It's not necessary to refer to this area because it only contains the internal variables and working buffers of the plugin. Make sure not to change the value of this area with the application program.

Table 2-13 XARelTDMrdr type structure information

| Member name | Outline | |
|---------------------------------|---|--|
| pVOID pMem_tabs | Pointer to memory tables | |
| WORD32 persist_size | Size of persistent memory | |
| WORD32 descript_size | Descriptor memory size | |
| WORD32 ring_size | Total size of ring buffer in sample | |
| WORD32 sample_size | Size of PCM sample in byte (respect channels and PCM width) | |
| WORD32 input_total | Number of input port based on channels mode of TDM plugin | |
| WORD32 channels | Format channel of input PCM data | |
| relTDMrdr_Parameters parameters | Parameter structure of TDM renderer plugin | |
| DMAC_SETTING dma_params | ADMAC parameters structure | |
| WORD32 output1_type | 1 st audio device type | |
| WORD32 output2_type | 2 nd audio device type | |
| WORD32 dma1_type | 1 st DMAC connection type | |
| WORD32 dma2_type | 2 nd DMAC connection type | |
| SSIU_SSI_MODULE ssi_module | SSI module information | |
| SRC_MODULES src_module | SRC module information | |
| CMD_MODULE cmd_module | CMD module information | |
| Fifo_modules fifo_module | FIFO module information | |
| WORD32 state | TDM renderer state | |
| WORD32 dmac_stage | ADMAC stage flag | |
| WORD32 hw_module | Store module information used in plugin | |
| WORD32 write_idx | FIFO writing position | |
| WORD32 read_idx | Software reading position | |
| WORD32 filled | Number of sample present in the buffer | |
| WORD32 merging_count | Number of bytes plugin has written into scratch area | |
| WORD32 port_filled[4] | Number of bytes port has been submitted from user | |
| WORD32 merging_done | Flag to tell plugin that merging process is done or not yet | |
| WORD32 consumed[4] | Number of byte consumed in each port | |
| XosEvent relrdr_event | TDM Renderer polling event | |
| XosThread relrdr_thread | TDM Renderer polling thread | |
| UWORD32 flag_24to16 | Flag for converting 24pcm data to 16pcm data | |
| UWORD32 src_sample_size | Sample size of input source data | |

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.3.2 XARelTDMcap type structure

The XAReITDMcap type structure is the work area used by the TDM Capture of TDM plugin. When using this plugin, secure the area with the application program. It's not necessary to refer to this area because it only contains the internal variables and working buffers of the plugin. Make sure not to change the value of this area with the application program.

Table 2-14 XARelTDMcap type structure information

| Member name | e structure information Outline |
|---------------------------------|---|
| pVOID pMem_tabs | Pointer to memory tables |
| WORD32 persist_size | Size of persistent memory |
| WORD32 descript_size | Descriptor memory size |
| WORD32 ring_size | Total size of ring-buffer in sample |
| WORD32 sample_size | Size of PCM sample in byte (respect channels and PCM width) |
| WORD32 output_total | Number of output port based on channel mode of plugin |
| WORD32 channels | Format channel of input PCM data |
| relTDMcap_Parameters parameters | Parameter structure of TDM Capturer plugin |
| DMAC_SETTING dma_params | ADMAC parameters structure |
| WORD32 input1_type | 1 st audio device type |
| WORD32 input2_type | 2 nd audio device type |
| WORD32 dma1_type | 1st DMAC connection type |
| WORD32 dma2_type | 2 nd DMAC connection type |
| SSIU_SSI_MODULE ssi_master | SSI module master information |
| SSIU_SSI_MODULE ssi_slave | SSI module slaver information |
| SRC_MODULES src_module | SRC module information |
| CMD_MODULE cmd_module | CMD module information |
| Fifo_modules fifo_module | FIFO module information |
| WORD32 state | TDM Capture plugin current state |
| WORD32 dmac_stage | ADMAC stage flag |
| WORD32 hw_module | Store module information used in plugin |
| WORD32 head_idx | Head index of ring buffer |
| WORD32 tail_idx | Tail index of ring buffer |
| WORD32 filled | Number of samples present in the buffer |
| WORD32 transfered_idx | Index of output port has been transferred in the last execution |
| WORD32 transferred[4] | Number of byte transferred by plugin for each port |
| XosEvent relcap_event | TDM Capture polling event |
| XosThread relcap_thread | TDM Capture polling thread |
| UWORD32 flag_24to16 | Flag for converting 24pcm data to 16pcm data |
| UWORD32 dst_sample_size | Sample size of output destination data |

Rev. 1.00 Page 83 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

Memory Specifications 2.4

This section describes the memory areas used by this software.

2.4.1 Persistent Area

Table 2-15 Persistent Area Description

| 10010 2 10 | Sisteric 7 ii ca Description |
|------------------|--|
| Item | Area which always holds values when this software is used. If the user manipulates this area after initialization, the correct execution of this software is not ensured. |
| Symbol name | - (freely defined by the user) |
| Size | Obtain the actually required size with 2.2.2.7 |
| Area reservation | The user should reserve this area. |
| Allocation | This area is included in RAM. |
| Alignment | Align this area on a 4-byte boundary. |

2.4.2 Stack Area

This software does not use a stack area.

2.4.3 Heap Area

This software does not use a heap area.

Rev. 1.00 Page 84 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.4.4 Input Buffer

Input buffer only is used in the TDM Renderer case.

Table 2-16 Input Buffer Description

| Item | Area which stores inputs from this software. The input buffer contains 16-bit or 24-bit linear PCM data (hereinafter called PCM data). If the user manipulates this area during rendering processing, the normal execution of the program cannot be ensured. |
|------------------|--|
| Symbol name | - (freely defined by the user) |
| Size | Please secure more than size with 2.2.2.7 (a multiple of 2.2.2.7). |
| Area reservation | The user should reserve this area. The user can freely use this area after the rendering of one block. |
| Allocation | This area is included in RAM. |
| Alignment | Align this area on a 4-byte boundary. |

2.4.5 Output Buffer

Output buffer only is used in the TDM Capture case.

Table 2-17 Output Buffer Description

| Table 2-17 Ot | itput buller Description |
|------------------|--|
| Item | Area which stores outputs from this software. The output buffer contains 16-bit or 24-bit linear PCM data (hereinafter called PCM data). If the user manipulates this area during rendering processing, the normal execution of the program cannot be ensured. |
| Symbol name | - (freely defined by the user) |
| Size | Please secure more than size with 2.2.2.7 (a multiple of 2.2.2.7). |
| Area reservation | The user should reserve this area. The user can freely use this area after the rendering of one block. |
| Allocation | This area is included in RAM. |
| Alignment | Align this area on a 4-byte boundary. |

Rev. 1.00 Page 85 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

(1) Input/ Output data storage method

Data is input/ output in the formats as shown in Figure 2-4(consecutive buffers are specified for the channels). The input/output buffer (memory) stores data in 2-byte (16-bit) units. The byte order for accessing the buffer is little endian (see Figure 2-2).

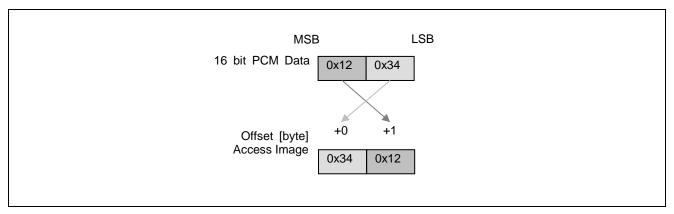


Figure 2-2 PCM 16-bit Data Access (Little Endian Mode)

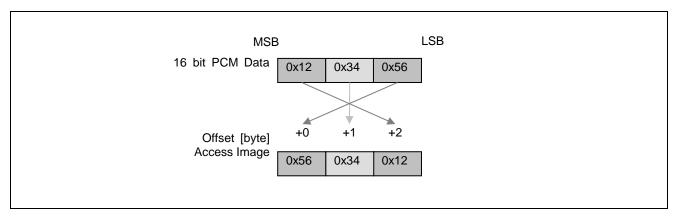


Figure 2-3 PCM 24-bit Data Access (Little Endian Mode)

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

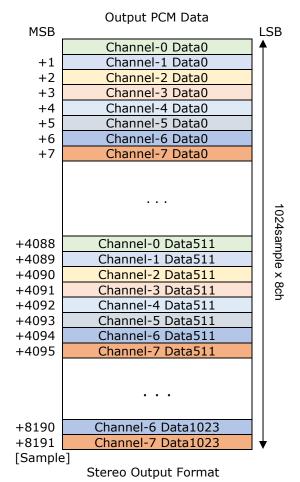


Figure 2-4 Output Formats

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

Error Processing 2.5

This software's functions return the error codes listed in Table 2-19.

Rev. 1.00 Page 88 of 94

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

2.5.1 Error codes

Below are the error codes for this software.

| Table 2-18 Error Codes for TD | | |
|--------------------------------|------------|---|
| Error code (32bit) | Value | Description |
| [1] | 0.0000000 | The processing results are normal. |
| XA_NO_ERROR | 0x00000000 | The process has terminated normally. |
| [2] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_MEM_ALLOC | | continuation. An address of API structure was |
| | | specified at the argument is NULL, the program |
| | 0xFFFF8000 | execution is incorrect. |
| | | Because it becomes the common API error, please |
| | | check the correct procedure. |
| [3] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_MEM_ALIGN | | continuation. An address of API structure was |
| | 0xFFFF8001 | specified at the argument does not 4 byte align. |
| | | Because it becomes the common API error, please |
| | | check the correct procedure. |
| [4] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_INVALID_CMD | | continuation. The command was specified at the |
| | 0xFFFF8002 | argument does not support. Because it becomes the |
| | | common API error, please check the correct |
| | | procedure. |
| [5] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_INVALID_CMD_TYPE | | continuation. The subcommand was specified at the |
| | 0xFFFF8003 | argument does not support. Because it becomes the |
| | | common API error, please check the correct |
| | | procedure. |
| [6] | | Abnormality has occurred, which disables process |
| XA_TDM_RDR_EXEC_FATAL_STATE | 0 | continuation. The command does not follow |
| | 0xFFFF9080 | procedure. Because it becomes the common API |
| | | error, please check the correct procedure. |
| [7] | | Abnormality has occurred, which disables process |
| XA_TDM_RDR_EXEC_FATAL_INPUT | 0 | continuation. The input size is not align with sample |
| | 0xFFFF9081 | size. Because it becomes the common API error, |
| | | please check the correct size of input buffers. |
| [8] | | Abnormality has occurred, which disables process |
| XA_TDM_RDR_EXEC_FATAL_INTERNAL | | continuation. Some of setting becomes incorrect |
| | Oveceeoooo | after combination (out of memory, hardware |
| | 0xFFFF9082 | modules are not available). Because it becomes |
| | | the common API error, please check the correct |
| | | parameters and make sure the resource is validity. |

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| | _ | |
|--|------------|---|
| [9] | | Abnormality has occurred, which disables process |
| XA_TDM_RDR_CONFIG_FATAL_STATE | | continuation. The command does not follow |
| | 0xFFFF8880 | procedure. Because it becomes the common API |
| | | error, please check the correct procedure. |
| [10] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_PCM_ | | the range. |
| WIDTH | 0xFFFF8881 | The pcm width value was specified at the argument |
| | | does not support. Please set an appropriate |
| | | value.(Refer to 2.2.2.16) |
| [11] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_CHAN | | the range. |
| NEL MODE | 0xFFFF8882 | The channel mode value was specified at the |
| | | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [12] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_SAMPL | | the range. |
| E_RATE | 0xFFFF8883 | The sample rate value was specified at the |
| L_IVATE | J | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [12] | | |
| [13] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_FRAM E_SIZE | 0xFFFF8884 | the range. |
| E_512E | 0211110004 | The frame size was specified at the argument does |
| | | not support. Please set an appropriate value.(Refer |
| 54.43 | | to 2.2.2.16) |
| [14] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_INVAL | 0 | the range. |
| ID_OUTPUT | 0xFFFF8885 | The output value was specified at the argument |
| | | does not support. Please set an appropriate |
| | | value.(Refer to 2.2.2.16 |
| [15] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_DMAC | | the range. |
| HANNEL | 0xFFFF8886 | The adma channel value was specified at the |
| | | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [16] | | It is an error for TDM Renderer specifications out of |
| XA_TDM_RDR_CONFIG_FATAL_VOLU | | the range. |
| ME_RATE | 0xFFFF8887 | The volume rate value was specified at the |
| | | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [17] | Others | Reserved |
| | | |

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

Frror Codes for TDM Canture

| Table 2-19 Error Codes for TDI Error code (32bit) | Value | Description |
|---|-------------|--|
| [1] | 00000000 | The processing results are normal. |
| XA_NO_ERROR | 0x00000000 | The process has terminated normally. |
| [2] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_MEM_ALLOC | | continuation. An address of API structure was |
| | 0 5555000 | specified at the argument is NULL, the program |
| | 0xFFFF8000 | execution is incorrect. |
| | | Because it becomes the common API error, please |
| | | check the correct procedure. |
| [3] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_MEM_ALIGN | | continuation. An address of API structure was |
| | 0xFFFF8001 | specified at the argument does not 4 byte align. |
| | | Because it becomes the common API error, please |
| | | check the correct procedure. |
| [4] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_INVALID_CMD | | continuation. The command was specified at the |
| | 0xFFFF8002 | argument does not support. Because it becomes the |
| | | common API error, please check the correct |
| | | procedure. |
| [5] | | Abnormality has occurred, which disables process |
| XA_API_FATAL_INVALID_CMD_TYPE | | continuation. The subcommand was specified at the |
| | 0xFFFF8003 | argument does not support. Because it becomes the |
| | | common API error, please check the correct |
| | | procedure. |
| [6] | | Abnormality has occurred, which disables process |
| XA_TDM_CAP_EXEC_FATAL_STATE | 0 | continuation. The command does not follow |
| | 0xFFFF90C0 | procedure. Because it becomes the common API |
| | | error, please check the correct procedure. |
| [8] | | Abnormality has occurred, which disables process |
| XA_TDM_CAP_EXEC_FATAL_INTERNAL | | continuation. Some of setting becomes incorrect |
| | 0.455550001 | after combination (out of memory, hardware |
| | 0xFFFF90C1 | module not available). Because it becomes the |
| | | common API error, please check the correct |
| | | parameters and make sure the resource is validity. |

Page 91 of 94 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual 2 Software Specifications

| [9] | | Abnormality has occurred, which disables process |
|-------------------------------|------------|--|
| XA_TDM_CAP_CONFIG_FATAL_STATE | | continuation. The command does not follow |
| AA_TDM_CAI_COM TO_FATAL_STATE | 0xFFFF88C0 | procedure. Because it becomes the common API |
| | | error, please check the correct procedure. |
| [10] | | |
| [10] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_PCM_ | | the range. |
| WIDTH | 0xFFFF88C1 | The pcm width value was specified at the argument |
| | | does not support. Please set an appropriate |
| | | value.(Refer to 2.2.2.16) |
| [11] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_CHAN | | the range. |
| NEL_MODE | 0xFFFF88C2 | The channel mode value was specified at the |
| | | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [12] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_SAMPL | | the range. |
| E_RATE | 0xFFFF88C3 | The sample rate value was specified at the |
| | | argument does not support. Please set an |
| | | appropriate value.(Refer to 2.2.2.16) |
| [13] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_FRAM | | the range. |
| E_SIZE | 0xFFFF88C4 | The frame size was specified at the argument does |
| | | not support. Please set an appropriate value.(Refer |
| | | to 2.2.2.16) |
| [12] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_INVAL | | the range. |
| ID_INPUT | 0xFFFF88C5 | The input value was specified at the argument does |
| | | not support. Please set an appropriate value.(Refer |
| | | to 2.2.2.16) |
| [13] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_DMAC | | the range. |
| HANNEL | 0xFFFF88C6 | The adma channel was specified at the argument |
| | | does not support. Please set an appropriate |
| | | value.(Refer to 2.2.2.16) |
| [14] | | It is an error for TDM Capture specifications out of |
| XA_TDM_CAP_CONFIG_FATAL_VOLU | | the range. |
| ME_RATE | 0xFFFF88C7 | The volume rate was specified at the argument |
| | | does not support. Please set an appropriate |
| | | value.(Refer to 2.2.2.16) |
| [15] | O+h | Reservered |
| [17] | Others | NGSGI VETEU |

Rev. 1.00 Jul. 04, 2017

ADSP TDM Renderer/Capture Plugin User's Manual

3 Processing Flow

3. Processing Flow

Figure 3-1 shows a flow diagram of processing performed by an application which uses this software. It applies for both case: TDM renderer and TDM capture.

The basic steps executed by the framework are white. The steps defined by the user framework are shaded. Design the process to suit the target system.

Rev. 1.00 Page 93 of 94

ADSP TDM Renderer/Capture Plugin User's Manual

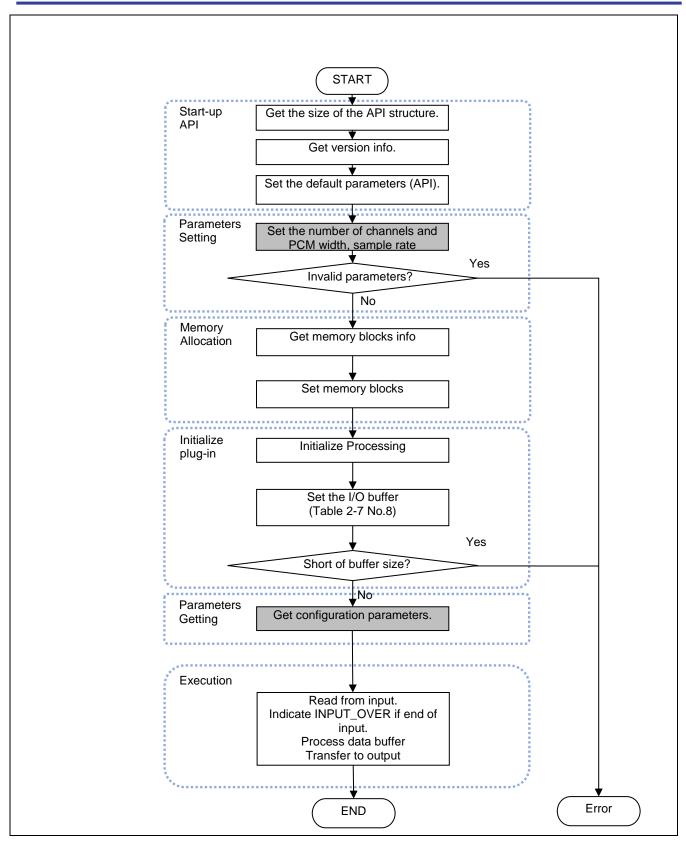


Figure 3-1 Example of the Application Processing Flow

| vision History ADSP TDM Renderer/Capture Plugin User's Manual | Renderer/Capture Plugin User's Manual | Revision History |
|---|---------------------------------------|------------------|
|---|---------------------------------------|------------------|

| Rev. | Date | Description | | | |
|------|---------------|-------------------|--|--|--|
| | | Page | Summary | | |
| 0.10 | Mar. 24, 2017 | - | Preliminary Edition | | |
| 0.11 | Jun. 06, 2017 | 44, 46, 53, 56 | Update the input range of SSI and SRC module. | | |
| | | 80, 81, 82 | Update API structure (table 2.13, table 2.14) and API size of TDM plugin (table 2.12). | | |
| | | - | Update page number | | |
| 0.12 | Jul. 04, 2017 | 38 | Update return error code for do-execute command | | |
| | | 83 | Update API structure for TDM Capture plugin | | |
| 1.00 | Jul. 04, 2017 | - | Official Release | | |
| | | 4, 12 | Determine the T.B.D item. | | |

ADSP TDM Renderer/Capture Plugin User's Manual

Publication Date: Jul 04, 2017 Rev. 1.00

Published by: Renesas Electronics Corporation



営業お問合せ窓口

http://www.renesas.com

営業お問合せ窓口の住所は変更になることがあります。最新情報につきましては、弊社ホームページをご覧ください。

ルネサス エレクトロニクス株式会社 〒135-0061 東京都江東区豊洲3-2-24(豊洲フォレシア)

| 技術的なお問合せおよび資料のご請求は下記へどうぞ。 総合お問合せ窓口:https://www.renesas.com/contact/ | | | | | | | |
|--|--|--|--|--|--|--|--|
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |



SALES OFFICES

Renesas Electronics Corporation

http://www.renesas.com

Refer to "http://www.renesas.com/" for the latest and detailed information.

Renesas Electronics America Inc. 2801 Scott Boulevard Santa Clara, CA 95050-2549, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333 Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd.
No.777C, 100 Feet Road, HAL II Stage, Indiranagar, Bangalore, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

© 2017 Renesas Electronics Corporation. All rights reserved.

ADSP TDM Renderer/Capture Plugin RCG3AHPLN0101ZNO

