		VERIFICATION PLAN: UART						guyen Van Huy
Section	Main Title	Sub Title	Description	Pass Condition	Testname	Method	Status	Remak
1	uart_lhs sends, uart_rhs receives							
1,1	tx/rx test		UART VIP Configuration: Baud Rate = 9600. Data Bits = 6 bits. Parity = None. Stop Bits = 1 bit. Stops: 1. Configure both uart_lhs and uart_rhs with the same settings. 2. uart_lhs sends random data through TX_LHS port. 3. Data freme includes State Bit (1), 8 Data Bits, and 1 Stop Bit.	Case uart, this receives the random data correctly through RX, RHS port. PASS: Received data matches the transmitted data with no bit errors.	uart_lhs_bxrx_test	Direct	PASS	
			uart_rhs receives through RX_RHS port.					
1,2	Stop Bit Test	1 Stop bit	- UJATT VIP Configuration: + Baud Rate = 9600. + Data Bits = 8 bits. + Parity = None. + Stop Bits = 1, 2. - Stopps:	 uart_ths correctly receives data from uart_his through RX_RHS port for both scenarios (1 Stop Bit and 2 Stop Bits). 	uart_lhs_1stopbit_test	Direct	PASS	
		2 Stop bit	1. Configure both uart, he and uart, his with the same settings, starting with 1 Stop Bit. 2. uart, his sends random data through TX_LHS port with frame format: * Start Bit (1), Data Bits (8), and Stop Bit (1). 3. uart, his receives the data through RX_RHS port. 4. Repeat the process with frame format: * Start Bit (1), Data Bits (8), and Stop Bits (2).		uart_lhs_2stopbit_test	Direct	PASS	
1,3	Data Bits Test	Data Bits = 9	UART VIP Configuration: + Baud Rate = 9600.	 uart_rhs correctly receives the data transmitted from uart_lhs for all Data Bits settings PASS: Data transmission is successful for all Data Bits configurations without errors. 	uart_lhs_9databit_test	Direct	PASS	
		Data Bits = 8	+ Parity = None. + Stop Bits = 1 bit.	rass. Data transmission is successful for all Data bits configurations without entities.	uart_lhs_8databit_test	Direct	PASS	
		Data Bits = 7	+ Data Bits = 9, 8, 7, 6, 5 Steps: 1. Configure both uart_fhs and uart_rhs with the same settings, starting with Data Bits = 9.		uart_lhs_7databit_test	Direct	PASS	
		Data Bits = 6	uart_lhs sends random data through TX_LHS port with frame format: + Start Bit (1), Data Bits (9), and Stop Bit (1).		uart_lhs_6databit_test	Direct	PASS	
		Data Bits = 5	uart_rhs receives the data through RX_RHS port. Repeat the process for Data Bits = 8, 7, 6, and 5.		uart_lhs_5databit_test	Direct	PASS	
1,4	4 Baud Rate Test	Baud Rate = 4800	- UART VIP Configuration: + Baud Rate = 4800, 9600, 19200, 57600, 115200.	Case 1: uart_rhs receives random data correctly through RX_RHS port for all baud rates. PASS: No data loss or errors for all baud rates.	uart_lhs_4800baudrate_test	Direct	PASS	
		Baud Rate = 9600	+ Data Bits = 8 bits.	PASS: No data loss or errors for all baud rates.	uart lhs 9600baudrate test	Direct	PASS	<u> </u>
		Baud Rate = 19200	+ Parity = None. + Stop Bits = 1 bit.		uart_lhs_19200baudrate_test	Direct	PASS	
		Baud Rate = 57600	Steps (Case 1): Configure both uart_lhs and uart_rhs with the same settings.		uart_lhs_57600baudrate_test	Direct	PASS	
		Baud Rate = 115200	2. uart. Ihs sends random data through TX. LHS port.		uart_lhs_115200baudrate_test	Direct	PASS	
		Baud Rate = custom(1200)	Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit. uart_rhs receives the data through RX_RHS port.		uart_lhs_custombaudrate_test	Direct	PASS	1
1,5	,5 Parity Check Test	Even Parity	UART VIP Configuration: + Boad Rate = 9000. + Data Bits = 8 bits. + Parity = Even and Odd (alternately). + Stop Bits = 1 bit. - Stops (Case 1 - Even Parity): 1. Configure both uart, lhs and uart, lhs with the same settings and Even Parity. 2. uart, lhs ended Sdat Hovelph TX_LHS port for two scenarios:	Case 1 (Even Parity): Scenario 1: When the data is 01010101, the number of 1 bits is even (4 bits), so the Parity Bit is 0. Scenario 2: When the data is 00001110, the number of 1 bits is odd (3 bits), so the Parity Bit is 1 to make the total even (4 bits). PASS: The received data and parity bit match the Even Parity configuration.	uart_lhs_evenparity_test	Direct	PASS	
		Odd Parity	Scenario 1: Data = 01010101, which requires Parity Bit = 0. Scenario 2: Data = 00001110, which requires Parity Bit = 1. Data frame includes Start Bit (1), 8 Data Bits, Parity Bit (0 or 1), and 1 Stop Bit. unt_his receives through RX_R155 port and checks for parity mismatches. Stops (Case 2 - 00d Parity): Configure both uart_his and uart_with with the same settings and Odd Parity. unt_his sends data through TX_R15 port for two scenarios: Scenario 1: Data = 0110_0011, which requires Parity Bit = 0. Scenario 2: Data = 1110_0101, which requires Parity Bit = 1.	 Case 2 (Odd Parity): 4 Scenario 1: When the data is 00001111, the number of 1 bits is odd (4 bits after adding parity), so the Parity Bit is 0. 4 Scenario 2: When the data is 11111000, the number of 1 bits is even (5 bits after adding parity), so the Parity Bit is 1. PASS: The received data and parity bit match the Odd Parity configuration. 	uart_lhs_oddparity_test	Direct	PASS	
		No Parity	3. Data frame includes Start Bit (1), 8 Data Bits, Parity Bit (0 or 1), and 1 Stop Bit. 4. uart, his receives through RX_LHS port and checks for parity mismatches. 5 Steps (Case 3 - No Parity) 1. Configure both uart, his and uart, with the same settings. 2. uart, his sends random date through TX_LHS port. 3. Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit. 4. uart, his receives through RX_PHS port.	Case_uart_rhs receives the random data correctly through RX_RHS port. PASS: Received data matches the transmitted data with no bit errors, no parity bit.	uart_lhs_noparity_test	Direct	PASS	
1,6	,6 Combination Test	Baurd rate = 4800 Data bit = 5 Parity = ODD Stop bit = 1	UART VIP Configuration: Baud Rate = random. Data Bits = random. Panty = random.	Case uart_rhs receives the random data correctly through RX_RHS port. PASS: Received data matches the transmitted data with no bit errors.	uart_lhs_combination1_test	Direct	PASS	
		Baurd rate = 9600 Data bit = 6 Parity = EVEN Stop bit = 2	Stop Bits = random. Steps: 1. Configure both uart_lhs and uart_rhs with the same settings. 2. uart_lhs sends random data through TX_LHS port. 3. Data frem includes 1 Stat Bit, (8,7,6,5) Data Bits, and (1,2) Stop Bit.	Case uart_rhs receives the random data correctly through RX_RHS port. PASS: Received data matches the transmitted data with no bit errors.	uart_lhs_combination2_test	Direct	PASS	
		Baurd rate = 57600 Data bit = 8 Parity = NO Stop bit = 2	uart_rhs receives through RX_RHS port.	Case uart, the receives the random data correctly through RX, RHS port. PASS: Received data matches the transmitted data with no bit errors.	uart_lhs_combination3_test	Direct	PASS	
		Baurd rate = 115200 Data bit = 9 Parity = NO Stop bit = 2		Case uart_rhs receives the random data correctly through RX_RHS port. PASS: Received data matches the transmitted data with no bit errors.	uart_lhs_combination4_test	Direct	PASS	

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Section	Main Title	Sub Title	Description Pass Condition		Testname	Method	Status	Remak
	uart_rhs sends, uart_lhs receives							
2,1	tw/nx test		- UART VIP Configuration: + Baud Rate = 9600. + Data Bits = 8 bits. + Parity = None. + Stop Bits = 1 bit. - Stops: 1. Configure both uart_rhs and uart_lhs with the same settings. 2. uart_rhs sends random data through TX_rhs port. 3. Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit. 4. uart_lhs receives through RX_lhs port.	Case uart_lhs receives the random data correctly through RX_lhs port. PASS: Received data matches the transmitted data with no bit errors.	uart_rhs_bvx_test	Direct	PASS	
2,2	Stop Bit Test	1 Stop bit	- UART VIP Configuration: + Baud Rate = 9600. + Data Bits = 8 bits. + Parity = None. + Stop Bits = 1, 2 Steps:	 uart_lhs correctly receives data from uart_rhs through RX_lhs port for both scenarios (1 Stop Bit and 2 Stop Bits) 	uart_rhs_1stopbit_test	Direct	PASS	
		2 Stop bit	1. Configure both uart, rhs and uart, lhs with the same settings, starting with 1 Stop Bit. 2. uart, rhs sender sandom data through TX, rhs port with frame format: + Start Bit (1), Data Bits (8), and Stop Bit (1). 3. uart, lhs receives the data through RX, lhs port. 4. Repeat the process with frame format: + Start Bit (1), Data Bits (8), and Stop Bits (2).		uart_rhs_2stopbit_test	Direct	PASS	
2,3	Data Bits Test	Data Bits = 9	UART VIP Configuration: + Baud Rate = 9600.	 - uart_lhs correctly receives the data transmitted from uart_rhs for all Data Bits settings PASS: Data transmission is successful for all Data Bits configurations without errors. 	uart_rhs_9databit_test	Direct	PASS	
		Data Bits = 8	+ Parity = None. + Stop Bits = 1 bit. + Data Bits = 9, 8, 7, 6, 5.		uart_rhs_8databit_test	Direct	PASS	
		Data Bits = 7	- Steps: 1. Configure both uart_rhs and uart_lhs with the same settings, starting with Data Bits = 9.		uart_rhs_7databit_test	Direct	PASS	
		Data Bits = 6	uart_rhs sends random data through TX_rhs port with frame format: + Start Bit (1), Data Bits (9), and Stop Bit (1) uart_lhs receives the data through RX_lhs port uart_lhs receives the data through RX_lhs port.		uart_rhs_6databit_test	Direct	PASS	
		Data Bits = 5	4. Repeat the process for Data Bits = 8, 7, 6, and 5.		uart_rhs_5databit_test	Direct	PASS	
2,4	Baud Rate Test	Baud Rate = 4800	- UART VIP Configuration: + Baud Rate = 4800, 9600, 19200, 57600, 115200.	Case 1: uart_lhs receives random data correctly through RX_lhs port for all baud rates. PASS: No data loss or errors for all baud rates.	uart_rhs_4800baudrate_test	Direct	PASS	
		Baud Rate = 9600	+ Data Bits = 8 bits.	FASS. NO data loss of errors for all pado rates.	uart_rhs_9600baudrate_test	Direct	PASS	
		Baud Rate = 19200	+ Parity = None. + Stop Bits = 1 bit.		uart_rhs_19200baudrate_test	Direct	PASS	
		Baud Rate = 57600	- Steps (Case 1):		uart_rhs_57600baudrate_test	Direct	PASS	
		Baud Rate = 115200	Configure both uart_rhs and uart_lhs with the same settings. uart_rhs sends random data through TX_rhs port.					
		Baud Rate = custom(1200)	Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit.		uart_rhs_115200baudrate_test	Direct	PASS	
2,5	,5 Parity Check Test	Even Parity	4. uart, lhs receives the data through RX, lhs port. - UART VIP Configuration: - Baud Rate = 9600. - Data Bits = 5 lh. - Party = Even and Odd (alternately). - Stop Dits = 1 lb. - Stop Dics = 1	- Case 1 (Even Parity): + Scenario 1: When the data is 01010101, the number of 1 bits is even (4 bits), so the Parity Bit is 0. + Scenario 2: When the data is 00001110, the number of 1 bits is odd (3 bits), so the Parity Bit is 1 to make the total even (4 bits). PASS: The received data and parity bit match the Even Parity configuration.	uart_lhs_custombaudrate_test uart_rhs_evenparity_test	Direct	PASS	
		Odd Parity	Scenario 1: Data = 01010101, which requires Parily Bit = 0. Scenario 2: Data = 00001110, which requires Parily Bit = 1. 3. Data frame includes Start Bit (1), 8. Data Bits, Parily Bit (0 or 1), and 1 Stop Bit. 4. uart, Ihre receives through RX_Ihrs port and checks for parily mismatches Stops (Case 2 - 04d Parily): 1. Configure both uart, this and uart, Ihr with the same settings and Odd Parily. 2. uart, Ihr sends data through TX_Ihr port for two scenarios: + Scenario 1: Data = 00001111, which requires Parily Bit = 0. + Scenario 2: Data = 11110000, which requires Parily Bit = 1.	- Case 2 (Odd Parity): + Scenario 1: When the data is 00001111, the number of 1 bits is odd (4 bits after adding parity), so the Parity Bit is 0. + Scenario 2: When the data is 11110000, the number of 1 bits is even (5 bits after adding parity), so the Parity Bit is 1. PASS: The received data and parity bit match the Odd Parity configuration.	uart_rhs_oddparity_test	Direct	PASS	
		No Parity	3. Data frame includes Start Bit (1), 8 Data Bits, Parily Bit (0 or 1), and 1 Stop Bit. 4. uart, rhs receives through RX, rhs port and checks for parity mismatches. - Steps (Case 3 - NO Parity): 1. Configure both uart, rhs and uart, lhs with the same settings. 2. uart, rhs sends random data through TX, rhs port. 3. Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit. 4. uart, lhs receives through RX, lhs port.	Case 3 uart_lbs receives the random data correctly through RX_lbs port. PASS: Received data matches the transmitted data with no bit errors, no parity bit.	uart_rhs_noparity_test	Direct	PASS	
2,6	2,6 Combination Test	Baurd rate = 4800 Data bit = 5 Parity = ODD Stop bit = 1	- UART VIP Configuration: + Baud Rate = random. + Data Bits = random. + Parity = random. + Stop Bits = random.	Case uart_lhs receives the random data correctly through RX_ths port. PASS: Received data matches the transmitted data with no bit errors.	uart_rhs_combination1_test	Direct	PASS	
		Baurd rate = 9600 Data bit = 6 Parity = EVEN Stop bit = 2	Steps: Configure both uart_rhs and uart_lhs with the same settings. uart_rhs sends random data through TX_rhs port. Data frame includes 1 Start Bit, (8,7,6,5) Data Bits, and (1,2) Stop Bit.	Case uart_lhs receives the random data correctly through RX_ths port. PASS: Received data matches the transmitted data with no bit errors.	uart_rhs_combination2_test	Direct	PASS	
		Baurd rate = 57600 Data bit = 8 Parity = NO Stop bit = 2	uart_lhs receives through RX_lhs port.	Case uart_lhs receives the random data correctly through RX_ths port. PASS: Received data matches the transmitted data with no bit errors.	uart_rhs_combination3_test	Direct	PASS	
		Baurd rate = 1152600 Data bit = 9 Parity = NO Stop bit = 2		- Case uart_lhs receives the random data correctly through RX_lhs port. PASS: Received data matches the transmitted data with no bit errors.	uart_rhs_combination4_test	Direct	PASS	

	I		VERIFICATION PLAN:	UART	T			uyen Van H
ction	Main Title	Sub Title	Description	Pass Condition	Testname	Method	Status	Remak
	Combination uart_lhs, uart_rhs sends and	receives						
3,1	tx/rx test		- UART VIP Configuration:	- Case uart_lhs and uart_rhs and uart_lhs receives the random data correctly through RX_lhs and RX_rhs port.				
			+ Baud Rate = 9600. + Data Bits = 8 bits.	PASS: Received data matches the transmitted data with no bit errors.				
			+ Parity = None.					
			+ Stop Bits = 1 bit. - Steps:		uart_rhs and uart_lhs_txrx_test	Direct	PASS	
			Configure both uartlhs_and_uartrhs with the same settings.					
			uart_rhs and uart_lhs sends random data through TX_rhs and TX_lhs port.					
			Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit.					
3.3	Stop Bit Test	1 Stop bit	uart_lhs and uart_rhs and uart_lhs receives through RX_lhs and RX_rhs port. UART VIP Configuration:	- uart_lhs and uart_rhs and uart_lhs correctly receives data from uart_rhs and uart_lhs through RX_lhs and RX_rhs por	1			
-,-	Clop Sit 1 co.	1 Stop bit	+ Baud Rate = 9600.	for both scenarios (1 Stop Bit and 2 Stop Bits)				
			+ Data Bits = 8 bits.		uart_rhs and uart_lhs_1stopbit_test	Direct	PASS	
			+ Parity = None. + Stop Bits = 1, 2.					
			- Steps:					
		2 Stop bit	Configure both uartlhs_and_uartrhs with the same settings, starting with 1 Stop Bit. uart_rhs and uart_lhs sends random data through TX_rhs and TX_lhs port with frame format:					
		·	+ Start Bit (1), Data Bits (8), and Stop Bit (1).					
			uart_lhs and uart_rhs and uart_lhs receives the data through RX_lhs and RX_rhs port.		uart_rhs and uart_lhs_2stopbit_test	Direct	PASS	
			Repeat the process with frame format:					
3.5	B Data Bits Test	Data Bits = 9	+ Start Bit (1), Data Bits (8), and Stop Bits (2) UART VIP Configuration:	- uart_lhs and uart_rhs and uart_lhs correctly receives the data transmitted from uart_rhs and uart_lhs for all Data Bits				
3,0	Data Dita Test	Data Dits = 9	+ Baud Rate = 9600.	settings	uart_rhs and uart_lhs_9databit_test	Direct		
		Data Bits = 8	+ Parity = None.	PASS: Data transmission is successful for all Data Bits configurations without errors.				
			+ Stop Bits = 1 bit. + Data Bits = 9, 8, 7, 6, 5.		uart_rhs and uart_lhs_8databit_test	Direct		
		Data Bits = 7	- Steps:		uart rhs and uart lhs 7databit test	Direct		
			 Configure both uartlhs_and_uartrhs with the same settings, starting with Data Bits = 9. 		uait_iris aiiu uait_iris_/databit_test	Pilect		
		Data Bits = 6	uart_rhs and uart_lhs sends random data through TX_rhs and TX_lhs port with frame format: + Start Bit (1), Data Bits (9), and Stop Bit (1).		uart_rhs and uart_lhs_6databit_test	Direct	PASS	
			uart_lhs and uart_rhs and uart_lhs receives the data through RX_lhs and RX_rhs port.					
		Data Bits = 5	 Repeat the process for Data Bits = 8, 7, 6, and 5. 		uart_rhs and uart_lhs_5databit_test	Direct	PASS	
3,4	Baud Rate Test	Baud Rate = 4800	- UART VIP Configuration:	- Case 1: uart_lhs and uart_rhs and uart_lhs receives random data correctly through RX_lhs and RX_rhs port for all	uart_rhs and uart_lhs_4800baudrate_test	Direct	PASS	
		Baud Rate = 9600	+ Baud Rate = 4800, 9600, 19200, 57600, 115200. + Data Bits = 8 bits.	baud rates. PASS: No data loss or errors for all baud rates.	uart_rhs and uart_lhs_9600baudrate_test	Direct	PASS	
		Baud Rate = 19200	+ Parity = None.	PASS. INO data loss or errors for all pado rates.			17100	
			+ Stop Bits = 1 bit.		uart_rhs and uart_lhs_19200baudrate_test	Direct	PASS	
		Baud Rate = 57600	Steps (Case 1): Configure both uartlhs and uartrhs with the same settings.		uart_rhs and uart_lhs_57600baudrate_test	Direct	PASS	
		Baud Rate = 115200	Configure both dartins_and_dartrns with the same settings. Luart_rhs and dart_lhs sends random data through TX_rhs and TX_lhs port.		uart_rhs and uart_lhs_115200baudrate_test	Direct	PASS	
		Baud Rate = custom(1200)	Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit.					
			uart_lhs and uart_rhs and uart_lhs receives the data through RX_lhs and RX_rhs port.		uart_lhs_custombaudrate_test	Direct	PASS	
3,5	Parity Check Test	Even Parity	- UART VIP Configuration: + Baud Rate = 9600.	- Case 1 (Even Parity): + Scenario 1: When the data is 01010101, the number of 1 bits is even (4 bits), so the Parity Bit is 0.				
			+ Baud Rate = 9600. + Data Bits = 8 bits.	+ Scenario 1: When the data is 01010101, the number of 1 bits is even (4 bits), so the Parity Bit is 0. + Scenario 2: When the data is 00001110, the number of 1 bits is odd (3 bits), so the Parity Bit is 1 to make the total				
			+ Parity = Even and Odd (alternately).	even (4 bits).				
			+ Stop Bits = 1 bit.	PASS: The received data and parity bit match the Even Parity configuration.	uart_rhs and uart_lhs_evenparity_test	Direct	PASS	
			 Steps (Case 1 - Even Parity): 1. Configure both uartlhs_and_uartrhs with the same settings and Even Parity. 	•				
			uart_rhs and uart_lhs sends data through TX_rhs and TX_lhs port for two scenarios:					
		Odd Desk	+ Scenario 1: Data = 01010101, which requires Parity Bit = 0.	One of Odd Deduction				
		Odd Parity	+ Scenario 2: Data = 00001110, which requires Parity Bit = 1. 3. Data frame includes Start Bit (1), 8 Data Bits, Parity Bit (0 or 1), and 1 Stop Bit.	 Case 2 (Odd Parity): + Scenario 1: When the data is 00001111, the number of 1 bits is odd (4 bits after adding parity), so the Parity Bit is 0. 				
			Learn line includes start bit (1), or bata bits, if all y bit (0 or i), and if stop bit. Learn line and uart_rhs and uart_lhs receives through RX_lhs and RX_rhs port and checks for parity.	+ Scenario 2: When the data is 11110000, the number of 1 bits is even (5 bits after adding parity), so the Parity Bit is 1.				
			mismatches.	PASS: The received data and parity bit match the Odd Parity configuration.	uart_rhs and uart_lhs_oddparity_test	Direct	PASS	
			Steps (Case 2 - Odd Parity): Configure both uartlhs_and_uartrhs with the same settings and Odd Parity.		dait_ins and dait_ins_ddupanty_test	Direct	1 700	
			configure both dartins_and_uarths with the same settings and God Parity. uart_lhs and uart_rhs and uart_lhs sends data through TX_lhs port for two scenarios:					
			+ Scenario 1: Data = 00001111, which requires Parity Bit = 0.					
		No Parity	+ Scenario 2: Data = 11110000, which requires Parity Bit = 1.	- Case 3 uart_lhs and uart_rhs and uart_lhs receives the random data correctly through RX_lhs and RX_rhs port.				
			 Data frame includes Start Bit (1), 8 Data Bits, Parity Bit (0 or 1), and 1 Stop Bit. uart_rhs and uart_lhs receives through RX_rhs port and checks for parity mismatches. 	PASS: Received data matches the transmitted data with no bit errors, no parity bit.				
			- Steps (Case 3 - NO Parity):					
3,6			 Configure both uartlhs_and_uartrhs with the same settings. 		uart_rhs and uart_lhs_noparity_test	Direct	PASS	
			uart_rhs and uart_lhs sends random data through TX_rhs and TX_lhs port. Data frame includes Start Bit (1), 8 Data Bits, and 1 Stop Bit.					
			Data frame includes start Bit (1), 8 Data Bits, and 1 Stop Bit. uart_lhs and uart_rhs and uart_lhs receives through RX_lhs and RX_rhs port.					
	Combination Test	Baurd rate = 4800	- UART VIP Configuration:	- Case_uart_lhs and uart_rhs and uart_lhs receives the random data correctly through RX_lhs and RX_rhs port.		1		
		Data bit = 5	+ Baud Rate = random.	PASS: Received data matches the transmitted data with no bit errors.	uart_rhs and uart_lhs_combination1_test	Direct	PASS	
		Parity = ODD Stop bit = 1	+ Data Bits = random. + Parity = random.		and the same and the combination in test	5/1001		
		Baurd rate = 9600	+ Parity = random. + Stop Bits = random.	- Case_uart_lhs and uart_rhs and uart_lhs receives the random data correctly through RX_lhs and RX_rhs port.		1		
		Data bit = 6	- Steps :	PASS: Received data matches the transmitted data with no bit errors.	uart the and uart line combination? **	Direct	PASS	
		Parity = EVEN	Configure both uartlhs_and_uartrhs with the same settings. until the conduct the conduct the conduct through TV, the conduct the conduct the conduct the conduct through TV.		uart_rhs and uart_lhs_combination2_test	Pilect	FASS	
		Stop bit = 2 Baurd rate = 57600	uart_rhs and uart_lhs sends random data through TX_rhs and TX_lhs port. Data frame includes 1 Start Bit , (8,7,6,5) Data Bits, and (1,2) Stop Bit.	- Case uart. Ihs and uart, rhs and uart. Ihs receives the random data correctly through RX. Ihs and RX. rhs port.		1		
		Data bit = 8	uart_lhs and uart_rhs and uart_lhs receives through RX_lhs and RX_rhs port.	 Case uart_ins and uart_ins and uart_ins receives the random data correctly through RX_ins and RX_ins port. PASS: Received data matches the transmitted data with no bit errors. 				
		Parity = NO			uart_rhs and uart_lhs_combination3_test	Direct	PASS	
		Stop bit = 2				ļ		
		Baurd rate = 1152600 Data bit = 9		 Case uart_lhs and uart_rhs and uart_lhs receives the random data correctly through RX_lhs and RX_rhs port. PASS: Received data matches the transmitted data with no bit errors. 				
		Parity = NO		1700. 170001700 data matories ure transmitted data with no DR 80015.	uart_rhs and uart_lhs_combination4_test	Direct	PASS	
	1	Stop bit = 2				<u></u>		
4	Config differences between uart_lhs and u	art_rhs tests	WARTING O. C I					
4,1	Config differences baud rate test		- UART VIP Configuration: + Baud Rate = random.	 Case uart_rhs receives the random data incorrectly through RX_RHS port. PASS: Received data not matches the transmitted data. 	uart_difference_baudrate_test	Direct	PASS	
4.2	Config differences stop bit test		+ Data Bits = random.	Case uart_rhs receives the random data incorrectly through RX_RHS port.	and difference and big a .	Disease	DAGG	
4,2			+ Parity = random.	PASS: Received data not matches the transmitted data.	uart_difference_stopbit_test	Direct	PASS	
4.3	Config differences data bit test	-	+ Stop Bits = random. - Steps :	Case uart_rhs receives the random data incorrectly through RX_RHS port. DASC_Dast_ind data and matches the transport and data.	uart difference databit test	Direct	PASS	
-4,0			 Configure both uart_lhs and uart_rhs with the differences settings (baud rate the same settings. 	PASS: Received data not matches the transmitted data.	dan_danoronoo_databa_tdst	Direct		
4,4			uart_lhs sends random data through TX_LHS port.	 Case uart_rhs receives the random data incorrectly through RX_RHS port. PASS: Received data not matches the transmitted data. 	uart_difference_paritybit_test	Direct	PASS	
,	Config differences baud rate, stop bit, data	bit, parity bit test	 Data frame includes 1 Start Bit , (8,7,6,5) Data Bits, and (1,2) Stop Bit. uart_rhs receives through RX_RHS port. 	 Case uart_rhs receives the random data incorrectly through RX_RHS port. 	the different to the control of the	D	PASS	
				PASS: Received data not matches the transmitted data.	uart_lhs_difference_combination_test	Direct	PASS	