

## **TFE4140 Modeling and Analysis of Digital Systems**

### **Assignment 3: 1-bit voter for student satellite communication system**

#### **1. Task 1: Read the term project description in detail**

- Download the term project description from it's learning.
- Read through the complete document to get acquainted with the specification.

#### **2. Task 2: Design and simulate a 1-bit voter**

- Use what you learned from Assignment 2 to design and simulate a 1-bit voter as specified in point 2 of the term project description. Make sure that your simulation verifies the correct behavior according to the specification. Note that the synthesis part of point 2 in the term project description is to be performed in Assignment 4.

#### **3. Procedure for Approval**

1. Do the assignment before the deadline.
  2. When you think you have a sensible result and understand what happens, go to Assignments -> Assignment 3 on the course pages on it's learning. What you are supposed to hand in this time is simply the VHDL file(s) you wrote in task 2 above and some screen shots from your simulation window showing correct behavior.
  3. A solution example will be made available on it's learning the same way as for Assignment 1. It contains a procedure for self-evaluation.
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