Addresses: To/from Rider Card

FPGA = PDU & Fault Monitors

REVISIONS

SEE SHEET 1

732-478-03

VR1

LT1121ACS8_33

SC_P5F

100K R299

CR23 702-104-00

C282 0.1UF

C145 1UF

TP27

TP28

3.3V

320 R195

SMC_MON_P3.3V_FPGA

1K R197

GND0

C144 0.1UF

GA1

VCCIO

VCC

2RM-904-01

QL3012_0PL84C

IO

TRSTB TMS TDI TCK STM I_3 I_2 I_1 I_0

GCLK/I GCLK/I ACLK/I ACLK/I

GND

TDO

Jag Pg18 - PDU Control

SMC_CNTRL_JAG_SWITCHED_AC*

MON IN Pg18 Flex 48V —

AD08 SMC_SC_FAN_IN_MONITOR

AD14 48V_ON_CNTRL

AD15 PSEN

AD10

Pg18 DC AC control

SMC_CNTRL_BC_SWITCHED_AC*

AD09 SMC_SPR_FAN_IN_MONITOR

MON IN MON IN

SMC_SPR2_FAN_IN_MON

Pg18 SMC_MON_BC_AC_ON*

Pg24 MON 24

SMC_MON_BC_FAN1_STATE

SMC_MON_BC_FAN0_STATE

SMC_MON_BC_FAN1_TACH_OUT

SMC_MON_PDU_JAG_THERMAL_FAULT*

9 SMC_MON_PDU_JAG_48V_GOOD

24 SMC_MON_BC_FAN0_TACH_OUT

10 SMC_MON_BC_PHASE_LOSS*

ADUC_WR*

AD01

AD03

JTAG_PULLDOWN_TRSTB

JTAG_PULLUP_TMS

JTAG_PULLUP_TDI

JTAG_PULLDOWN_TCK

JTAG_PULLDOWN_STM

29P 1.21K R154

1.21K R156

28P R153 1.21K

R155 1.21K

R157 1.21K

ALE

12 PWRCLR*

C300 111-102-00 100NF

U45 741-645-00

NC7WZ17

11MHZ_CLK

R360 636-151-00 51

HARDWARE:OPEN:#01+#03 357-290-00

SMC_MON_BC_OUTPUT_FAULT*

SMC_CNTRL_BC_48VDC_ON*

SMC_MON_BC_OVR_TMP*

Page 10 — Pg18 Pg9

SMC_PDU_VAB

SMC_PDU_MAINT

SMC_PDU_FREQ

Pg9-10

SMC_MON_DC90_OUTPUT_FAULT*

SMC_CNTRL_DC90_48V_ON*

K1_ON_CNTRL

Page 18 AC ON/OFF Flex

SMC_MON_DC90_OVR_TMP*

SMC_MON_DC90_AC_PWR_FAULT*

AD0

AD04

SMC_MON_BC_AC_PWR_FAULT* Pg10

SMC_PDU_K1_ON JAG_K1_ON=HIGH — Monitors Page 18 CKT Result

SMC_TH_FAN_IN_MONITOR

AD02

AD05

GND0

SMC_MON_DC90_48V_ON From Pg23

AD06

AD07

AD12

ADUC_RD*

AD11

SMC_PDU_V1

SMC_PDU_48V_ON JAG_48V_ON=HIGH — Monitors Result of this CKT

SMC_PDU_V0

SMC_PDU_I1

SMC_PDU_V2

AD13

SMC_PDU_VBC

Pg9 Pages 9-10

SMC_CNTRL_JAG_48VDC_ON* Command to Page 18 for PDU Control

SMC_PDU_I2

SMC_PDU_VAC

Page 9-10

FPGA

DRAWING

LA975

LAST_MODIFIED=Tue Sep 24 04:46:44 2013

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B	949-975-03		J
ENGINEER: ROBERT BROADBENT		SHEET 13 OF 26	