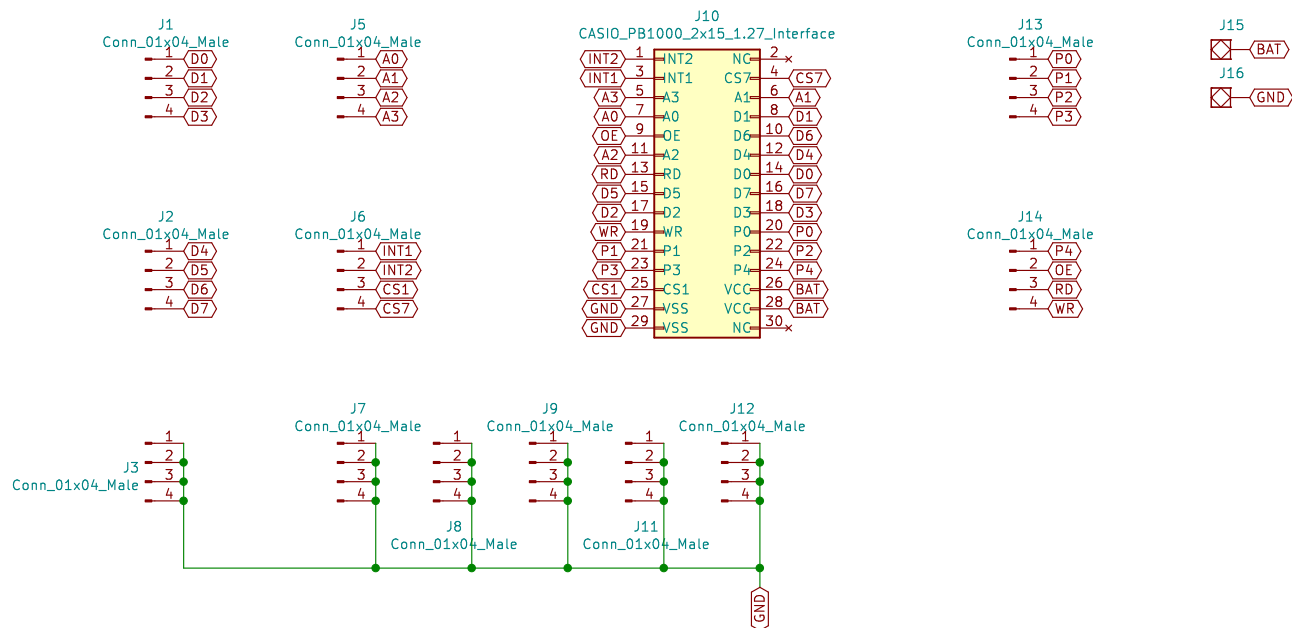


make sure to fabricate the PCB with 0.8mm thickness, otherwise the connector won't fit!

Actually VSS=-5V and VCC=0V (the actual ground...)



is Saleae Logic Analyzer friendly (the ground pins with the actual Bus pins)
by HWR0 <evilhardware@mail.ru>

Sheet: /
File: Casio PB-1000 Debug Unit.sch

Title: Casio PB-1000 System Bus Debug / Breakout Board

Size: A4	Date: 20.01.2020	Rev: v1.2
KiCad E.D.A. eeschema (5.1.5)-3		Id: 1/1