board 1 - CONNECTORS

CODEC JUMPER OR EXTENSION

	2 sm	4 STCK	6 STFS)) (0	ACC O	12 ZERO	14 ML	16 ML1	\$1	20 DACR
	B1	B2	B3	B4	BS	Be	B7	- B8	- BB	B10
120	STD	STCK	STFS	VCC	VCC	ZERO	ML	ML1	GND	AUD.RIGHT
PLD20	DA_DIN	DA_BCLK	DA_LRCIN	SRD	SRCK	SRFS	MC	MD	GND	AUD.LEFT
8	A1	A2	A3	44	A5	A6	A7	A8	49	A 10
	DIN	BCKIN	LRCIN 5	SRD 7	srck 9	SRFS 11	13	M ₀ 15	17	DACL 19

KEYB. DISP.

		2	4 (COLØ)	6 (001)	8 (00.2)	10 (col.3)	12 O VCC	14 SCK	16 ADV	18 RESET	20
		B1	B2	B3	B4	B5	Be	B7	B8	B3	B10
2	20	GND	COLØ	C0L1	COL2	COL3	NCC	SCK	SS	RESET	GND
	PBD20	GND	ROWØ	ROW1	KBD	<u>LEDS</u>	VCC	MOSI	MISO	RQB	GND
	X12	A1	A2	A3	A4	A5	A6	A7	A8	49	A 10
		-	ROWB	ROW1 5	KBD 7	6 Sugar	VCC 0 11	MOSI) 13	MISO 15	RQB 17	<u>6</u>

ALTERA JTAG

)												
)	PLD-10	TCK	TDO	TMS	NCC	IDI	GND				GND	
]]	X13	A1	A2	A3	A4	A5	B1	B2	B3	B4	BS	
• '		PLM_TCK		PLM_TMS 5	vcc o 7	6 √п_тл	2	4	9	∞	92	' >

MAIN JTAG

VDP CON.

PLS-9

X15

SS SS

 \downarrow

MOSI SCK

PLD-14	Ē	TDO	ΤÇ		RESET	3.3V		GND	GND	GND		TMS	
X14	A1	A2	A3	A4	A5	A6	A7	B1	B2	B3	B4	B5	
	1	TDO) 3	TCK 5	7	9 JRESET 9	VCC O 11	13	2	4	9	80	10 TMS	5

MISO RESET GND 200 ROA MISO RESET

CART CON.

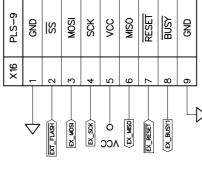
TRST/DE

N.B. JTAG разводятся как "IDC", но запаиваются "PLD" тип!

2

12 B6 4

+50 0



QNĐ	<u>SS</u>	ISOM	SCK	OOA	OSIM	RESET	BUSY	GND	
1	2	3	4	2	9	2	8	6	
\downarrow	EXT_FLASH	EX_MOSI)))	(EX_MISO	EX_RESET	(EX_BUSY1		\triangleright

20 <u>0</u>

57 6

2 4 1

2 9

WORK MODE DAC CONTROLED BY DSP

	SND.SYNT.301.DE		
15.08.01	15.08.01	Дата	
		Подпись	
		No документа	
		ист	

Изм.

Лист 6