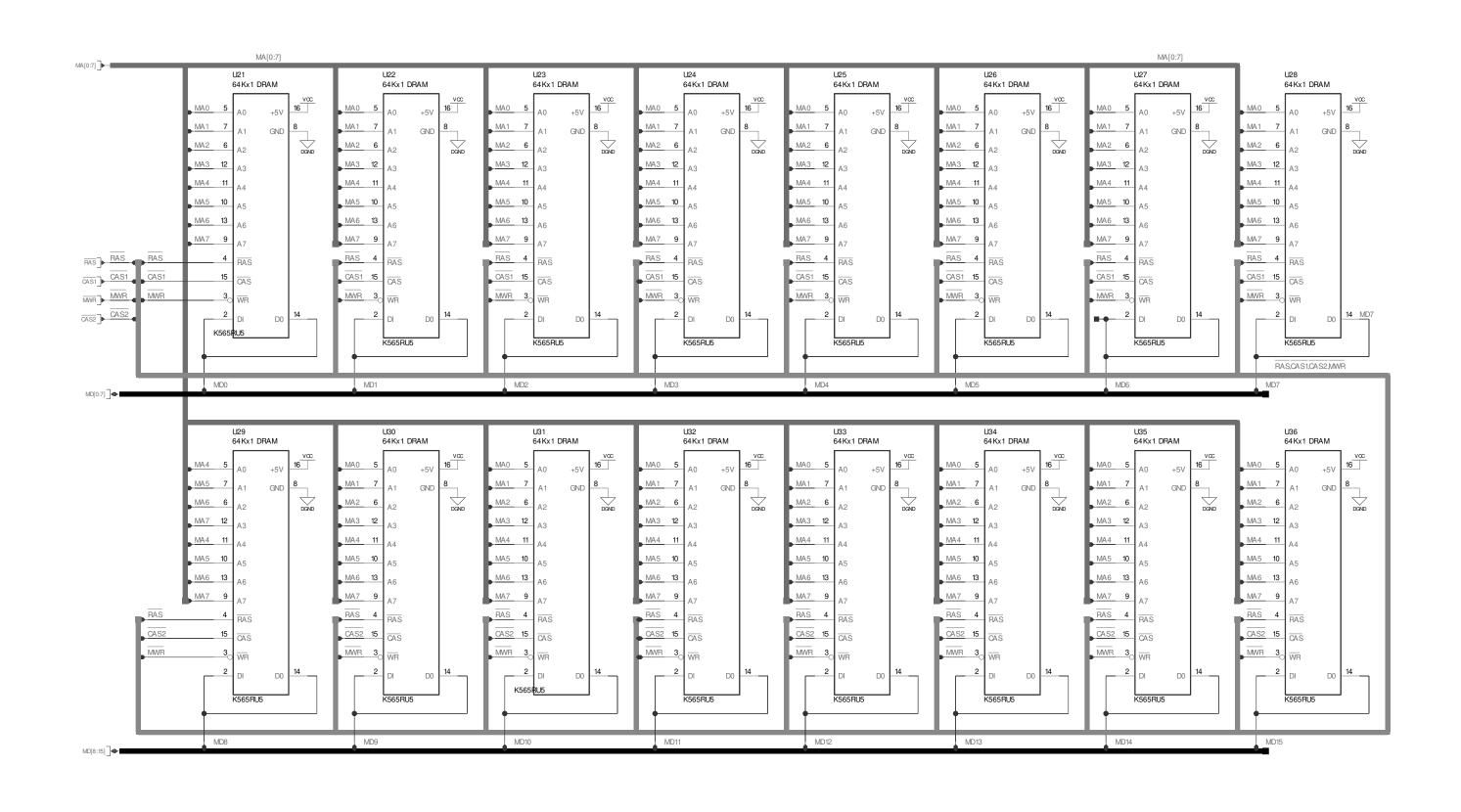


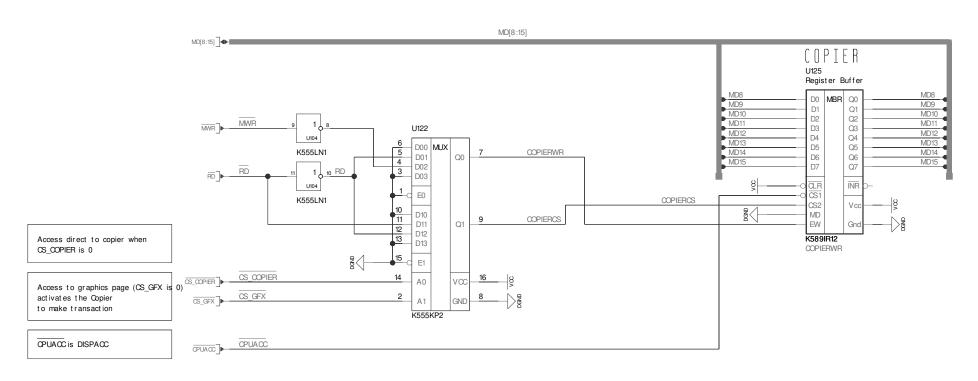


SIZE	DWG NO				REV
<b>A</b> 3	Alfa3			ROM 1	a3
DRAWN BY		SHEET	of		
Sherlock		2	16	21/ 04/ 2019:11:29	





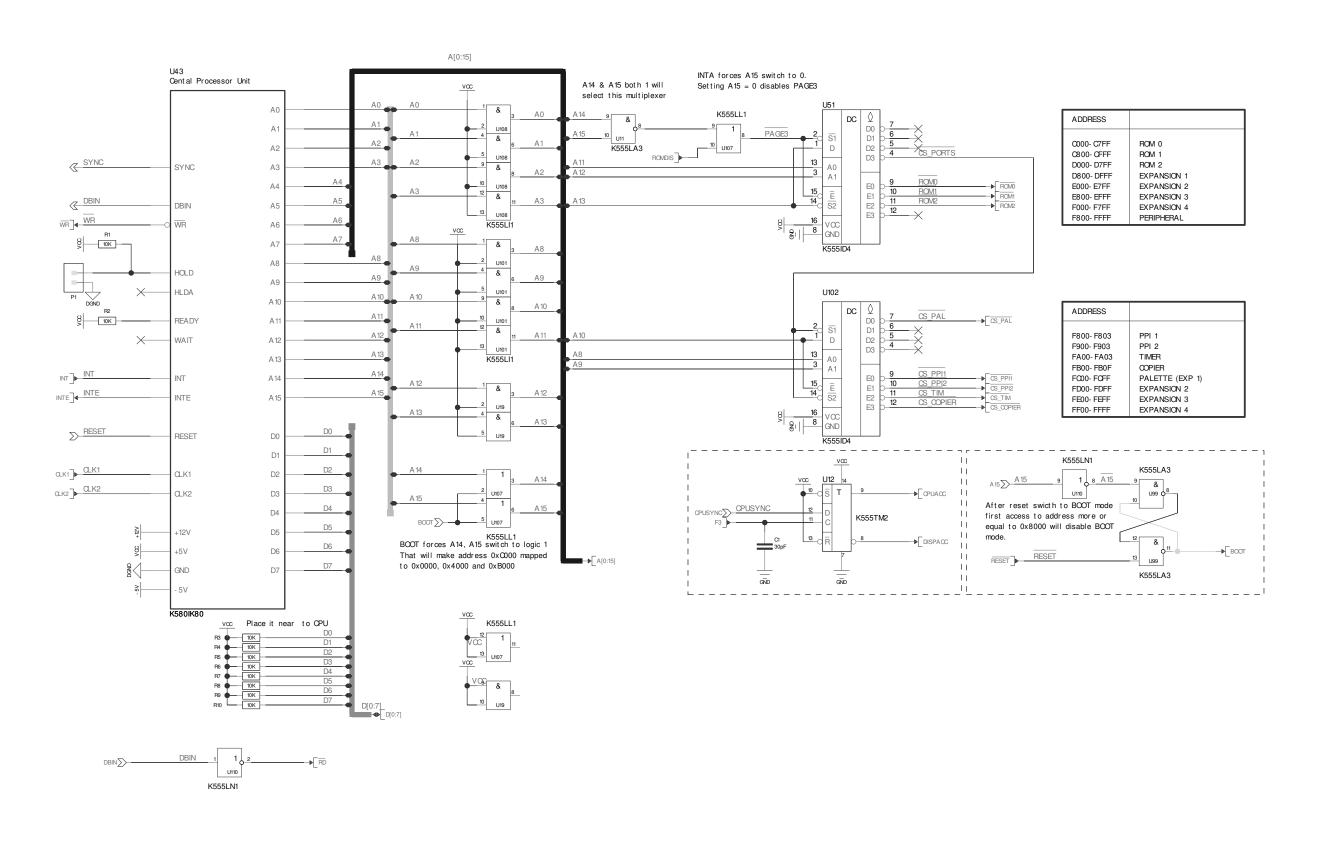
RADUGA Computer



B 8	UF_PAGE2 000-BFFF					
CPUACC	CS	COPIER	RD	MWR		COPIER's Operation
1	Х	Х	Х	Х		Nothing
Х	0	0	Х	Х		Nothing
Х	1	1	Х	Χ		Nothing
0	0	1	Acces	s to	PAGE2	
	Selecte	d In 1	0	Х	Read	Store
			1	X	Write	Read
0	1	0	Acces	s to	COPIER	
	Selecte	d Tn 2	0	1	Read	Read
	332000	<u>_</u>	1	0	Write	Store

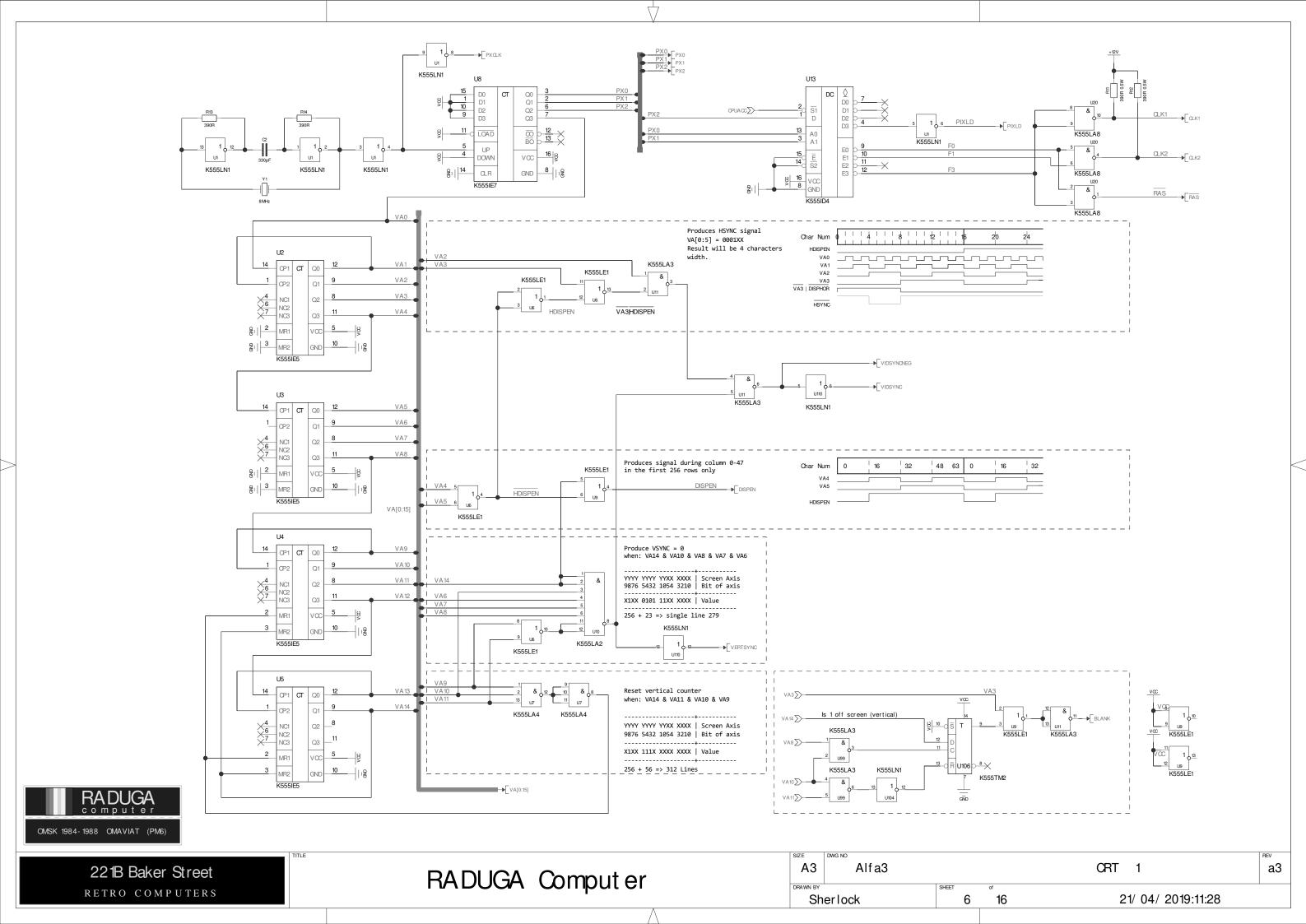


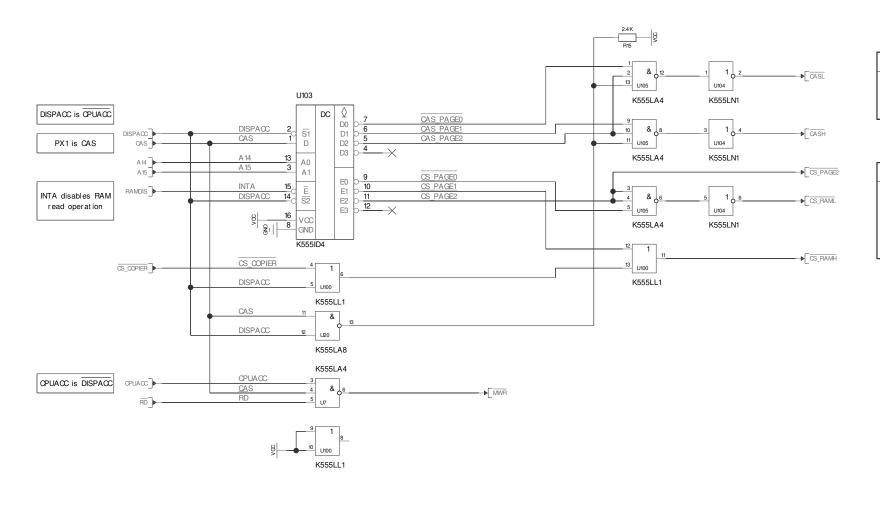
RADUGA Computer





A3	Alf a3			CPU 1	a3
DRAWN BY	erlock	SHEET 5	of <b>16</b>	21/ 04/ 2019:11:25	



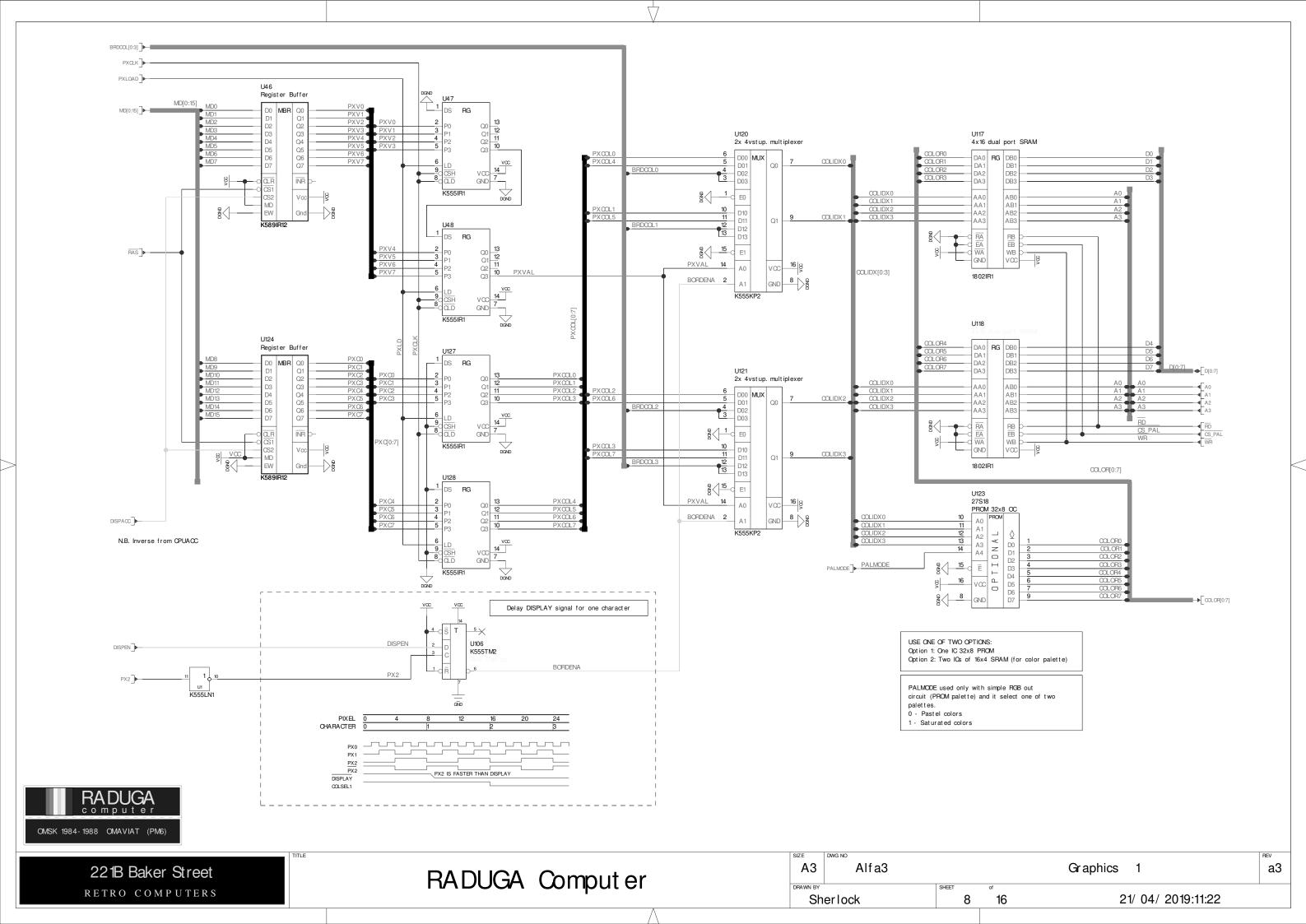


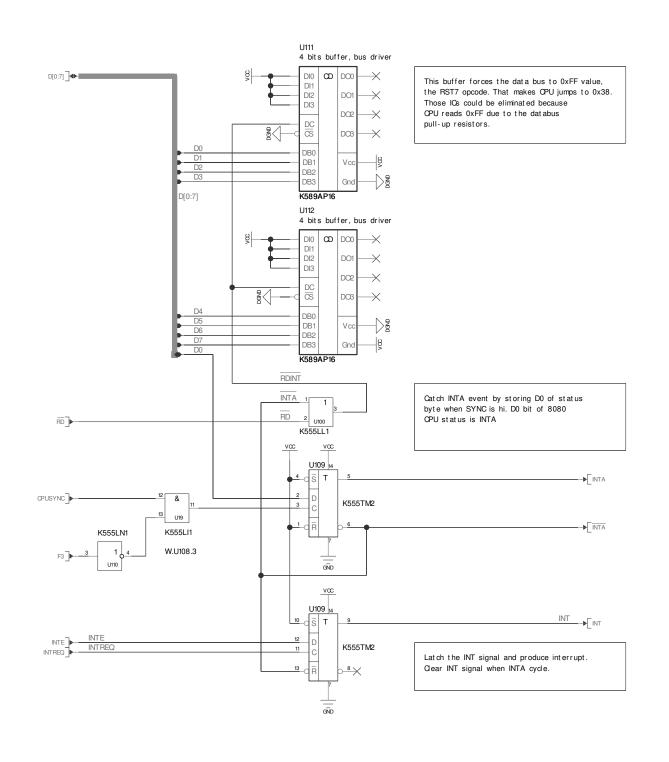
PAGE	ADDERSS	CAS1	CAS2
3	C000-FFFF	NO	NO
2	8000-BFFF	YES	YES
1	4000-7FFF	NO	YES
0	0000-3FFF	YES	NO

DACE	ADDERSS	MEMBUF1	MEMBUF2
FAGE	ADDENSS	IVIEIVIDUFI	IVIEIVIDUFZ
3	Ω00- FFFF	NO	NO
2	8000- BFFF	YES	YES
1	4000-7FFF	NO	YES
0	0000-3FFF	YES	NO
COPIER	FB00- FB0F	NO	YES



SIZE	DWG NO					REV	
А3	Alfa3				ControllerDRAM 1	a3	l
DRAWN BY		SHEET		of			
Sherlock			7	16	21/ 04/ 2019:11:22		1

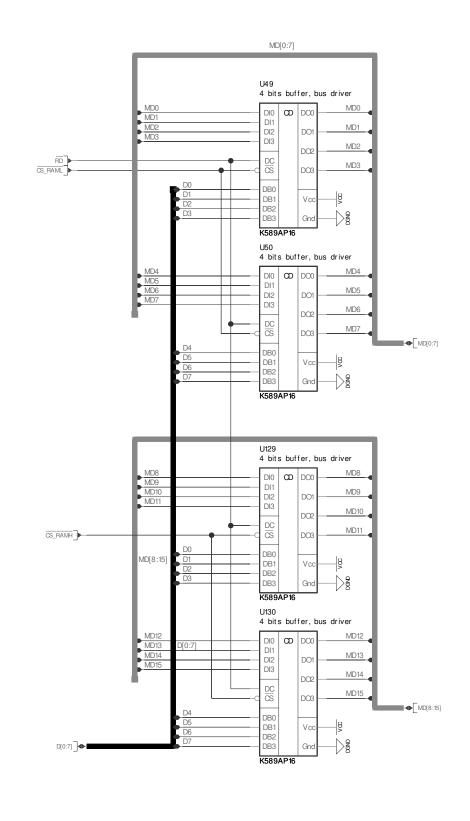






RADUGA Computer

A3 DWG NO Inerrupt s 1 a3 REV a3 Sherlock 9 16 21/ 04/ 2019:11:22

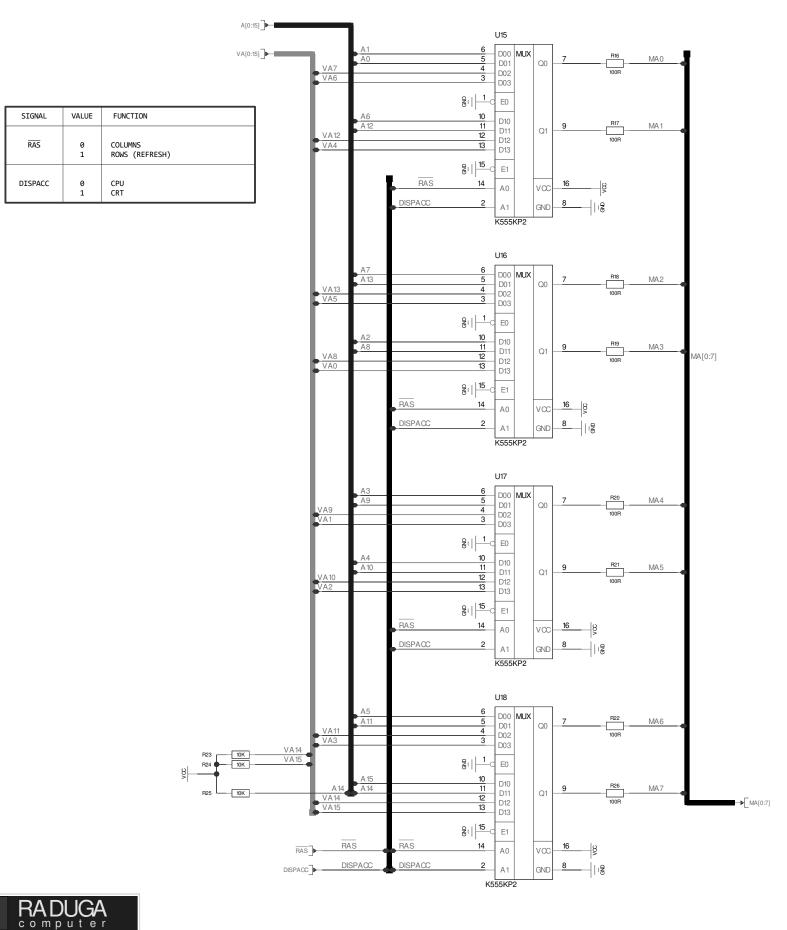


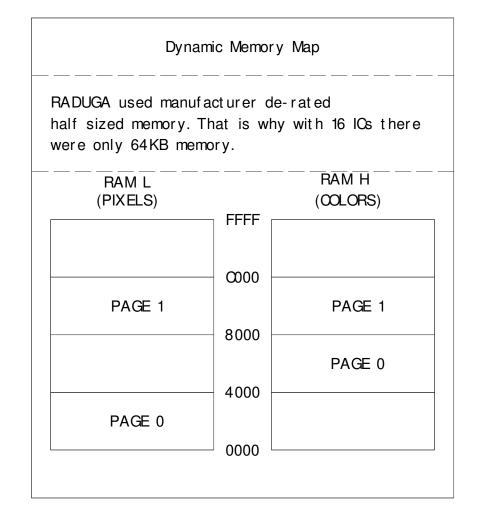


RADUGA Computer

A3 Alf a3 Memory Buffer 1 a3

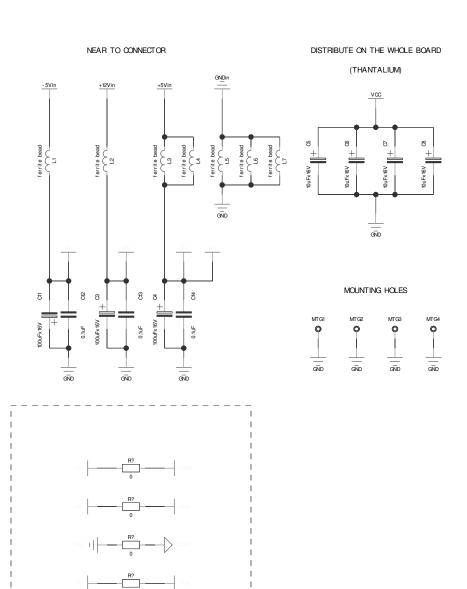
DRAWN BY Sherlock 10 16 21/ 04/ 2019:11:22

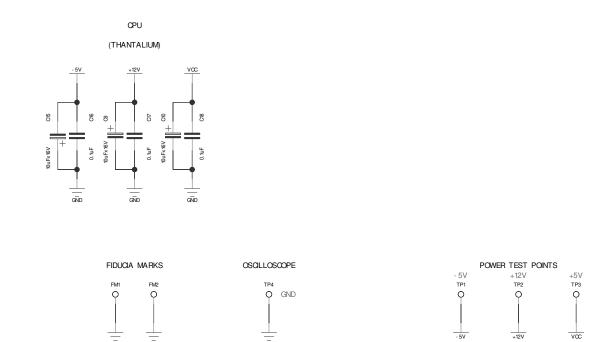






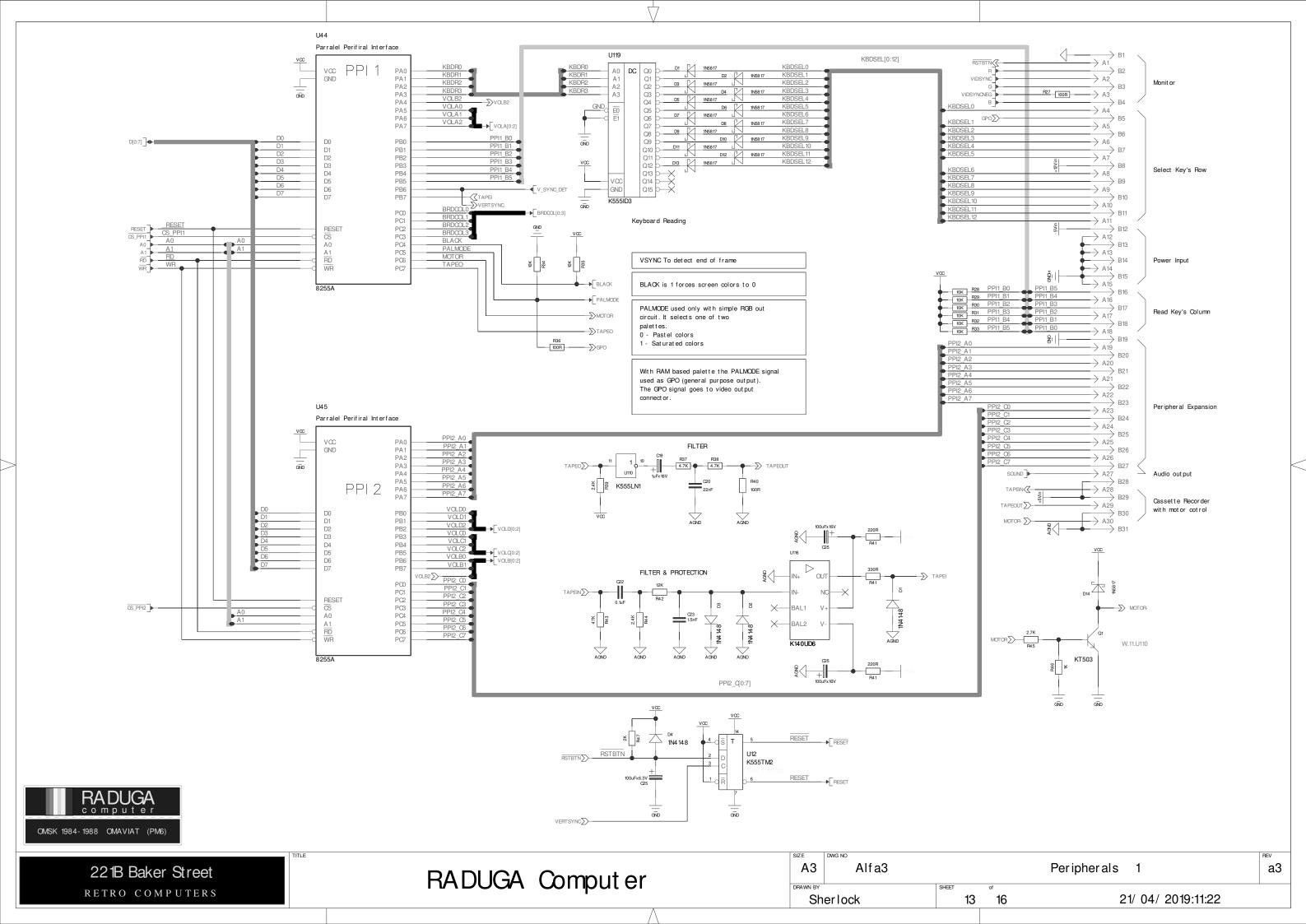
RADUGA Computer

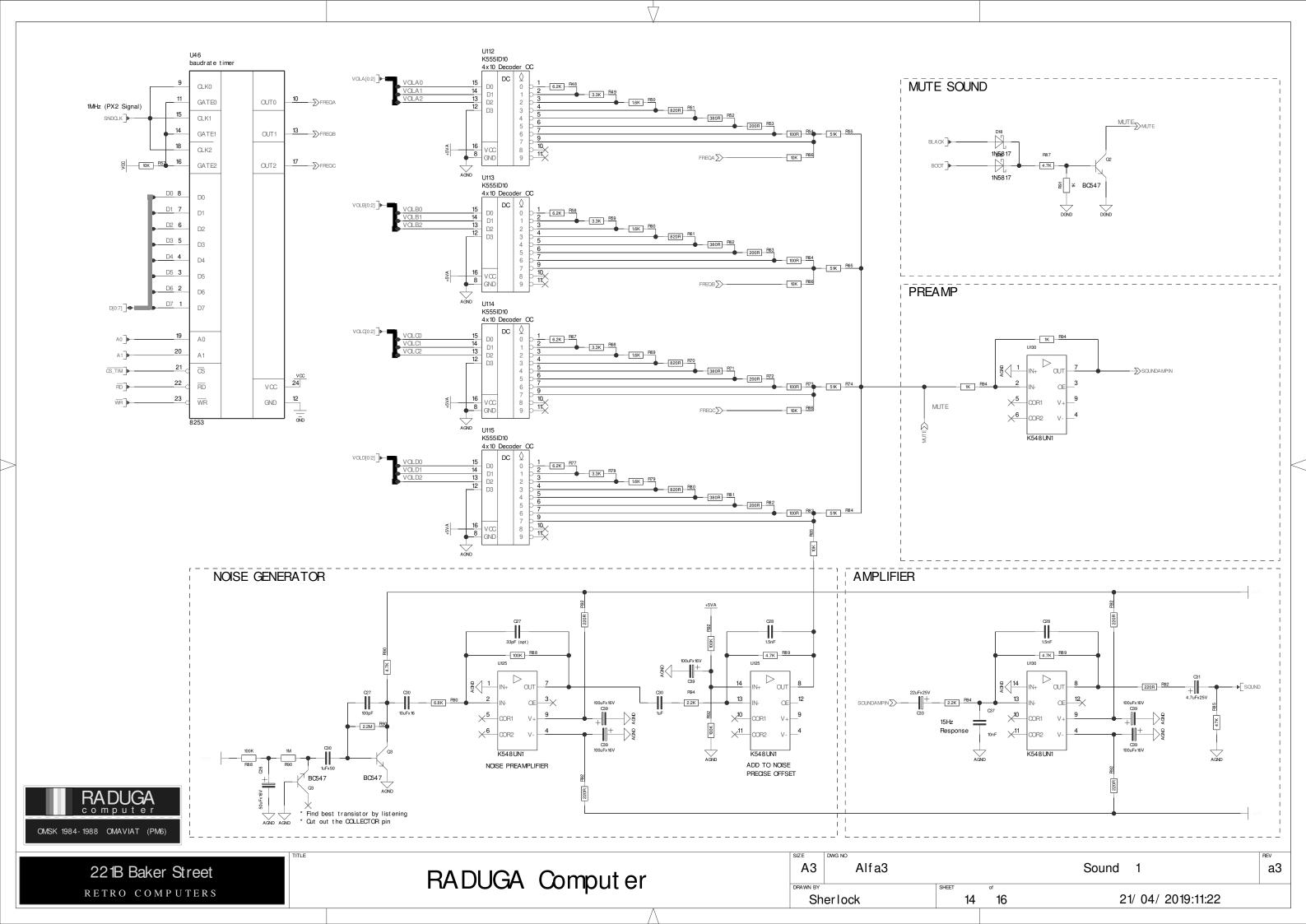






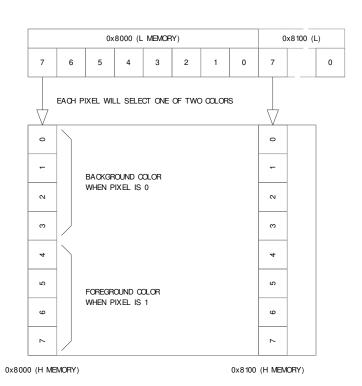
ZE	DWG NO				REV
А3	Alfa3			Power 1	a3
RAWN BY		SHEET	of		
Sherlock		12	16	21/ 04/ 2019:11:22	

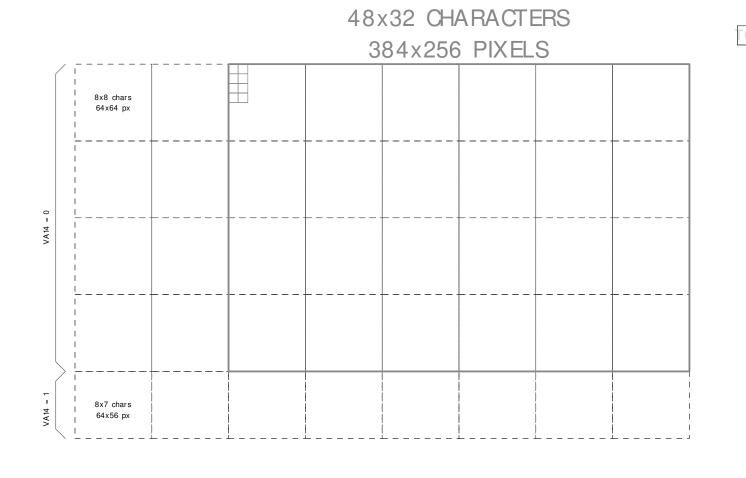




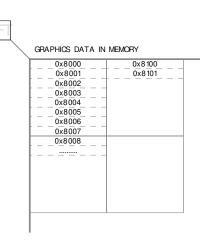
## 

## STANDART SPECIALIST HOR\_VISIBLE HOR\_SYNC BLANK





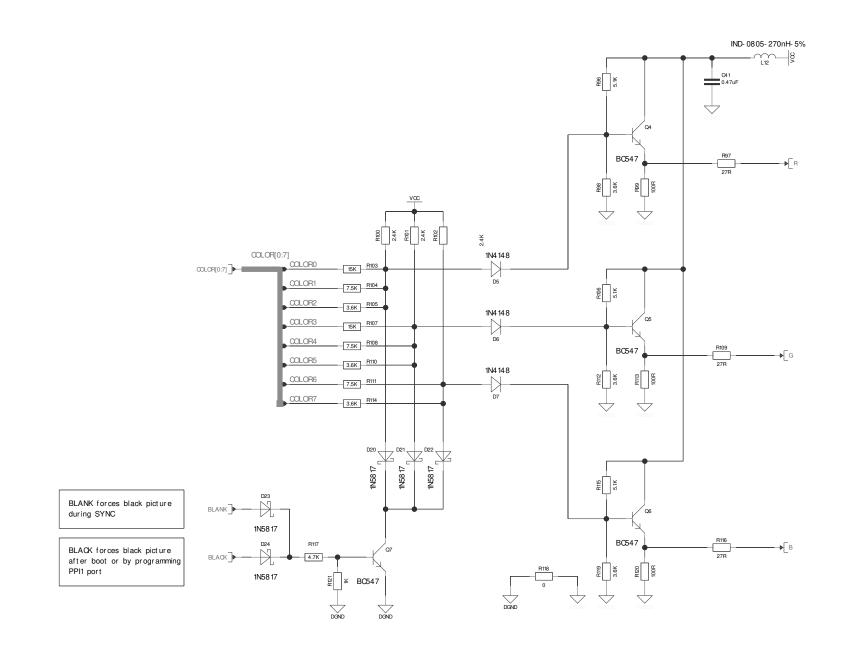
VISIBLE AREA

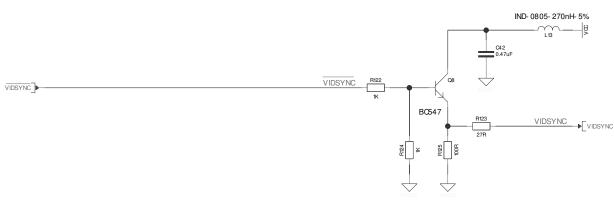


COMPUTE PM6)

221B Baker Street

IZE	DWG NO				REV	
А3	Alfa3		CR <sup>-</sup>	T_TimeDiagrams 1	a3	
RAWN BY		SHEET	of			
Sherlock		15	16	21/ 04/ 2019:11:27		







	I			1	1
SIZE	DWG NO			REV	1
А3	Alfa3		VideoOut 1	a3	
DRAWN BY		SHEET of			
Sherlock		16 16	21/ 04/ 2019:11:22		