

Discover our embedded software solutions

- ST, third parties and open source -

STM32 and STM8 microcontrollers

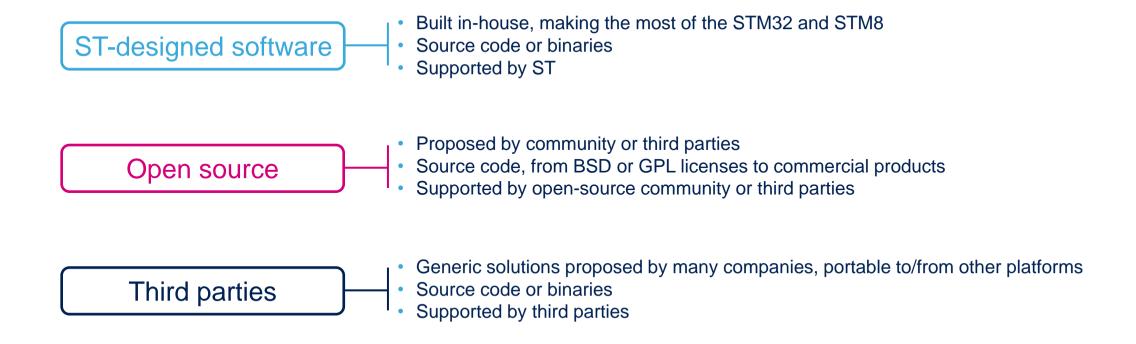
July 2018





A full portfolio and several models 2

- Extensive embedded software ecosystem around the STM32 and STM8
- You will find your solution, fitting your requirements in terms of price, license and support





A large community of third parties... and growing!



































































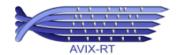




















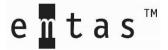








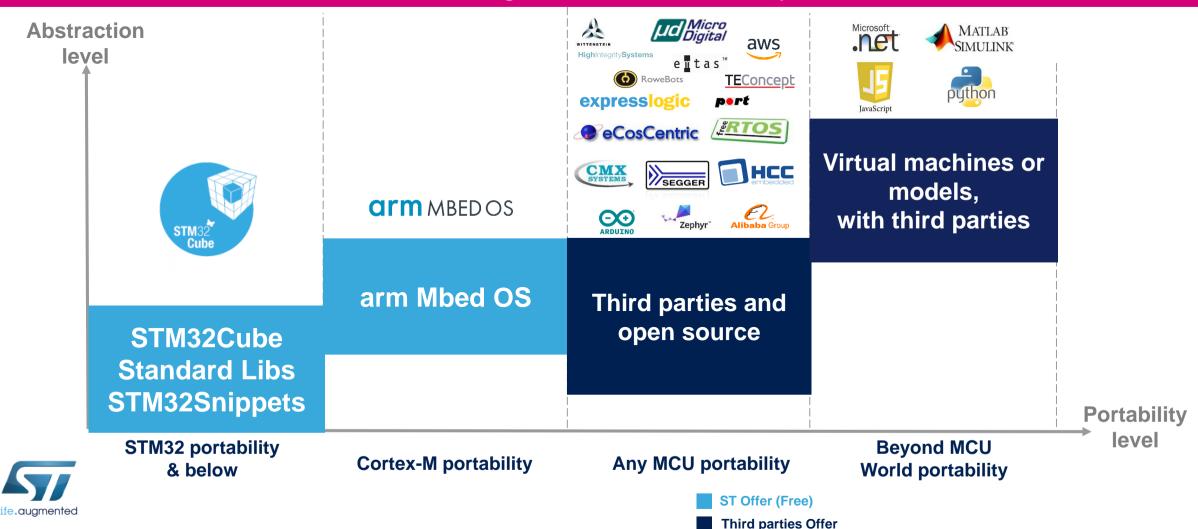






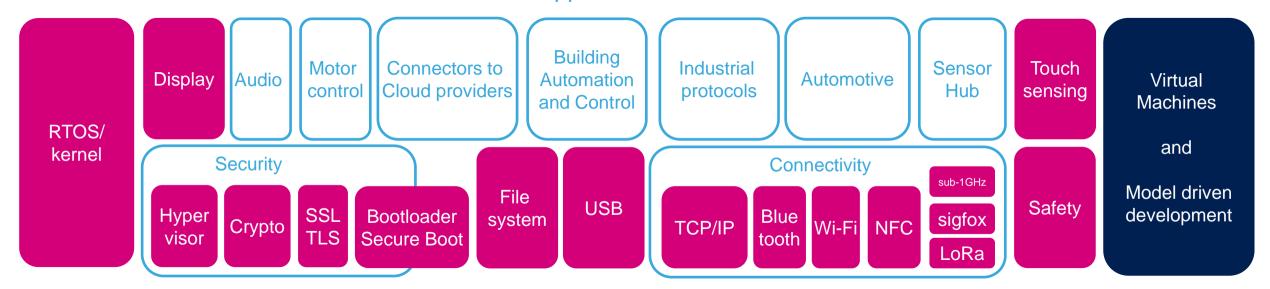
STM32 Embedded Software Offer - Overview

Several solutions mixing levels of Portability and Abstraction



Solutions at all levels 5

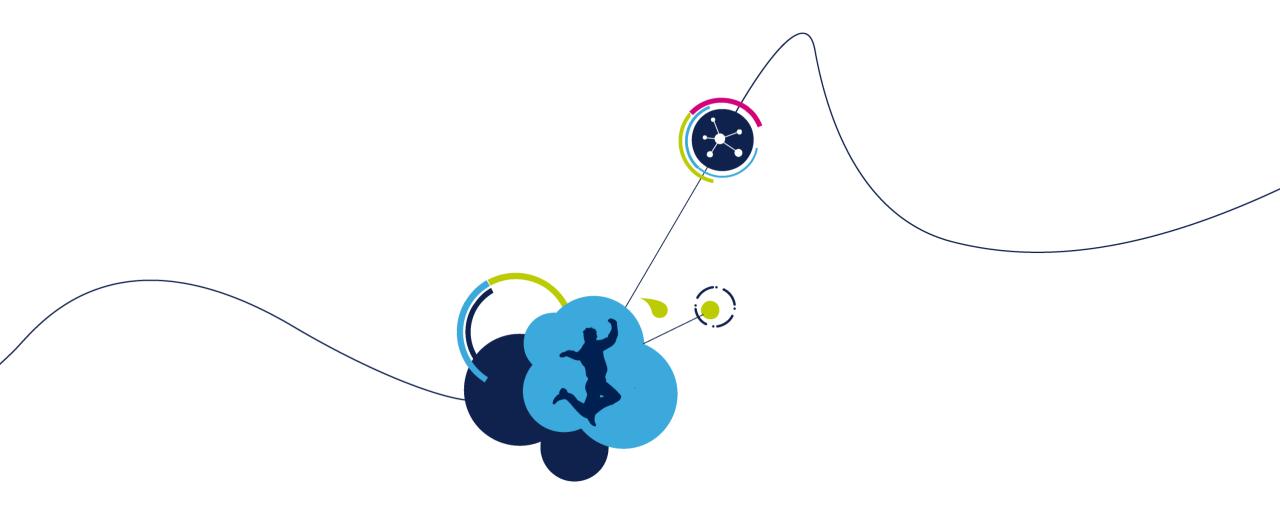
Middleware / Application fields



Hardware abstraction layer (HAL)



Select the area of interest for more details



Hardware abstraction layer (HAL)



Hardware abstraction layer ____

This layer is the first one to interact with the MCU hardware

- Consistent programming interface
 - When microcontrollers have different hardware implementations
- Full microcontroller coverage
 - All peripherals are handled





STM32 - Hardware abstraction layer

| | | | | Availability | | | | | | | | | | | |
|----------|---|----------------|------------|--------------|----|----|----------|----------|----|----|----|----|----|---------|--|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F30x | | F4 | F7 | H7 | LO | L1 | L4 /L4+ | |
| ST | STM32Snippets | Source | Free | Υ | | | | N.A. | | | | Y | | N.A. | |
| ST | Standard peripheral library | Source | Free | Y | Y | Y | <u>Y</u> | <u>Y</u> | Y | N¹ | N¹ | N¹ | Y | N¹ | |
| ST | STM32Cube - HAL Hardware Abstraction Layer | Open Source | Free (BSD) | Υ | Υ | Υ | Y | | Y | Υ | Υ | Υ | Υ | Y | |
| ST | STM32Cube – LL Low-Layer | Open Source | Free (BSD) | Υ | Υ | Υ | Y | | Υ | Υ | Y | Y | Y | Υ | |

N.A.: Not applicable. No plan to make it available

1/ Not plan to make it available

More details about hardware abstraction layer solutions: <u>STM32 Embedded Software overview</u>



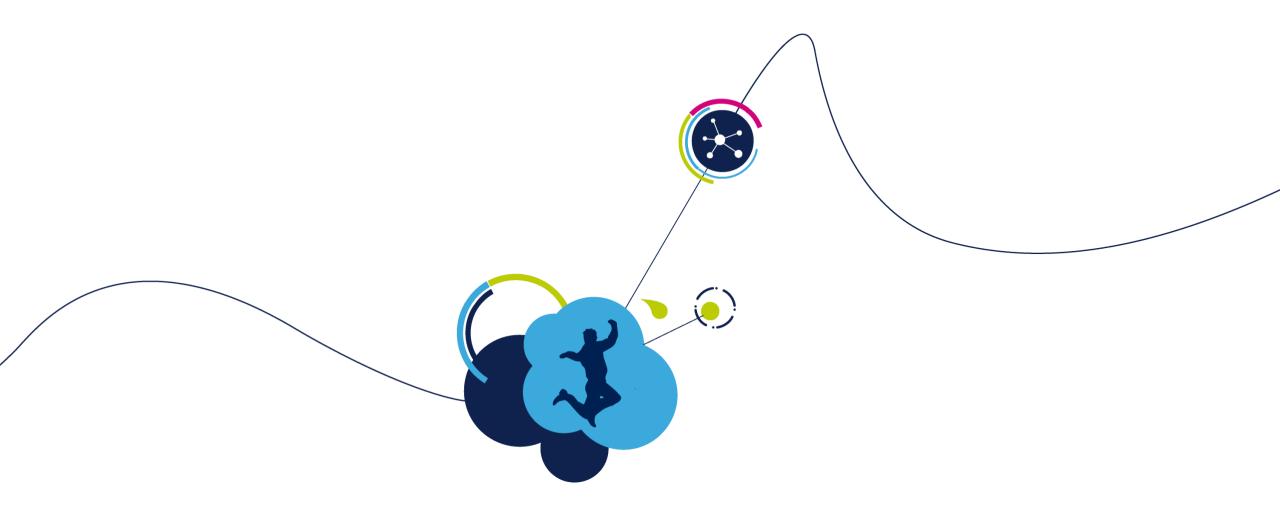


STM8 - Hardware abstraction layer

| | | | | | Availability | | | | | | |
|----------|-----------------------------|--------|------|-----------------|--------------|--------------------------------|--|--|--|--|--|
| Provider | Solution name | Model | Cost | STM8S STM8AF | STM8L10x | STM8Lx5x STM8Lx6x STM8AL | | | | | |
| ST | Standard peripheral library | Source | Free | <u>Y</u> | <u>Y</u> | <u>Y</u> | | | | | |







Middleware and application fields



Middleware and Application fields —11

Middleware stacks fill the gap between hardware and your application. ST and third parties bring the required solutions.

ST also proposes application software bricks to speed up customer development.

All standard middleware covered

- RTOS/kernel
- File system
- USB
- Security (hypervisor, crypto, SSL/TLS, secure boot)
- Connectivity (TCP/IP, Bluetooth, Wi-Fi, NFC, Cloud connectors...)
- Safety
- Industrial protocols
- Audio
- Motor Control



Middleware – RTOS / kernel 12

This is the root component to share time between several tasks on a single core. It ensures task switching within a known and limited duration.

A multitude of solutions for STM32 and STM8 available now





STM32 – RTOS / kernel (1/3) 13

| Provider | Solution name | Model | Cost | | | | | Ava | ailabi | ility | | | |
|-------------|---------------------------|---|--------------------|----|----|----|----|-----|--------|----------------|----|----|---------|
| Provider | | Wiodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4 /L4+ |
| Alibaba | Rhino RTOS | Open Source (Apache 2.0) | Free | N | N | N | N | Υ | Υ | N | Υ | N | Υ |
| arm | Mbed OS ⁵ | Open Source (Apache 2.0) | Free | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ | Υ |
| AWS | FreeRTOS kernel | Source ³ | Free | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| AVIX-RT | AVIX | Binaries | License | N | Υ | Υ | Υ | Υ | Υ | N | N | Υ | N |
| Chibios | ChibiOS/RT ChibiOS/NIL | Open source (GPL3) or Source | Free or License | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ | Υ |
| CMX | CMX-RTX | Source | License | N | Υ | Υ | Υ | Υ | Υ | N | N | Υ | N |
| CMX | CMX-Tiny+ | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ | N |
| eCosCentric | <u>eCosPro</u> | Modified GNU GPL or Source ¹ | Free or License | N | Υ | Υ | Υ | Υ | Υ | N ⁴ | N | Υ | Y |
| eForce | μC3/Compact | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ | Y |
| Emcraft | <u>uCLinux</u> | Open Source (GPL) ² | Free ² | N | N | N | N | Υ | Υ | N ⁴ | N | N | N |
| EUROS | <u>EUROS</u> | Binaries or source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |



^{1/} eCos is an open-source kernel, a subset of eCosPro. eCosPro comes with TCP/IP stack, FAT, jFFS2, RAM and ROM FS

^{2/} uCLinux is open source, but this company proposes some ports on STM32.

^{3/} FreeRTOS kernel license is MIT from V10, previous versions are under a modified GPL license

^{4/} Please contact supplier

^{5/} Mbed OS is Thread certified



STM32 – RTOS / kernel (2/3) 14

| Dravidar | Solution name | Madal | Coot | | | | | Avai | ilabilit | y | | | |
|---------------|--|--------------------------|---------|----|----|----|----|------|----------|----------------|----|----|--------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| Express Logic | <u>ThreadX</u> ⁵ | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Green Hills | <u>μ-velOSity</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ² | Υ | Υ | Υ |
| HCC | <u>eTaskSync</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ² | Υ | Υ | Υ |
| Keil / arm | MDK-ARM | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Mentor | Nucleus Kernel | Source | License | N | Υ | Υ | Υ | Υ | Υ | N ² | N | Υ | Y |
| Micrium | μC-OS | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ² | Υ | Υ | Y |
| Micro Digital | <u>SMX</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ² | Υ | Υ | Υ |
| NuttX | <u>NuttX</u> | Open Source ⁴ | Free | Υ | Υ | Υ | Υ | Υ | Υ | N | N | Υ | Υ |
| Quadros | RTXC Rtos | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Rowebots | <u>Unison</u> | Source ¹ | License | N | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ |
| SEGGER | embOS software expansion for STM32Cube | Source | License | Υ | Υ | Υ | Υ | Y | Υ | Υ | Y | Υ | Y |
| Sciopta | SCIOPTA 61508 ³ | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |



^{2/} Please contact supplier



^{3/} Safety certified RTOS for systems requiring certification

^{4/} BSD license

^{5/} ThreadX is part of the X-WARE IoT platform, which is Thread certified



STM32 – RTOS / kernel (3/3) 15

| Providor | Provider Solution name | Model | Model | Cost | | | | | Ava | ilabilit | y | | | |
|---|--------------------------------|---|-------------------|------|----|----|----|----|-----------|----------------|----------------|----|--------|--|
| Provider | Solution name | Wiodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ | |
| ST | STM32Cube – FreeRTOS kernel | Open source (modified GPL) ⁵ | Free | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | |
| Wittenstein - High Integrity Systems | OpenRTOS ¹ | Source | License | Y | Υ | Υ | Y | Y | Υ | N ⁴ | Y | Y | Υ | |
| Wittenstein - High Integrity Systems | SafeRTOS ² | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ⁴ | N ⁴ | Υ | Υ | |
| Wittenstein - High Integrity Systems | SafeRTOS CORE ³ | Source | License | Y | Υ | Υ | Υ | Y | Y | N ⁴ | N ⁴ | Y | Υ | |
| Zephyr | Zephyr kernel | Open Source | Free ⁶ | Υ | Υ | N | Υ | Υ | N | N | N | N | Υ | |



- 1/ OpenRTOS is FreeRTOS with commercial support
- 2/ Safety certified RTOS for systems requiring certification
- 3/ SafeRTOS version without full safety certification/documentation
- 4/ Available on customer request. Please contact supplier
- 5/ FreeRTOS kernel license is MIT from V10, previous versions are under a modified GPL license. Included in STM32Cube MCU packages (\Middlewares\Third_Party folder).
- 6/ Apache 2.0 license



STM8 - RTOS / kernel 16

| | | | | Availa | ability |
|-------------|------------------|------------------------------|-----------------|-----------------|-----------------|
| Provider | Solution name | Model | Cost | STM8S STM8AF | STM8L STM8AL |
| AtomThreads | AtomThreads RTOS | Open source (BSD) | Free | Υ | N ¹ |
| Chibios | ChibiOS/RT | Open source (GPL3) or Source | Free or License | Υ | Y |
| CMX | CMX-Tiny+ | Source | License | Y | N ¹ |
| SEGGER | <u>embOS</u> | Source | License | Y | Y |







Middleware – Hypervisor

An hypervisor allows application and task isolation, enabling independent secure domains inside the same MCU chip. The hypervisor runs in privileged mode, and manage all access to security-critical peripherals

- Many solutions
 - Through third parties





STM32 – Hypervisor 18

| Duestiden | Calutian nama | nome Medel | | | | | | Ava | labilit | y | | | |
|-------------|---------------|------------|---------|----|----|----|----|----------------|---------|----|----|----|--------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| arm | Mbed uVisor | Source | Free | N | N | N | N | Y ¹ | N | N | N | N | N |
| Prove & Run | ProvenCore-M | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |







Middleware – Crypto 19

Cryptology helps users ensure the security of data or authentication. This is a key element in ensuring the integrity of IoT applications for instance.

Many supported algorithms

Through third parties (some with certifications) or directly from ST

| Often-seen acronyms | | | | | | | |
|----------------------|--|--|--|--|--|--|--|
| Symmetric ciphering | Ciphering method that ensures the reuse of a unique key, both for encryption and decryption. Example: AES, DES, 3DES, ARC4, | | | | | | |
| Asymmetric ciphering | Ciphering method based on pair of key, a private and a public one. This also brings some kind of authentication: only the owner of private key can decrypt content that was encrypted with a public one. And people able to decrypt with public key can be sure it was encrypted by owner of private key. Example: ECDSA, RSA, | | | | | | |
| Hashing | Method to calculate a unique value for a given data content. This allows ensuring a content was not modified for instance. Example: MD5, SHA, | | | | | | |





STM32 – Crypto (1/2) 20

| Provider | ovider Solution name Model | | Cost - | | | | | Ava | ilabilit | у | | | |
|--------------|---|----------|---------|----|----|----|----|-----|----------|----------------|----|----|--------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| Cypherbridge | UVPN SDK IKEv1/IKEv2/IPsec | Source | License | N | N | N | N | Υ | Υ | N ¹ | N | N | N |
| EUROS | SSL/TLS SSL v3, TLS 1.0, 1,1. 1.2 | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| EUROS | Cryptographic library, Verifiable Encryption manager 1. Encryption methods: - ARC4, - Blowfish,-Camellia- CAST5 - DES, - DAS, - ECC, - MD4, - MD5 - RSA, - SHA1,- SHA256, - SHA512 - TwoFish, - Whirpool, - HMAC96 - HMAC256 2. Coding: - Rice, - RLE (Run-length encoding) 3. Compression: - zlib, - libbz2, - LZ77,- LZW based (ZIP, GZ) - Huffman, - Shannon-Fano | Binaries | License | Y | Y | Y | Y | Y | Y | Y | Y | Y | Y |
| НСС | Verifiable Encryption manager AES, 3DES, DSS, EDH, MD5, RSA, SHA1, SHA256 | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| Rowebots | UNISON SSL/TLS Stack AES, Blowfish, Triple-DES (3DES), DES, ARC4, Camellia, XTEA ECB, CBC, CFB, CTR, GCM, CCM MD2, MD4, MD5, SHA-1, SHA-224, SHA-256, SHA-384, SHA-512, RIPEMD-160 ECC | Source | License | N | Y | Y | Y | Y | Y | Y | N | Y | Y |
| SEGGER | emSecure signatures | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| SEGGER | emLib AES and emLib DES | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| SEGGER | emFile encryption | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |







STM32 - Crypto (2/2) 21

| Provider | Solution name | Model | Cost | | | | | Ava | ilabilit | y | | | |
|----------|--|------------------------------------|--------------------|----|----|----|----|-----|----------|------------|----|----|--------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| ST | STM32 Cryptographic library ^{1, 2} AES, DES, 3DES, ARC4, MD5, SHA1, SHA2, RSA sig, ECC Key gen, ECDSA, | Binaries | Free | N | Y | Υ | Y | Y | N | N | N | Y | N |
| ST | X-CUBE-CRYPTOLIB | Binaries | Free | Υ | Υ | Υ | Υ | Υ | Υ | Y 3 | Υ | Υ | Y |
| wolfSSL | WolfCrypt ¹ , part of wolfSSL MD2, MD4, MD5, SHA-1, SHA-256, SHA-384, SHA-512, BLAKE2b, RIPEMD-160, Poly1305 AES (CBC, CTR, GCM, CCM), Camellia, DES, 3DES, ARC4, RABBIT, HC- 128, ChaCha20 RSA, DSS (DSA), DH, EDH, NTRU ECDH-ECDSA, ECDHE-ECDSA, ECDH-RSA, ECDHE-RSA | Open source (GPL2) or Source | Free or license | N | N | Y | N | Y | Y | Y | Y | Υ | Y |





^{2/} Subject to trade regulations. See website.



Middleware – SSL/TLS (1/2)

- Transport Layer Security (TLS) and its predecessor, Secure Sockets Layer (SSL), are cryptographic protocols that provide communications security.
- The Transport Layer Security protocol aims primarily to provide privacy and data integrity between a client and a server





STM32 – SSL/TLS solutions 23

| Provider | Solution name | Model | Cost | Availability | | | | | | |
|--------------|-----------------------------------|------------------------------|-----------------|--------------|----|----|----|----------------|--|--|
| Fiovidei | Solution name | Widaei | Cost | F107 | F2 | F4 | F7 | H7 | | |
| arm | Mbed TLS ¹ | Open source | Free | Υ | Υ | Υ | Υ | Y | | |
| CypherBridge | Embedded TLS SDK (uSSL™) | Source | License | N | Υ | Υ | Υ | N ² | | |
| HCC | Verifiable SSL/TLS | Source | License | Υ | Υ | Υ | Υ | N ² | | |
| Oryx Emb. | CycloneSSL | Open source (GPL2) or Source | Free or license | Υ | Υ | Υ | Υ | Y | | |
| SEGGER | <u>emSSL</u> | Source | License | Υ | Υ | Υ | Υ | N ² | | |
| ST | STM32Cube – Mbed TLS ³ | Open source | Free | Υ | Υ | Υ | Υ | Y | | |
| wolfSSL | Embedded SSL Library | Open source (GPL2) or Source | Free or license | N | Υ | Υ | Υ | N ² | | |



^{2/} Please contact supplier

^{3/} Apache 2.0 license, included in STM32Cube MCU packages (\Middlewares\Third_Party folder)



STM32 – SSL/TLS solution details 24

| Provider | Solution name | Details |
|--------------|--------------------------|--|
| arm | Mbed TLS | SSL version 3, TLS version 1.0, 1.1, 1.2, More |
| CypherBridge | Embedded TLS SDK (uSSL™) | IETF standard SSL 3.0/TLS 1.2 protocols. Supported crypto and hash functions include: RSA, DSS, PKCSv1.5, OAEP, DES, 3DES, AES, RC4, SHA1, SHA2, MD2, MD4, MD5, RNG, X.509 certificate Processing |
| HCC | Verifiable SSL/TLS | TLS 1.0, 1.1, 1.2 (RFC 5246), SSL3.0, DTLS 1.2 (RFC6347), 1.0 (RFC 4347), RFC 6520, HTTP over TLS (RFC 2818), HTTPS, FTPS, embedded encryption manager for full certificate management, cipher suites: DH/DHE/DSS/ECDHE/RSA, AES/RC4/3DES, SHA/MD5 |
| Oryx Emb. | <u>CycloneSSL</u> | Server and/or client operation, Supports TLS 1.0, 1.1, 1.2 and SSL 3.0, Supports DTLS 1.0 and DTLS 1.2, Robust and efficient implementation, Supports ECC (Elliptic Curve Cryptography), Rich set of TLS cipher suites (including Suite B profile), RSA, Diffie-Hellman and ECDH key exchange algorithms, PSK (Pre-Shared Key) cipher suites, Supports stream ciphers, CBC block ciphers as well as AEAD ciphers (CCM and GCM), ChaCha20Poly1305 AEAD, Cryptographic library for common encryption algorithms (RC4, IDEA, DES, 3DES, AES, Camellia, SEED and ARIA), Supports MD5, SHA-1, SHA-256 and SHA-384 hash algorithms, SSL/TLS session resumption, PKIX path validation, Compliant with BSD socket API, Supports hardware accelerated encryption engines (when available), Flexible memory footprint. Built-time configuration to embed only the necessary features, Portable architecture (no processor dependencies), The library is distributed as a full ANSI C and highly maintainable source code |
| SEGGER | <u>emSSL</u> | TLS 1.0, 1.1 and 1.2. More |
| ST | STM32Cube - TLS | SSL version 3, TLS version 1.0, 1.1, 1.2, More |
| wolfSSL | Embedded SSL Library | SSL version 3.0 and TLS versions 1.0, 1.1, 1.2, and 1.3 (client and server), DTLS 1.0, 1.2 support (client and server) More |





Middleware – Bootloader / Secure Boot

A bootloader aims to enable firmware update in the field through a communication interface.

STM32 devices embed a built-in bootloader in system memory, supporting UART, SPI, I2C, CAN, USB interfaces.

Alternatively, developers can implement their own bootloader in flash memory (also called In-Application Programming)

ST also proposes a solution for Secure Boot and Secure Firmware Update





STM32 - Bootloader / Secure Boot

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | |
|----------------|-------------------------------|------------------------------|--------------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|--|
| Provider | Solution name | Iwiodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ | |
| ST | X-CUBE-IAP-USART ³ | Source | Free | N ² | Υ | N ² | Υ | N ² | Υ | |
| ST | X-CUBE-IAP-SD ³ | Source | Free | Υ | N ² | Υ | |
| ST | X-CUBE-SBSFU ⁴ | Source | Free | N | N | N | N | N | N | N | N | N | Y | |
| Cypherbridge | uLoadXL SDK | Source | License | N | N | Υ | N | Υ | Υ | N¹ | N | N | N | |
| eCosCentric | RedBoot | Source | License | N | Υ | Υ | Υ | Υ | Υ | N ¹ | N | Υ | Υ | |
| EUROS | Bootloader | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | |
| Feaser | <u>OpenBLT</u> | Open source (GPL3) or Source | Free or Licence | N¹ | Υ | Υ | Υ | Υ | N ¹ | N ¹ | N ¹ | N ¹ | N¹ | |
| HCC | <u>Bootloaders</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | Υ | Υ | Y | |
| Simma Software | ssBL-target, ssBL-PC | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | N ¹ | N ¹ | Υ | |



^{2/} Available on STM32Cube, so porting is very easy

^{3/} In-application programming example

^{4/} Secure Boot + Secure Firmware Update solution, with example

STM32 – Bootloader / Secure Boot details 27

| Provider | Solution name | Details |
|----------------|----------------------|--|
| ST | X-CUBE-SBSFU | Secure Boot / Root of Trust, Secure firmware loader, Dual image support for safe firmware programing, Single image support for maximized user application size, Asymmetric and symmetric cryptographic schemes supported, Cryptography with integrated firmware, Secure Engine services, STM32 security mechanisms |
| Cypherbridge | uLoadXL SDK | Software updates and secure boot loader for embedded platforms. Safe install, to manage multiple images for an update, activation and safe-boot to rollback or factory version. The integrated boot loader provides secure root of trust and software integrity checks |
| eCosCentric | RedBoot | Debug and bootstrap firmware |
| Feaser | <u>OpenBLT</u> | UART, CAN, TCP/IP, USB, SD-CARD External flash and serial EEPROM |
| HCC | Bootloaders | Serial, USB Device, USB Host , FAT Fail-saferty, AES encryption |
| Simma Software | ssBL-target, ssBL-PC | Supported protocols include CAN, J1939, J1708, CANopen, UDS, Bluetooth, USB, and RS232 |





STM8 – Bootloader 28

| | | | | Availability | | | | |
|----------------|------------------------------|--------|---------|--------------|--------|--|--|--|
| Provider | Provider Solution name Model | Model | Cost | STM8S | STM8L | | | |
| | | | | STM8AF | STM8AL | | | |
| Simma Software | ssBL-target, ssBL-PC | Source | License | Υ | N^1 | | | |





Middleware – File system 29

A file system is the way in which files are named and how they are placed logically for storage and retrieval. Several standards exist, such as FAT and JFFS2

- Safety solutions
 - Ensuring data is not corrupted in any way (power supply removal, ...)
- NAND memory access solutions
 - With error correction and wear-leveling





STM32 – File system (1/2) 30

| Drovidor | Solution name | Model | Coot | | | | | Ava | ilabilit | y | | | |
|---------------|--|-------------------|---------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|----------------|--------|
| Provider | Solution name | Wodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| AWS | FreeRTOS+™ FAT SL ³ | Source | Free | Υ | Υ | Υ | Υ | Υ | Υ | N ⁴ | Υ | Υ | Y |
| arm | Mbed LittleFileSystem, FATFileSystem | Open source | Free | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Υ | Y |
| ChaN | <u>FatFS</u> | Open source (BSD) | Free | Y ² | Y ² | Y2 |
| CMX | CMX-FFS | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N |
| Cypherbridge | <u>uFile</u> | Source | License | N | N | Υ | N | Υ | Υ | N ⁴ | N | N | N |
| EmCraft | JFFS2, YaFFS, FAT, NFS, etc | Open source (GPL) | Free | N | N | Υ | N | Υ | Υ | N ⁴ | N | N | N |
| eCosCentric | eCosPro-YAFFS ¹ , MMFS, JFFS2 | Source | License | N | Υ | Υ | Y | Y | Y | N ⁴ | N | Y | Y |
| eForce | μC3-FileSystem | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N^4 | Υ | Υ | Υ |
| Express Logic | <u>FileX</u> + LevelX | Source | License | Υ | Υ | Υ | Y | Y | Υ | N ⁴ | Υ | Υ | Y |
| EUROS | FMS, FAT, SafeFAT | Binaries | License | Υ | Υ | Υ | Y | Y | Y | Υ | Y | Y | Y |
| HCC | FAT FS: <u>FAT, THIN, SafeFAT</u> Flash FS: <u>SafeFlash, TINY</u> Flash Translation Layer: <u>SafeFTL</u> | Source | License | Y | Υ | Υ | Y | Y | Y | Υ | Y | Y | Y |



^{2/} FatFS ported on STM32 available on demos

^{3/} For STM32 users, the license is the same as FreeRTOS kernel (MIT)

^{4/} Please contact supplier



STM32 – File system (2/2) 31

| Drevider | Solution name | Model | Cost | | | | | Avai | labilit | у | | | |
|--|--------------------------------|----------------------|---------|----|----|----|----|------|---------|----------------|----|----|--------|
| Provider | Solution name | Iviodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| Wittenstein - High Integrity Systems | CONNECT File System | Source | License | Y | Y | Y | Y | Y | Y | N¹ | Y | Y | Y |
| Green Hills | μ-velOSity File System | Source | License | N | Υ | Υ | Υ | Υ | Υ | N ¹ | N | Υ | Y |
| Keil / arm | MDK-ARM Flash | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Y | Υ | Y |
| Mentor Embedded | Nucleus Storage | Source | License | N | Υ | Υ | Υ | Υ | Y | N¹ | N | Υ | Y |
| Micrium | μC/FS | Source | License | Υ | Υ | Υ | Υ | Υ | Y | N ¹ | Υ | Υ | Y |
| Micro Digital | <u>smxFS</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Y | N ¹ | Υ | Υ | Y |
| Quadros | <u>RTXCfatfile</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | Υ | Υ | Y |
| Rowebots | Unison FAT File System | Source | License | N | Υ | Υ | Υ | Υ | Υ | N¹ | N | Υ | Y |
| SEGGER | <u>emFile</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | Υ | Υ | Y |
| ST | STM32Cube - FatFS ² | Open source (BSD) | Free | Υ | Υ | Υ | Y | Υ | Y | Υ | Υ | Υ | Y |



STM8 – File system 32

| | | | | Availability | | | | |
|----------|---------------|-------------------|---------|----------------|----------------|--|--|--|
| Provider | Solution name | Model | Cost | STM8S | STM8L | | | |
| | | | | STM8AF | STM8AL | | | |
| ChaN | Petit FatFS | Open source (BSD) | Free | N ¹ | Y ² | | | |
| HCC | FAT THIN | Source | License | Υ | Υ | | | |
| SEGGER | <u>emFile</u> | Source | License | Υ | Υ | | | |





Universal Serial Bus requires a dedicated software stack. This serial bus is organized in a star topology with host and device roles, with the host organizing the traffic. Several device classes are specified, in order to ease communication in different application cases.

| | Often-seen acronyms |
|---------|--|
| OTG | On-The-Go: An OTG peripheral can switch host and device roles on the fly |
| HUB | Defines what protocols to implement to build a hub application |
| MS | Mass storage: Protocols to interact with storage block devices (for files) |
| HID | Human interface device: Protocols for peripherals interacting with humans (mouse, keyboard, etc.) |
| CDC | Communication device class: Protocols for serial communications, different sub-classes define details, for instance ACM for a standard COM port, or ECM for modems |
| Printer | Defines what protocols to implement to build a printer application |
| Audio | Defines what protocols to implement to build an audio application (microphone, headset, etc.) |
| DFU | Device firmware upgrade: Protocols to implement firmware upgrade ability |
| USB-PD | USB Power Delivery |
| ТСРМ | USB Type-C Port Manager |





STM32 – USB solutions (1/2) 34

| | | Model Cost | | | | | Avai | labili | ty | | | | |
|--------------------|-------------------------------------|---------------------------------|--------------------|----|----|----|------|--------|-----------|----------------|----|----|--------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| Chibios | ChibiOS/HAL | Open source (GPL3) or Source | Free or License | Υ | Υ | Υ | Υ | Υ | Y | N¹ | Υ | Υ | Y |
| CMX | CMX-USB <u>Device</u> , <u>Host</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | N |
| eCosCentric | eCosPro-Host, Device | Source | License | N | Υ | Υ | Υ | Υ | Y | N ¹ | N | Υ | Y |
| EUROS | USB Host & Device | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Y |
| EmCraft | Linux USB Host | Open source (GPL) | Free | N | N | Υ | N | Υ | N | N ¹ | N | N | N |
| Express Logic | <u>USBX</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | Y |
| HCC | HCC-USB Host, Device | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | Y |
| Keil / arm | MDK-ARM USB | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | Y |
| Mentor Embedded | Nucleus USB | Source | License | N | Υ | Y | Υ | Υ | Y | N¹ | N | Υ | Υ |
| Micrium | USB Host, USB Device | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | Y |





STM32 – USB solutions (2/2) 35

| | | | | Availability | | | | | | | | | | |
|---------------|--|--------|-------------------|----------------|----------|--------------|----------|-----------------------|----------|----|----------------|-----------------------|-----------------------|----------------|
| Provider | Solution name | Model | Model Cost | | F1 | | | | | | | | | |
| | | | | F0 | Others | F105 F107 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| Micro Digital | <u>smxUSB</u> | Source | License | Υ | Υ | | Υ | Υ | Υ | Υ | N ¹ | Υ | Υ | Υ |
| Quadros | <u>RTXCusb</u> | Source | License | N¹ | Υ | | Υ | Υ | N¹ | N¹ | N ¹ | N¹ | N¹ | N ¹ |
| Rowebots | <u>Unison USB System</u> | Source | License | N | Υ | | Υ | Υ | Υ | N | N ¹ | Υ | N | Υ |
| SEGGER | emUSB Device, emUSB Host | Source | License | Υ | Υ | | Υ | Υ | Υ | Υ | N¹ | Υ | Υ | Y |
| ST | USB FS device library | Source | Free | <u>Y</u> | <u>Y</u> | N | N | <u>Y</u> | N | N | N ¹ | N | <u>Y</u> | N |
| ST | USB FS&HS Host&Device lib | Source | Free | N | N | <u>Y</u> | <u>Y</u> | N | <u>Y</u> | N | N ¹ | N | N | N |
| ST | STM32Cube – USB Host&Device ⁴ | Source | Free | Y ² | Υ2 | | Υ | Y ² | Υ | Υ | N ¹ | Y ² | Y ² | Y ² |
| ST | X-CUBE-USB-PD ⁵ | Binary | Free | Υ | N | | N | N | N | N | N | N | N | N |
| Thesycon | Embedded USB Device | Source | License | N¹ | N¹ | | Υ | N¹ | Υ | Υ | N¹ | N¹ | N¹ | N¹ |
| Zephyr | USB device stack | Source | Free ³ | Y | Υ | | N | Y | Y | N | N | N | N | Y |



- 1/ Available on customer request. Please contact supplier
- 2/ Device only
- 3/ Apache 2.0 license
- 4/ Included in STM32Cube MCU packages (\Middlewares\Third_Party folder)
- 5/ Examples for P-NUCLEO-USB001 and P-NUCLEO-USB002 Nucleo Packs, and for ON-FUSB3-STM32



STM32 – USB solutions details (1/2) 36

| Provider | Solution name | Details |
|-----------------|-------------------------------------|--|
| Chibios | ChibiOS/HAL | Device: HID, MS, CDC |
| CMX | CMX-USB <u>Device</u> , <u>Host</u> | Device: HID, MS, CDC (ACM, ECM, RNDIS), Audio, Midi, MTP, PHDC Host: HID, MS, CDC (ACM, ECM, RNDIS, OBEX), Audio, Midi, Printer, HUB |
| eCosCentric | eCosPro-Host, Device | Device: MS, CDC (ACM, ECM, EEM, RNDIS) Host: MS, CDC (ACM, ECM, EEM, RNDIS), Hub |
| EUROS | USB Host & Device Stack | Device: HID, MS, CDC (ACM, ECM) Host: HID, MS, CDC (ACM, ECM), HUB |
| Express Logic | <u>USBX</u> | Device: HID, MS, CDC (ACM, ECM, RNDIS), Still Image, PTP, PictBridge, DFU, PIMA w/MTP Host: HID, MS, CDC (ACM, ECM), Audio, Printer, HUB, ASIX, PIMA, GSER, PROLIFIC |
| EmCraft | Linux USB Host | Host: HID, MS, CDC (ACM, ECM), HUB |
| HCC | HCC-USB | Device: HID, MS, CDC (ACM, ECM, EEM, RNDIS), Audio, Video, MIDI, PTP, MTP, PictBridge, DFU, PHDC Host: HID, MS, CDC (ACM, ECM, EEM, NCM, OBEX, FTDI), Audio, Video, PTP, MTP, iPod, HUB |
| Keil / arm | MDK-ARM USB | Device: HID, MS, CDC (ACM), Audio Host: HID, MS |
| Mentor Embedded | Nucleus USB | Device: HID, MS, CDC (ACM, ECM) Host: HID, MS, CDC (ACM, ECM), HUB |





STM32 – USB solutions details (2/2) 37

| Provider | Solution name | Details |
|---------------|-----------------------------|--|
| Micrium | USB Host, USB Device | Device: HID, MS, CDC (ACM), Audio, PHDC (Medical) Host: HID, MS, CDC (ACM), Audio, Printer, PHDC (Medical) |
| Micro Digital | <u>smxUSB</u> | Device: HID, MS, CDC (ACM, RNDIS), Audio+Midi, Video, PTP, MTP, DFU Host: HID, MS, CDC (ACM, ECM, Ethernet Adapter, WiFi and wireless modem,), Audio+Midi, Video, Printer, HUB |
| Quadros | RTXCusb | Device: MS, CDC (ACM, ECM, RNDIS) Host: HID, MS, CDC (ACM), HUB |
| Rowebots | <u>Unison USB System</u> | Device: MS, CDC (ACM) Host: MS, CDC (ACM), HUB, others on demand (inc . PHDC) |
| SEGGER | emUSB Device, emUSB Host | Device: HID, MS, CDC (ACM), RNDIS, Printer, MTP Host: HID, MS, CDC (ACM), HUB |
| ST | USB FS device library | Device: HID, MS, CDC (ACM), Audio, DFU |
| ST | USB FS&HS Host&Device | Device: HID, MS, CDC (ACM), Audio, DFU Host: HID, MS |
| ST | STM32Cube – USB Host&Device | Device: HID, MS, CDC (ACM), Audio, DFU Host: HID, MS, CDC (ACM), Audio, MTP |
| Thesycon | Embedded USB Device | Device: Audio, CCID, Mass Storage, network, HID, CDC (ACM) |
| Zephyr | USB device stack | Device: CDC (ACM), DFU |





Middleware – TCP/IP (1/2) 38

- TCP and IP were developed by a US Department of Defense research project to connect a number of different networks designed by different vendors into a network of networks (the Internet).
- It was initially successful because it delivered a few basic services that everyone needs (file transfer, electronic mail, remote logon) across a very large number of client and server systems, and is now widely deployed.





Middleware – TCP/IP (2/2)

| | Often-seen acronyms |
|---------|---|
| ARP | Address resolution protocol: Provides physical address from IP address |
| IP | Internet protocol: Primary protocol in Internet Protocol Suite. 2 flavors: IPv4 and IPv6. IPv4 will disappear as it only supports up to 2 ³² addresses, not enough for future needs, while IPv6 supports 2 ¹²⁸ |
| 6LoWPAN | IPv6 over low-power wireless personal area networks: Provides IPv6 connectivity to low-rate wireless networks |
| IPSec | Internet protocol security: Secured version of IP, using cryptography |
| TCP | Transmission control protocol: Provides reliable, ordered delivery of a stream of bytes |
| UDP | User datagram protocol: Provides unreliable service. Datagrams may arrive in any order, duplicated, or may be missing. Used for time-sensitive applications, when data drop is better than delay |
| DHCP | Dynamic host configuration protocol: Provides means to allocate IP address dynamically |
| DNS | Domain name system: Translates domain names meaningful to humans into numerical IP ones |
| FTP | File transfer protocol: Provides means to copy files from one host to another |
| TFTP | Trivial file transfer protocol: Similar to FTP, but based on UDP, and simpler (for example, no directory) |
| SMTP | Simple mail transfer protocol: Used to send e-mail to a server |
| POP | Post office protocol: Used to retrieve e-mail from a server |
| HTTP | Hypertext transfer protocol: Used by web browsers |
| SSL/TLS | Transport layer security: Secured container for application protocols using cryptography. Example: HTTPS means HTTP over SSL, FTPS, etc IPSec applies cryptography at a lower level than SSL/TLS, making it more universal. However SSL is widely used. |





STM32 – TCP/IP solutions (1/2) 40

| Dueviden | Colution nome | Model | Cont | Availability | | | | | | | |
|---|--|-------------------|---------|--------------|----|----|----|----------------|--|--|--|
| Provider | Solution name | Model | Cost | F107 | F2 | F4 | F7 | H7 | | | |
| AWS | FreeRTOS+TM TCP ² | Source | Free | Υ | Υ | Υ | Υ | N ¹ | | | |
| AWS | FreeRTOS+TM UDP ² | Source | Free | Υ | Υ | Υ | Υ | N ¹ | | | |
| CMX | CMX-TCP/IP | Source License | | Υ | Υ | Υ | Υ | N¹ | | | |
| CMX | CMX-MicroNet | Source License | | Υ | Υ | Υ | Υ | N¹ | | | |
| Cypherbridge | Embedded SSH SDK (uSSH™) | Source | License | N | Υ | Υ | Υ | N¹ | | | |
| EUROS | TCP/IP stack | Binaries | License | Υ | Υ | Y | Y | Υ | | | |
| Express Logic | NetX and NetX Duo IPv4/IPv6 and NetX Secure SSL/TLS/DTLS | Source | License | Y | Υ | Y | Υ | N¹ | | | |
| eCosCentric | SecureSockets, SecureShell eCosPro stacks | Source | License | Υ | Y | Y | Y | N ¹ | | | |
| eForce | μNet3 | Source | License | Υ | Υ | Υ | Υ | N ¹ | | | |
| EmCraft | Linux TCP/IP stack | Open source (GPL) | Free | N | Υ | Υ | Υ | N¹ | | | |
| GreenHills | μ-velOSity TCP/IP v4/v6 | Source | License | Υ | Υ | Υ | Υ | N ¹ | | | |
| Wittenstein - High Integrity Systems | CONNECT TCP | Source | License | Υ | Υ | Y | Y | N ¹ | | | |

^{2/} The license is the same as FreeRTOS kernel (MIT)



STM32 – TCP/IP solutions (2/2)

| Provider | Solution name | Model | Cost | Availability | | | | | | | |
|-----------------|---------------------------------|------------------------------|-------------------|--------------|------------|------------|----------------|----------------|--|--|--|
| Flovidei | Solution name | Model | Cost | F107 | F2 | F4 | F7 | H7 | | | |
| HCC | MISRA HCC-TCP/IP v4/v6 | Source | License | Υ | Y | Υ | Υ | N ¹ | | | |
| Interniche | <u>NicheStack</u> | Source | License | Υ | Υ | Υ | N ¹ | N^3 | | | |
| Interniche | embTCP v4/v6 | Source Licens | | Υ | Υ | Υ | N ¹ | N ³ | | | |
| Keil / arm | MDK-ARM TCPNET | Source | License | Υ | Y | Y | N¹ | N ³ | | | |
| Mentor Embedded | Nucleus Network | Source | License | Υ | Y | Y | Υ | N ³ | | | |
| Micrium | μC/TCP-IP | Source | License | Υ | Y | Υ | Υ | N ³ | | | |
| Micro Digital | smxNS and smxNS6 (Dual IPv6/v4) | Source | License | Υ | Υ | Υ | Υ | N ³ | | | |
| Oryx Emb. | <u>CycloneTCP</u> | Open source (GPL2) or source | Free or license | Υ | Υ | Υ | Υ | Υ | | | |
| Quadros | RTXC Quadnet | Source | License | Υ | Υ | Υ | Υ | N ³ | | | |
| Rowebots | <u>Unison TCP-IP/v4-v6</u> | Source | License | N | Υ | Υ | Υ | N^3 | | | |
| SEGGER | embOS/IP | Source | License | Υ | Y | Υ | Υ | Y | | | |
| SICS | <u>LwIP</u> | Open source (BSD) | Free | <u>Y</u> 1 | <u>Y</u> 1 | <u>Y</u> 1 | N | N | | | |
| ST | STM32Cube – LwIP ² | Open source (BSD) | Free | Υ | Y | Υ | Υ | Y | | | |
| Zephyr | Zephyr Networking | Source | Free ⁴ | Υ | N | Υ | N | N | | | |



^{1/} A port to STM32 was implemented by ST

^{2/} A port to STM32 was implemented by ST, as part of STM32Cube, Licensing model cascaded to end-user. Included in STM32Cube MCU packages (\Middlewares\Third Party folder)

^{3/} Please contact supplier

^{4/} Apache 2.0 license



STM32 – TCP/IP solution details (1/2)

| Provider | Solution name | Details |
|---|-----------------------------|---|
| AWS | FreeRTOS+™ TCP | ARP, DHCP, DNS, LLMNR, NBNS, UDP |
| AWS | FreeRTOS+™ UDP | ARP, DHCP and DNS |
| CMX | CMX-TCP/IP | PPP, PPPoE, ARP, IGMP, ICMP, IPv4, UDP, TCP, DHCP(cs), DNS, FTP(cs), IMAP4, NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, SSH, TFTP(cs), HTTP(s) |
| CMX | CMX-MicroNet | PPP, ARP, IGMP, ICMP, IPv4, UDP, TCP, DHCP(c), DNS, FTP(cs), POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, TFTP (c), HTTP(s) |
| CypherBridge | Embedded SSH SDK (uSSH™) | SSH 2.0, Configurable DSS and RSA asymmetric session, Configurable crypto with 3DES AES and blowfish support, SCP, SFTP |
| EUROS | TCP/IP stack | PPP, PPPoE, ARP, IGMP, ICMP, IPv4, IPv6, IPSec/IKE, UDP, TCP, DNS, DHCP(cs), FTP(cs), NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, TFTP, HTTP(cs), SFTP, FTPS, SSH |
| Express Logic | NetX and NetX Duo IPv4/IPv6 | PPP, ARP, IGMP, ICMP, IPv4, IPv6, IPSec/IKE, UDP, TCP, DNS, DHCP(c), FTP(cs), NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), TFTP, HTTP(s) |
| eCosCentric | <u>SecureSockets</u> | SSH2 |
| eCosCentric | SecureShell | SSL/TLS |
| eCosCentric | eCosPro stacks | PPP, ARP, ICMP, UDP, TCP, IPv4, IPv6, DHCP, BOOTP, SMTP, TFTP, FTP(c+s), HTTP, SNMP, NTP, mDNS, Bonjour |
| eForce | μNet3 | PPP, ARP, IGMP, ICMP, IPv4, IPv6, UDP, TCP, DNS, DHCP(c), FTP(s), HTTP(cs), TFTP, SNMP, SNTP, Telnet(s), POP3(c), SMTP, SSL/TLS |
| EmCraft | Linux TCP/IP stack | PPP, DNS, NAT, SSH (cs), DHCP (cs), SNMP (cs), Telnet (cs), FTP (cs), HTTP (cs) |
| HCC | MISRA HCC-TCP/IP v4/v6 | ARP, ICMP, IPv4, IPv6, UDP, TCP, DNS, DHCP(c), FTP(s), SMTP, TFTP(s), HTTP(s) |
| Wittenstein - High Integrity Systems | CONNECT TCP | TCP, UDP, ARP, ICMP, IPv4, DNS, DHCP, FTP, TFTP, HTTP, LLMNR, NBNS, Auto-IP, standard sockets or callback interface |



(c): Client

(s): Server

(cs): Client and Server



STM32 – TCP/IP solution details (2/2)

| Provider | Solution name | Details |
|--------------------|---------------------------------|--|
| Green Hills | μ-velOSity TCP/IP v4/v6 | ARP, ICMP, IGMP, IPv4, IPv6, IPv4/6, UDP, TCP, DNS, DHCP(c), |
| Interniche | NicheStack | SLIP, PPP, PPPoE, ARP, IGMP, ICMP, IPv4, IPv6, IPSec/IKE, UDP, TCP, DNS, DHCP(cs), FTP(cs), NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, TFTP, HTTP(s), RTP/RTCP, SSH |
| Interniche | embTCP v4/v6 | ARP, TCP/IP v4, IPv4/v6 HTTP, FTP Telnet ICMP, UDP, TCP. DNS, DHCP |
| Keil / arm | MDK-ARM TCPNET | SLIP, PPP, ARP, IPv4, ICMP, UDP, TCP, DNS, DHCP(c), FTP(s), SMTP, SNMP, Telnet(s), TFTP(s), HTTP(s) |
| SICS | <u>LwIP</u> | PPP, ARP, ICMP, IPv4, UDP, TCP, DHCP(c), IPv6 |
| Mentor Embedded | Nucleus Kernel | PPP, PPPoE, ARP, IGMP, ICMP, IPv4, IPv6, IPSec/IKE, UDP, TCP, DHCP(c), FTP(cs), NAT, SNMP, SNTP, Telnet(cs), SSL/TLS, TFTP (cs), HTTP(cs) |
| Micrium | μC/TCP-IP (and μC/SSL) | ARP, ICMP, IPv4, UDP, TCP, DNS, DHCP(c), FTP(cs), SMTP, POP3(c), SNTP, Telnet(s), SSL/TLS, TFTP, HTTP(s) |
| Micro Digital | smxNS and smxNS6 (Dual IPv6/v4) | SLIP, PPP, PPPoE, ARP, IGMP, ICMP, IPv4, IPv6, IPv4/6, UDP, TCP, DNS, mDNS, DHCP(cs), FTP(cs), NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, TFTP, HTTP(cs), SSH |
| Oryx Emb. | CycloneTCP | ARP, IPv4, ICMP, IGMP, IPv6, ICMPv6, MLD, NDP, SLAAC, UDP, TCP, DNS, DHCP(c), DHCPv6(c), SMTP(c), FTP(cs), HTTP(s), |
| Quadros | RTXC Quadnet | PPP, PPPoE, ARP, IGMP, ICMP, IPv4, IPv6, IPSec/IKE, UDP, TCP, DNS, DHCP(cs), FTP(cs), NAT, POP3(c), SMTP, SNMP, SNTP, Telnet(s), SSL/TLS, TFTP, HTTP(cs), UPnP, Prioritized Packets Handling |
| Rowebots | <u>Unison TCP-IP/v4-v6</u> | PPP, ARP, ICMP, IGMP, IPv4, IPv6, IPv4/6, 6LowPan, IPSec, UDP, TCP, DNS, DHCP(cs), SMTP(c), SNMP, Telnet(s), TFTP(cs), HTTP(cs), NAT |
| SEGGER | embOS/IP | PPP, PPPoE, ARP, ICMP, IGMP, IPv4, UDP, TCP, DNS, DHCP(cs), FTP(cs), SMTP(c), SNTP(c), Telnet(s), TFTP(cs), HTTP(s), PTP IEEE 1588 |
| SICS | Contiki/ulP6 | IPv6, 6LoWPAN |
| ST | STM32Cube - LwIP | PPP, ARP, ICMP, IPv4, UDP, TCP, DHCP(c), IPv6 |
| Zephyr | Zephyr Networking | IPv4, IPv6 (dual stack support), ICMPv4, ICMPv6, UDP, TCP, HTTP, MQTT, CoAP, LWM2W, RPL, DNS/ 6LoWPAN |



(c): Client

(s): Server

(cs): Client and Server





Middleware – Bluetooth

Bluetooth is a wireless communication technology for exchanging data over short distances, typically used in the mobile world between phones and accessories.

Solutions with STM32 + Bluetooth transceiver

 Several solutions are available, using STM32 with ST's <u>SPBTLE-RF</u> or other components

| | Often-seen acronyms |
|----------------------|---|
| Bluetooth Low Energy | (Also called Bluetooth Smart) very interesting for application running out of battery (coin cell type) as power consumption is very low, with a lower data rate and connection time |
| Classic Bluetooth | Original Bluetooth before Bluetooth Low Energy appearance. Still required for certain Audio applications. |
| Dual mode devices | Devices able to handle Classic Bluetooth, and Bluetooth Low Energy. Also called Bluetooth Smart ready |
| HCI | Host/controller interface: Standardized communication between controller and radio chips |
| SPP | Serial port profile: Profile that emulates serial line over Bluetooth |
| A2DP | Advanced audio distribution profile: Profile to stream high-quality audio |
| HSP | Headset profile: Profile to implement a basic headset application |
| HDP | Health device profile: Profile designed to facilitate transmission and reception of medical data |
| HFP | Hands-free profile: Typical profile used in cars for hands-free phone use |





STM32 – Bluetooth solutions 45

| Drevider | Colution name | Madal | Cont | | | | | Ava | iilabil | ity | | | |
|----------|--|--|--------------------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| arm | Mbed BLE ⁴ | Source | Free | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Alpwise | OpenSynergy Blue SDK | Binaries or Source | License + royalties | N ² | Υ | Υ | N ² | Υ | Y | N ² | N ² | N ² | Y |
| Alpwise | BlueWiseLE® bluetooth 5 Link Layer stack | Binaries or Source License + royalties | | Υ | Υ | Y | Υ | Y | Y | Υ1 | Υ | Y | Y |
| A&W | <u>PhoneLink</u> | Binaries or Source | License and/or royalties | N | Υ | Υ | N | Υ | Y | Y 1 | N | N | N |
| Clarinox | ClarinoxBlue ⁸ | Binaries or Source | License and/or royalties | Y 1 | Y ¹ | Y ¹ | Y ¹ | Υ | Y ¹ | Y 1 | Y ¹ | Y ¹ | Y ¹ |
| EUROS | BLE stack ⁶ | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Rowebots | UNISON Bluetooth System | Source | License | N | Υ | Υ | Y | Y | Y | Y ¹ | N | Υ | Y |
| SEARAN | <u>dotStack</u> | Binaries or Source | License and/or royalties | Υ | Υ | Υ | Y | Υ | Y | Y 1 | Υ | Y | Y |
| ST | X-CUBE-BLE1 | Binaries and Source | Free | N ³ | N ³ | N ³ | N ³ | Υ | N ³ | N ³ | Υ | N ³ | Y |
| ST | I-CUBE-nRF51DRV | Binaries and Source | Free | N^3 | N ³ | Υ | N ³ | N ³ |
| ST | STM32CubeL4-BLE ⁵ | Source | Free | N ³ | N ₃ | N ³ | N ³ | Y |
| Zephyr | Bluetooth | Source | Free ⁷ | N | Υ | N | Υ | Υ | N | N | N | N | Υ |

- 1/ Available on customer request. Please contact supplier
- 2/ Available on specific conditions. Please contact supplier
- 3/ Available on STM32Cube so porting is very easy
- 4/ Support BLE, Heart Rate Service, iBeacon on various ST boards including DISCO-L475VG-IOT01A, X-NUCLEO-IDB05A1 with STM32 Nucleo boards
- 5/ point to point and HeartRate profile example running on running on B-L475E-IOT01A with ST SPBTLE-RF
- 6/ SmartBond™ DA14681 Bluetooth® low energy SoC adapted to the EUROS RTOS
- 7/ Apache 2.0 license
- 8/ ClarinoxBlue supports mutliple RTOS and many wireless radio supporting HCI (UART, USB)





STM32 – Bluetooth solution details 46

| Provider | Solution name | Details |
|----------|--|--|
| arm | Mbed BLE | More |
| Alpwise | OpenSynergy Blue SDK | BT 5.x BLE Dual-Mode Host Stack and Profiles (compliant with version v4.2 and as well as all earlier specification versions including v2.1+EDR, v3.0+High Speed and v4.0, and v4.1) classic profiles: HSP, HFP, MAP, PBAP, AVRCP, A2DP, VDP, SAP, OPP, FTP, BIP, BPP, GPP, CTN, HCRP, HID, PAN, HDP LE profiles: ATT and GATT, SPP-like, alert notification, battery service, device information service, proximity/FindMe, ID over GATT, Health thermometer profile, haert rate profile, transport discovery profile, internet protocol support (IPv6 w/6LoWPAN) |
| Alpwise | BlueWiseLE® bluetooth 5 Link Layer stack | BT4.2 BLE Single Mode Host Stack Supported profiles over GAP, GATT: Blood Pressure, Phone Alert, HID over Gatt, iBeacon, Proximity, Alert Notification, Find me, Pulse Oximeter, Running Speed & Candence Health Thermometer, Time, Heart Rate, Network Availability, Location & Navigation, Cycling Speed & Cadence, Cycling power, Glucose Meter, Weight Scale, and additional proprietary profiles, Data Exchange (Serial port over BLE), Cable Replacement, FOTA (Firmware update Over The Air), Voice over BLE |
| A&W | <u>PhoneLink</u> | BT2.1+EDR, BT4.0 Supported Profiles: HFP, HSP, PBAP, A2DP, AVRCP, HID, OBEX, FTP, OPP, SPP, PAN, MAP and more |
| Clarinox | <u>ClarinoxBlue</u> | BT4.2, BT 4.1, BT4.0 + EDR + LE + Mesh Supported profiles: Classic Porfiles (A2DP, AVRCP, BIP, BPP, CTN, DI, FTP, GAP, GAVDP, HCRP, HDP, HFP, HID, HSP, MAP, MPS, OPP, PAN, PBAP, SDAP, SPP), Classic Protocols (AVCTP, AVDTP, BNEP, HCI, L2CAP, MCAP, OBEX, RFCOMM, SDP), Smart Profiles / Services (AIOP, AIOS, ANP, ANS, BAS, BCS, BMS, BLP, BLS, CGMP, CGMS, CPP, CPS, CSCP, CSCS, CTS, DIS, ESP, ESS, FMP, GAP, GLP, GLS, HIDS, HOGP, HPS, HRP, HRS, HTP, HTS, IAS, IPS, IPSP, LLS, LNP, LNS, NDCS, OTP, OTS, PASP, PASS, PLXP, PLXS, PXP, RSCP, RSCS, RTUS, ScPP, SCPS, TIP, TDS, TPS, UDS, WSP, WSS, Mesh, Custom), Smart Protocols (ATT, GATT, SM, MESH) |
| SEARAN | <u>dotStack</u> | BT2.1+EDR, BT4.0, BT4.1, BT 4.2, and 5.0 ready. Single and dual mode. Apps on iOS and Android BLE (GATT) - ANP/ANS, FMP, HIDS, HOGP, PASP/PASS, PXP, TIP, BAS, DIS, IAS, LLS, TPS, ANCS, BLP/BLS, GP, HTP, HRP/HRS BR/EDR - SPP, HID, PAN, MAP, FTP, HSP, HFP, A2DP, AVRCP, PBAP, iAP, HCRP |
| ST | X-CUBE-BLE1 | BT4.0, BT4.1 All profiles supported |
| ST | I-CUBE-nRF51DRV | BT4.0, BT4.1 All profiles supported |
| Zephyr | Bluetooth | BT5.0, GAP, GATT, BR/EDR, |



Wi-Fi[™] is an implementation of the IEEE 802.11 radio communication specification for wireless local area networking. It is usually used with a TCP/IP stack, so all TCP/IP bricks can be reused on Wi-Fi, adapting the lowest firmware layer

Solutions with STM32 + Wi-Fi transceiver

• Several solutions are available, using STM32 with ST's <u>SPWF01SA</u>, <u>SPWF04SA</u> modules or other modules (Espressif ESP8266, Inventek ISM43362-M3G-L44)





STM32 – Wi-FiTM solutions 48

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | |
|----------|---------------------------------------|-----------------------|-----------------------------|----------------|-------|----------------|----------------|----|----------------|----------------|-------|----------------|----------------|
| | | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| arm | Mbed Wifi ⁶ | Source | Free | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Clarinox | <u>ClarinoxWiFi</u> ⁷ | Binaries or Source | License and/or Royalties | N | N | N | N | Y | Y | Y | Υ | Y | Y |
| ST | STM32CubeL4/L4+- WiFi ² | Source | Free | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | Y |
| ST | STM32CubeF4 – WiFi ³ | Source | Free | N ¹ | N^1 | N ¹ | N ¹ | Υ | N ¹ | N ¹ | N^1 | N ¹ | N ¹ |
| ST | STM32CubeF7 – WiFi ⁴ | Source | Free | N ¹ | N¹ | N¹ | N¹ | N¹ | Υ | N¹ | N¹ | N¹ | N¹ |
| ST | X-CUBE-WIFI1 ⁵ | Source | Free | N ¹ | Υ | N¹ | N¹ | Υ | N¹ | N¹ | Υ | N¹ | Y |

- 1/ Available on STM32Cube, so porting is very easy
- 2/ Client Server application and HTTP Server application examples running on B-L475E-IOT01A with Inventek ISM43362-M3G-L44
- 3/ Client Server application and HTTP Server application examples running on STM32F413H-DISCO with Inventek ISM43362-M3G-L44
- 4/ In-Application programming client example running on STM32F723E-DISCO and STM32F769I-DISCO
- 5/ Middleware and examples for ST's SPWF01SA and SPWF04SA modules
- 6/ Support ST boards including DISCO-L475VG-IOT01A, DISCO-F413ZH
- 7/ Clarinox WiFi protocol stack supports multiple RTOS, and SDIO, SPI, USB





Middleware & Drivers – NFC 49

Complementary to other wireless technologies, Near Field Communication (NFC) is designed to execute short transactions between two devices in close proximity.

Operating in the 13.56 MHz (RFID) HF band, it is based on ISO/IEC 14443 & ISO/IEC 15693 RFID standards, and regulated by NFC Forum industry association

Solutions with STM32 + ST25 NFC / RFID Tag & Reader Ics



- NFC / RFID reader Ics
 - ST25R3911B high performance HF Reader/NFC initiator (ISO14443, ISO15693, FeliCa) with 1.4 W output power, supporting VHBR, capacitive sensitive wake up and Automatic Antenna Tuning (AAT)
 - Other product variant: ST25R3912, ST25R3913, ST25R3914 and ST25R3915
 - CR95HF entry level HF Reader/ NFC Initiator (ISO14443, ISO15693, FeliCa)
- Dynamic NFC tag Ics
 - ST25DV dynamic RFID ISO15693 NFC Type 5 Tag Ics
 - M24LR dynamic RFID ISO15693 / NFC compatible Tag ICs (M24LR04 is NFC Forum Type 5)
 - M24SR dynamic RFID ISO14443 type A NFC Type 4 Tag ICs





STM32 – NFC solutions 50

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | |
|----------|---------------|--------|------|----------------|----------------|----------------|----------------|----|----------------|----------------|----------------|----------------|--------|
| | | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| ST | X-CUBE-NFC1 | Source | Free | Υ | Υ | N ¹ | Υ | Υ | N ¹ | N ¹ | Υ | Υ | Y |
| ST | X-CUBE-NFC2 | Source | Free | N¹ | N ¹ | N ¹ | N ¹ | Υ | N¹ | N ¹ | Y | N¹ | N¹ |
| ST | X-CUBE-NFC3 | Source | Free | N ¹ | Υ | N¹ | N¹ | Υ | N¹ | N¹ | N¹ | N ¹ | N¹ |
| ST | X-CUBE-NFC4 | Source | Free | N ¹ | N¹ | N ¹ | N ¹ | Υ | N¹ | N¹ | Y | N ¹ | N¹ |
| ST | X-CUBE-NFC5 | Source | Free | N ¹ | N ¹ | N ¹ | N ¹ | Υ | N ¹ | N ¹ | N ¹ | N ¹ | Y |

NFC / RFID readers

- X-CUBE-NFC3: Drivers and Middleware (NFC Lib, NDEF Lib) for CR95HF, HF Reader/ NFC Initiator, packaged to run on X-NUCLEO-NFC03A1
- X-CUBE-NFC5: Drivers and Middleware (RFAL Lib) for ST25R3911B, HF Reader/ NFC Initiator, packaged to run on X-NUCLEO-NFC05A1

Dvnamic NFC tags

- X-CUBE-NFC1: Drivers and Middleware (NDEF Lib) for M24SR Dynamic NFC/RFID Tag IC, packaged to run on X-NUCLEO-NFC01A1
- X-CUBE-NFC2: Drivers and Middleware (NDEF Lib) for M24LR Dynamic NFC/RFID Tag IC, packaged to run on X-NUCLEO-NFC02A1
- X-CUBE-NFC4: Drivers and Middleware (NDEF Lib) for ST25DV Dynamic NFC/RFID Tag IC, packaged to run on X-NUCLEO-NFC04A1







Middleware – LoRa® 51

LoRa® is a type of wireless telecommunication network designed to allow long range communications at a very low bit-rate and enabling long-life battery operated sensors. LoRaWAN™ defines the communication and security protocol that ensures the interoperability with the LoRa® network

Solutions with STM32 + LoRa® radio

- Semtech SX1276MB1MAS, SX1276MB1LAS and SX1272MB2DAS LoRa® radio expansion boards
- Murata CMWX1ZZABZ-091 LoRa® module
- USI WM-SG-SM-42 LoRa® module
- RiSiNGHF® RHF0M003 modem for LoRa®
- CMWX1ZZABZ-xxx module for sigfox





STM32 – LoRa® solution 52

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | |
|------------|---------------------------|---------------|-------------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----|----|--------|
| | | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| ST | I-CUBE-LRWAN ² | Source | Free | N ¹ | Υ | Υ | Y |
| arm | MbedOS LoRa ³ | Source | Free | N | N | N | N | N | N | N | Υ | N | N |
| StackForce | LoRaWAN stack | Source/Object | License/Royalties | N | N | N | N | N | N | N | Υ | N | N |

1/ Available on STM32Cube, so porting is very easy

2/ Supports NUCLEO-L053R8, NUCLEO-L152RE and NUCLEO-L476RG with I-NUCLEO-LRWAN1

B-L072Z-LRWAN1

I-NUCLEO-LRWAN1

P-NUCLEO-LRWAN1 (NUCLEO-L073RZ + I-NUCLEO-SX1272D)

P-NUCLEO-LRWAN3

3/ Available on DISCO-L072CZ-LRWAN1







Middleware – Sigfox™ ■

Sigfox™ is a long range wireless area network allowing low-power sensors to benefit from a planetary network, enabling end devices with low BOM cost

Solutions with STM32 + radio

Murata CMWX1ZZABZ-xxx module





STM32 – SigfoxTM solution 54

| Provider | Solution name | Model | Cost | | | | | Ava | ilabili | ty | | | |
|----------|---------------|--------|------|----------------|-------|----------------|----------------|----------------|----------------|----------------|-----------------------|----------------|----------------|
| | | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| ST | X-CUBE-SFOX | Source | Free | N ¹ | N^1 | N ¹ | Y ² | N ¹ | N ¹ |





^{1/} Available on STM32Cube, so porting is very easy

^{2/} Supports Murata CMWX1ZZABZ-xxx module only in B-L072Z-LRWAN1

sub-1gнz Middleware — sub-1GHz 55

Sub 1GHz RF operates in the ISM spectrum bands below Sub 1GHz – typically in the 769 – 935 MHz, 315 MHz and the 468 MHz frequency range. This spectrum band below 1GHz is particularly useful for RF IOT applications

Solutions with STM32 + radio

SPIRIT1





STM32 – sub-1GHz solution 56

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | |
|------------|--|-----------------|----------------|----------------|----------------|----------------|----------------|----|----------------|----------------|----|----|----------------|--|
| | | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ | |
| ST | X-CUBE-SUBG1 ² | Source | Free | N ¹ | N ¹ | N ¹ | N ¹ | Υ | N ¹ | N ¹ | Υ | Y | N ¹ | |
| StackForce | emb6 - 6LoWPAN based IoT stack ³ | Source/Object | Free / License | N | N | N | N | N | N | N | Y | N | N | |
| StackForce | WM-Bus stack ³ | Source/Binaries | License | N | N | N | N | N | N | N | Υ | N | N | |



^{2/} Supports WM-bus with X-NUCLEO-IDS01A4, Contiki OS and Contiki 6LoWPAN protocol on STM32F4 and STM32L1





Safety targets protection of people and equipment from danger and harm, through a set of counter-measures ensuring defined targets

Ready for certification offers on STM32

Through third parties or directly from ST

| | Often-seen acronyms |
|--------|--|
| ClassB | The IEC 60730 safety standard defines the test and diagnostic methods that ensure the safe operation of embedded control hardware and software for household appliances. The IEC 60730 standard category Class B prevents unsafe operation of the controlled equipment. |
| SIL | Safety Integrity Level (SIL) is defined by the industry standard IEC 61508 as a relative level of risk-reduction provided by a safety function, or to specify a target level of risk reduction. |
| ASIL | ASIL is a risk classification scheme defined by the ISO 26262 - Functional Safety for Road Vehicles standard. This is an adaptation of the Safety Integrity Level used in IEC 61508 for the automotive industry. |





STM32 – Safety solutions 58

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | | |
|----------|-------------------------|--------|---------------|--------------|----------------|----------------|----------------|----------------|----------------|----------------|----|----|----------------|--|--|
| Provider | | Wodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ | | |
| ST | X-CUBE-CLASSB | Source | Free | Υ | Υ | Y | Υ | Υ | Υ | N ¹ | Υ | Υ | Υ | | |
| ST | STM32-CLASSB-SPL | Source | Free | Y | Y | Y 3 | Υ | Υ3 | N | N | N | Υ3 | N | | |
| ST | X-CUBE-STL ⁴ | Object | Free | Y | N | N | N ¹ | N ¹ | N¹ | N ¹ | N | N | N ¹ | | |
| MESCO | Safety Design Package | Source | Lic. + Royal. | N | N ² | N ² | N^2 | N^2 | N ² | N^2 | N | Υ | N ² | | |







- 1/ Porting ongoing
- 2/ Can be ported
- 3/ Derived package only (not certified)
- 4/ IEC 61508 SIL3 (SC3) compliant, certified by TÜV Rheinland. Delivered as compiler independant object code under NDA



STM8 – Safety solutions 59

| | | | | Availability | | | | | | | |
|----------|-----------------|----------|------|--------------|--------|-----------------|--|--|--|--|--|
| Provider | Solution name | Model | | STM8S | STM8AF | STM8L STM8AL | | | | | |
| ST | STM8-SafeCLASSB | Source | Free | Υ | Υ | Υ | | | | | |
| ST | STM8A-SafeASIL | Document | Free | N | Y | N | | | | | |











ST's MCUs can drive displays through serial or parallel interfaces.

Getting the most from hardware and software

• ST has built a close relationship with third parties providing software solutions based on our microcontrollers. Customers can make the most of their hardware.

| | Often-seen acronyms |
|----------------|--|
| Anti aliasing | Technique to minimize distortion artifacts known as aliasing when presenting a high-resolution image at a lower resolution. Aliased images show some stair effects on curves. Anti-aliasing removes this by modifying edge pixel colors. |
| Alpha blending | Alpha blending is the process of combining a translucent foreground color with a background color, thereby producing a new blended color. |
| GUI | Graphical user interface |
| bpp | Bits per pixel (also known as color depth: Number of bits used to represent the color of a single pixel in an image. 1 bpp corresponds to monochrome images. |
| Palette | Technique to lower image memory size by storing the set of colors used in a table and using this table for each pixel |
| JPEG | Commonly used method of lossy compression for digital image. The degree of compression can be adjusted, allowing a trade-off between storage size and image quality. JPEG typically achieves 10:1 compression with little perceptible loss in image quality. |
| RGB / ARGB | Color model in which red, green and blue are merged to reproduce a broad array of colors. "A" field in ARGB format handles transparency information. |
| Widgets | Element of a graphical user interface that can be changed by the user (such as text box, radio button) |





STM32 – Display solutions (1/2)

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | | | |
|---------------|------------------------------|---------------------|---------|----------------|----|----------------|----|----------|----------------|----------------|----------------|----------------|--------|--|--|--|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ | | | |
| ST | Embedded GUI library | Source | Free | N | Υ | Υ | Υ | Υ | N | N | N | Υ | N | | | |
| ST | <u>STemWin</u> | Binaries | Free | N | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | N | | | |
| ST | STM32Cube - STemWin | Binaries | Free | N | Υ | Υ | Υ | Υ | Υ | Υ | N | Υ | Y | | | |
| ST | ST Java UI | Binaries | Free | Y | Υ | Υ | N | Υ | Υ | Υ | Υ | Υ | N | | | |
| Altia | Altia Design and Deep screen | Source | License | Y | Υ | Υ | Υ | <u>Y</u> | Υ | Υ | N ¹ | Υ | N | | | |
| Crank | <u>Storyboard</u> | Binaries | License | N | N¹ | N ¹ | Υ | Υ | Υ | Υ | N | N ¹ | N | | | |
| Draupner | <u>TouchGFX</u> | Binaries /Source | License | N | N | N | N | Υ | Y | Υ | N | N | Y | | | |
| eCosCentric | eCosPro-PEG | Source | License | N ¹ | Υ | Υ | Υ | Υ | N ¹ | N ¹ | N ¹ | N¹ | N¹ | | | |
| EUROS | <u>eGUI</u> | Binaries | License | N¹ | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | Υ | N | | | |
| Express Logic | GUIX | Source | License | Y | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N | | | |





STM32 – Display solutions (2/2)

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | |
|---------------|-----------------|----------------------|---------|--------------|----|----|----|----|----|----|----------------|--------|--|--|
| Provider | Solution name | Wiodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | L0 | L1 | L4/L4+ | | |
| Korulab | Koru | Binaries | License | N | N | N | N | Υ | Υ | N | N | Υ | | |
| Micrium | μC/GUI | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N | | |
| Micro Digital | <u>PEG</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y | | |
| Rowebots | emWin GUI | Source | License | N | Υ | Υ | Υ | Υ | Υ | N | N ¹ | N | | |
| SEGGER | <u>emWin</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Y | Y | Υ | N | | |
| Tara Systems | Embedded Wizard | Binaries / Source | License | N | N | N | N | Υ | Υ | N | N | Υ | | |





Capacitive touch sensing is an electrical cost-efficient technology, replacing conventional mechanical switches to detect user actions, to build modern GUI look and feel.

NRE/royalty-free C source code

• Complete solution for touch keys, linear and rotary touch sensors, with acquisition, post processing and API layers, debounce filtering and calibration functions

| | Often-seen acronyms |
|-----------------------|--|
| Surface capacitance | The capacitance of a single ended electrode is modified when the finger gets close to it. |
| Projected capacitance | The capacitance between two sensing electrodes is modified when the finger gets close to them. |
| RC acquisition | Resistor-capacitor acquisition for surface capacitance only. It consists in measuring the charge and discharge time duration of a RC cell made of the electrode capacitance and a load resistor. |
| CT acquisition | Charge transfer acquisition for surface capacitance only. It consists in measuring the duration for charging the electrode capacitance and transferring part of the accumulated charge into a sampling capacitor. The CT acquisition is more robust than the RC one. |





STM32 – Touch-sensing solutions 64

| Drovidor | Solution name | Acquicition | Model | Coot | Availability | | | | | | | | | | |
|----------|-----------------------------|-------------|--------|------|--------------|----|----|----------|----|----|----|----|----------|--------|--|
| Provider | Solution name | Acquisition | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | Н7 | LO | L1 | L4/L4+ | |
| ST | STM32 Touch Sensing Library | СТ | Source | Free | <u>Y</u> | N | N | <u>Y</u> | N | N | N | N | <u>Y</u> | N | |
| ST | STM32Cube – Touch Sensing | СТ | Source | Free | Y | N | N | Y | N | N | N | Υ | Y | Y | |





STM8 – Touch-sensing solutions 65

| Provider | Solution name | Acquisition | Model | Cost | Availa | ability |
|----------|----------------|-------------|--------|------|--------------|--------------|
| Tiovidei | Colution name | Acquisition | Model | Cost | STM8S/STM8AF | STM8L/STM8AL |
| ST | STM8 Touch Lib | RC + CT | Source | Free | <u>Y1</u> | <u>Y</u> 1 |







STM32 – Instrumentation 66

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | |
|----------|-----------------|--------|------|--------------|----|----|----|----|----|----|----|----|--------|--|
| Provider | Solution name | | | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ | |
| AWS | FREERTOS+™ CLI¹ | Source | Free | Y | Υ | Y | Y | Y | Υ | Y | Y | Y | Y | |

Enabling your FreeRTOS application to process command line input







Connectors to cloud providers 67

STM32 acts as an end device securely connected to cloud services providers

Connectors to cloud provider (Amazon AWS, Microsoft Azure, IBM Watson...)

STM32Cube Expansion Packages

AWS, Azure, Watson Exosite, Grovestream, AvSvstem, Litmus Automation, Ubidots Configuration of the connectivity interfaces (WiFi, Ethernet...) Connection to the Internet and the cloud provider servers publication of messages Subscription over a variety of connectivity protocols (MQTT, HTTPS, ...)

Examples of end device applications using ST sensors and RFID/NFC tag and connecting to cloud providers

STM32 ODE Functions packs AWS, Azure, Watson...

Temperature and humidity sensor (HTS221)

Pressure sensor (LPS25HB)

Motion sensors (LIS3MDL, LSM303AGR, LSM6DS0, LSM6DSL, IIS2DH)

Writing/reading the RFID/NFC tag (M24SR64-Y)







STM32 - Connectors to cloud providers 68

| Provider | Solution name | Model | Cost | | Availability | | | | | | | | |
|---------------|------------------------------|--------|-------------------|----|--------------|----|----|----|----|----|----|----|--------|
| Provider | Solution name | Wodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| ST | X-CUBE-CLD-GEN ¹ | Source | Free | N | N | N | N | Υ | Υ | N | N | N | Υ |
| ST | X-CUBE-AWS | Source | Free | N | N | N | N | Υ | Υ | N | N | N | Υ |
| ST | X-CUBE-AZURE | Source | Free | N | N | N | N | Υ | Υ | N | N | N | Υ |
| ST | X-CUBE-WATSON | Source | Free | N | N | N | N | Υ | Υ | N | N | N | Υ |
| ST | FP-CLD-AWS1 | Source | Free | N | N | N | N | Υ | N | N | N | N | N |
| ST | FP-CLD-AZURE1 | Source | Free | N | N | N | N | Υ | N | N | N | N | Υ |
| ST | FP-CLD-WASTON1 | Source | Free | N | N | N | N | Υ | N | N | N | N | N |
| AWS | Amazon FreeRTOS ² | Source | Free ³ | N | N | N | N | N | N | N | N | N | Υ |
| Cypherbridge | uMQTT Toolkit | Source | License | N | N | N | N | Υ | Υ | N | N | N | N |
| Express Logic | X-WARE IoT Platform | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N | N | N | N |





^{1/} Supports B-L475E-IOT01A, 32F413HDISCOVERY, 32F769IDISCOVERY, P-L496G-CELL01 and P-L496G-CELL02. Examples with Exosite, Grovestream, AvSystem, Litmus Automation and Ubidots cloud providers

^{2/ &}quot;Connect to AWS lot" and "Connect to AWS Greengrass" packages are available for STM32L4/L4+Discovery kit IoT node 3/ MIT license



Application field – Audio

A complete solution for all audio aspects

• All audio aspects can be covered by solutions from ST or third parties

Optimized for ST products

• Unlike open-source non-optimized solutions, ST works with third parties to propose optimized algorithms for ST platforms

| Often-seen acronyms | | | | | | | | |
|---------------------|--|--|--|--|--|--|--|--|
| Codec | A codec is a program capable of encoding and decoding a digital data stream. The encoded stream can be compressed or not, with a lossy (MP3, WMA,) or lossless (FLAC, ALAC,) mechanism. | | | | | | | |
| PCM | Pulse-code modulation: Digital representation of an analog signal, in which the magnitude of the analogue signal is sampled regularly, each sample being quantized to the nearest value within a range of digital steps. | | | | | | | |
| AAC, MP3, WMA | Music codecs with patents. Royalties need to be paid to patent owners. | | | | | | | |
| Speex | Open source, no royalties speech codec | | | | | | | |
| G711 | Simple codec with no royalties often used in telephony | | | | | | | |
| G726 | ADPCM (adaptive differential pulse code modulation): Simple compression of PCM data | | | | | | | |





STM32 – Audio solutions 70

| | | | | | | | | A | vailabi | lity | у | | | | | |
|----------|---|----------|------|----|----------------|-----------------------|----------------|----------------|----------------|----------------|----------------|----|----------------|----------------|--|--|
| Provider | Solution name | Model | Cost | F0 | F1 | F105 F107 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ | | |
| ST | ADPCM Vocoder, Speex Vocoder | Source | Free | N | Υ | N¹ | N ¹ | N ¹ | N ¹ | N | N | N | N ¹ | N¹ | | |
| ST | G711, G726, G726A Vocoders | Source | Free | N | N¹ | N¹ | Υ3 | N ¹ | Υ3 | N | N | N | N ¹ | N¹ | | |
| ST | Audio Engine MP3 Decoder With Channel Mixer, Equalizer and Loud | Binaries | Free | N | N¹ | Y | Y | N¹ | Y | Y | Y | N | N¹ | N ¹ | | |
| ST | Audio Engine MP3 Codec With Channel Mixer, Equalizer and Loud | Binaries | Free | N | N¹ | Y | Y | N¹ | Y | N | N | N | N¹ | N ¹ | | |
| ST | Audio Engine WMA Decoder With Channel Mixer, Equalizer and Loud | Binaries | Free | N | N ¹ | Υ | Υ | N¹ | Υ | Y | N | N | N¹ | N ¹ | | |
| ST | Audio Engine AAC Decoder AAC-LC, HE-AAC+ v1, HE-AAC+ v2 | Binaries | Free | N | N ¹ | Y ³ | Y 3 | N ¹ | Y 3 | N¹ | N¹ | N | N ¹ | N ¹ | | |
| ST | Audio Engine AC3 Decoder | Binaries | Free | N | N | N | N | N | N² | N ¹ | N¹ | N | N | N | | |
| ST | Audio Engine Post Processing Smart volume control, Equalizer, Sample rate converters, Stereo widening | Binaries | Free | N | N | N | N | N ¹ | Y | N¹ | N ¹ | N | N | N | | |
| ST | Apple iAP Streaming Library (iPod/iPhone/iPad) ² | Source | Free | N | N | Y | Υ | Υ | Υ | N ¹ | N¹ | N | Υ | N | | |
| ST | USB audio class and stream synchro. (feedback pipe, external PLL,) | Binaries | Free | Y | N | Y | Υ | N¹ | Y | Y | N | Y | N | N | | |
| ST | Source Rate Converter ³ | Binaries | Free | N | Υ | Y | Y | N¹ | Y | Y | N | N | N¹ | N | | |



- 1/ The library will run immediately on these targets, even if not ported officially.
- 2/ Only available by request to local sales for companies being a licensee of Apple MFi (Made for iPod) program.
- 3/ Available on demand. Ask your local ST Sales office.



STM32 – Audio solutions 71

| | | | | | | | | A۱ | /ailabi | lity | | | | | |
|------------|--|----------|---------|----|----|--------------|----|----|---------|----------------|----|----|----|--------|--|
| Provider | Solution name | Model | Cost | F0 | F1 | F105 F107 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ | |
| ST | X-CUBE-AUDIO ³ | Source | Free | N | N | N | N | N | Υ | Y | N | N | N | N | |
| ST | X-CUBE-AVS ⁴ | Source | Free | N | N | N | N | N | N | Υ | N | N | N | N | |
| DSPConcept | Audio Weaver Algorithms | Binaries | License | N | N | N | N | N | Υ | Υ | Υ | N | N | N | |
| Craftwork | Remote Speakers (DLNA Media Renderer) | Binaries | License | N | N | N | N¹ | N | Y | N¹ | N¹ | N | N | N | |
| Sensory | Voice recognition ² | Binaries | License | N | N | N | N | N | Υ | N ¹ | N¹ | N | N | N | |
| Vestec | Voice recognition ² | Binaries | License | N | N | N | N | N | Υ | N ¹ | N¹ | N | N | N | |

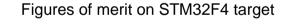


- 1/ The library will run immediately on these targets, even if not ported officially.
- 2/ Available on demand. Ask your local ST Sales office.
- 3/ Audio processing components (BAM, BIQ, CDC, GAM, GREQ, HPV, SRC236, SRC441, OMNI2, SVC, SDR, SMR)
- 4/ Framework for the Alexa Voice Service (AVS)



Focus – STM32 audio post-processing (1/2)

| Modules Names | Quality | Measured MHz | Flash Memory | Static RAM | Dynamic RAM | Remarks |
|-----------------------------------|----------------------------|---|--|------------|---------------------|--|
| SRC441 | Standard | 13.7 MHz | 3204 Bytes | 232 Bytes | 3228 Bytes | Sampling Rate Conversion |
| 31(0441 | High Quality | 20.4 MHz | 20.4 MHz 3894 Bytes 360 Bytes 3676 Bytes | | from 44.1 to 48 kHz | |
| SRC236 | Standard | 8->16: 4.4 MHz 48->96: 25.3 MHz 16->48: 12.5 MHz 8->48: 12.3 MHz 32->48: 12.7 MHz 24->16: 6.2 MHz | 1740 Bytes | 972 Bytes | 3364 Bytes | Sampling Rate Conversion supporting ratios |
| CINGZOO | High Quality | 8->16: 7.1 MHz 48->96: 40.7 MHz 16->48: 20.4 MHz 8->48: 20.2 MHz 32->48: 20.5 MHz 24->16: 10.2 MHz | 2238 Bytes | 1932 Bytes | 4804 Bytes | 2, 3, 6, 1/2, 1/3, 1/6, 3/2 and 2/3. |
| | Enhanced Mono to Stereo | 6.4 MHz | | | | |
| Omnisurround – Stereo Widening | SW 2.0 to 2.0 | 13.2 MHz | 3660 Bytes | 1992 Bytes | 384 Bytes | Signal is sampled at 48 kHz |
| | Closely Spaced Speakers | 4.0 MHz | | | | |
| Omnisurround – 7.1 Virtualizer | 5.1 or 7.1 -> 2.0 | 25.6 MHz | 4764 Bytes | 3028 Bytes | 1152 Bytes | Signal is sampled at 48 kHz |







Focus – STM32 audio post-processing (2/2)

| Modules Names | Quality | Measured MHz | Flash Memory | Static RAM | Dynamic RAM | Remarks |
|-------------------------------|--|--|-----------------|------------|----------------|---|
| Gfx Equalizer (GrEq) | 10-bands | 14 MHz | 4390 Bytes | 552 Bytes | 3840 Bytes | Graphical Equalizer. Signal is sampled at 48 kHz, 10 parallel bands with 6 available presets Can run in place (Input buf = Output buf) |
| Biquad Filters | 10 Biquads | 14.1 MHz | 570 Bytes | 600 Bytes | 4 Bytes | Generic Biquads filter (used for Transducer Equalizer for instance) Signal is sampled at 48 kHz Can run in place (Input buf = Output buf) |
| Bass Manager (BAM) | with Limiter without Limiter | 17.1 MHz 12.7 MHz | 9266 Bytes | 2236 Bytes | 5760 Bytes | Bass Manager. Up to +/- 24 dB, between 60 and 300Hz Can run in place (Input buf = Output buf) |
| Smart Volume Control (SVC) | Standard High Quality | 6.2 MHz 10.9 MHz | 6160 Bytes | 2648 Bytes | 4800 Bytes | Smart Volume Control (includes DRC) Signal is sampled at 48 kHz Can run in place (Input buf = Output buf) |
| Gain Manager (GAM) | Standard | Stereo: 2.6 MHz 8 channels: 8.6 MHz | 1472 Bytes | 120 Bytes | 1 Bytes | Gain Manager Signal is sampled at 48 kHz Can run in place (Input buf = Output buf) |
| Donning | L/R Panning F/R Balancing | 8.4 MHz | 5654 Bytes | 6872 Bytes | 7680 Bytes | Signal is sampled at 48 kHz 2.0 input, 4.0 output |
| Panning | Sweet Spot Mode 1 Sweet Spot Mode 2 (Ambi) | 4.2 MHz 4.7 MHz | 7956 Bytes | 6968 Bytes | 7680 Bytes | Contains Front and Rear Left/Right Panning, Front/Rear balancing. Sweet Spot Mode for one user. |

Figures of merit on STM32F4 target





Application Field - Building Automation and Control

Building automation is the automatic centralized control of a building's heating, ventilation and air conditioning, lighting and other systems through a building management system or building automation system (BAS). A building controlled by a BAS is often referred to as a smart building or a smart home.

| | Often-seen acronyms |
|--------------|--|
| BACnet | communications protocol for Building Automation and Control (BAC) networks that leverage the ASHRAE, ANSI, and ISO 16484-5 standard protocol. BACnet was designed to allow communication of building automation and control systems for applications such as heating, ventilating, and air-conditioning control (HVAC), lighting control, access control, and fire detection systems and their associated equipment. The BACnet protocol provides mechanisms for computerized building automation devices to exchange information, regardless of the particular building service they perform. |
| DALI | Digital Addressable Lighting Interface (DALI) is a trademark for network-based systems that control lighting in building automation. The underlying technology was established by a consortium of lighting equipment manufacturers as a successor for 0-10 V lighting control systems, and as an open standard alternative to Digital Signal Interface (DSI), on which it is based. DALI is specified by technical standards IEC 62386 and IEC 60929. Standards conformance ensures that equipment from different manufacturers will interoperate. The DALI trademark is allowed on devices that comply with the current standards when manufactured. |
| DLMS / COSEM | IEC 62056 is a set of standards for Electricity metering data exchange by International Electrotechnical Commission. The IEC 62056 standards are the International Standard versions of the DLMS/COSEM specification. DLMS or Device Language Message Specification (originally Distribution Line Message Specification[1]), is the suite of standards developed and maintained by the DLMS User Association and has been adopted by the IEC TC13 WG14 into the IEC 62056 series of standards. COSEM or Companion Specification for Energy Metering, includes a set of specifications that defines the Transport and Application Layers of the DLMS protocol |
| DMX-512 | DMX512 (Digital Multiplex) is a standard for digital communication networks that are commonly used to control stage lighting and effects. It was originally intended as a standardized method for controlling light dimmers, which, prior to DMX512, had employed various incompatible proprietary protocols. It soon became the primary method for linking controllers (such as a lighting console) to dimmers and special effects devices such as fog machines and intelligent lights |
| KNX | KNX is a standardised (EN 50090, ISO/IEC 14543), OSI-based network communications protocol for building automation. KNX is the successor to, and convergence of, three previous standards: the European Home Systems Protocol (EHS), BatiBUS, and the European Installation Bus (EIB or Instabus) |





STM32 – Building Automation and Control 75

| Provider | Solution name | Model | Cost | | | | | Ava | ailabili | ity | | | |
|------------------------|-----------------------|-------------------|---------------------|----------------|----|----------------|----------------|----------------|----------------|----------------|----------------|------------|----------------|
| Provider | | Wodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| MBS | BACnet stack embedded | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| prizm.io | BACnet Stack | Source | License | Y | Υ | Υ | Υ | Υ | Y | Υ | Y | Υ | Υ |
| ST | DALI ² | Source | Free | N | Υ | N | N | N | N | N | N | Υ | N |
| MBS | DALI stack | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| Andrea Informatique | DLMS / COSEM | Binaries | License | N ³ | Υ | Y | N ³ | Υ | N ³ | N ³ | N ³ | Y | N ³ |
| ST | DMX-512 ² | Source | Free | N ¹ | Υ | N ¹ | N¹ | N ¹ |
| prizm.io | DMX-512 Stack | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| TAPKO | KAlstack KNX | Binaries / source | License + royalties | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y 3 | Y 3 | Υ3 |
| Weinzierl | <u>KNX</u> | Source | License | Υ | Υ | N ¹ | N ¹ | N ¹ | N | N | N ¹ | Υ | N ¹ |



^{1/} Can be ported

^{2/} Provided on demand to sales office, supporting DALI slave only.

^{3/} Please contact supplier



STM8 – Building Automation and Control 76

| Provider | Solution name | Application | Model | Cost | Availa | bility |
|----------|---------------|---------------------|----------------------|---------|------------------|------------------|
| Flovidei | Solution hame | Application | Wodel | COSI | STM8S/ STM8AF | STM8L/ STM8AL |
| ST | <u>DALI</u> | Lighting | Source | Free | Υ | N¹ |
| TAPKO | KAlstack KNX | Building automation | Binaries / source | License | N | Υ |

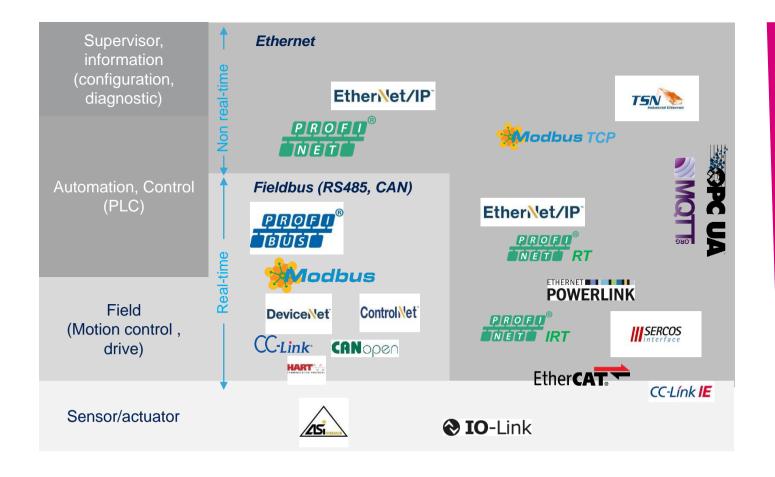






Application field – Industrial protocols

Industrial market needs are very fragmented in terms of communication protocols. Many different protocols are available for different target applications in factory automation



TCP / UDP

Prioritization

schedulina

Lower latency





STM32 – Industrial protocols (1/8) 78

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabil | ity | | | |
|-------------|-----------------------|---------------------|--------|---------|----|----|----|----|-----|--------|----------------|----|----|--------|
| Provider | Solution hame | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| CMX | CMX-CANopen | Automation, medical | Source | License | N | Υ | Υ | Υ | Υ | Υ | N¹ | N | N | N¹ |
| | <u>eCosPro-CAN</u> | Factory Automation | Source | License | N | Υ | Υ | Υ | Υ | Υ | N ¹ | N | N | Υ |
| eCosCentric | <u>CANopen</u> | Factory Automation | Source | License | N | Υ | Υ | Υ | Υ | Υ | N¹ | N | N | Υ |
| | eCosPro-ModbusTCP | Factory Automation | Source | License | N | N | Υ | Υ | Υ | Υ | N ¹ | N | N | Υ |
| | <u>CANopen</u> | Automation, medical | Source | License | Υ | Υ | Υ | N | Υ | Υ | Υ | N | N | Υ |
| omtoo | <u>J1939</u> | Commercial vehicles | Source | License | Υ | Υ | Υ | N | Υ | Υ | Υ | N | N | Υ |
| emtas | <u>EnergyBus</u> | Commercial vehicles | Source | License | Υ | Υ | Υ | N | Υ | Υ | Υ | N | N | Υ |
| | EtherCAT ² | Automation, medical | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |

^{2/} Requires external hardware.



STM32 – Industrial protocols (2/8)

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabil | ity | | | |
|----------|--|-------------|----------|---------|----|----|----|----|-----|-----------|-----|----|----|--------|
| Provider | | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| | CANOpen | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | PROFINET Automation Binaries License Y Y Y Y Y | Υ | Υ | Υ | Υ | | | | | | | | | |
| | Modbus | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| EUROS | EtherCAT ¹ | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | Ethernet | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | OPC UA Server/Client | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | MQTT Client | Automation | Binaries | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |



STM32 – Industrial protocols (3/8)

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabili | ty | | | |
|----------|--|--------------------|------------|----------|----|----|----|----|-----|---------|----|----|----|--------|
| Provider | Solution name | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| | I-CUBE-NETX + PROFIBUS DP Slave (1) | | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| | I-CUBE-NETX + CANopen Slave (1) | Fieldbus _ | Source + | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Hilscher | I-CUBE-NETX + DeviceNet Slave (1) | Factory automation | Binary (2) | 2.001.00 | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| | CC-Link Slave (1) | | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| | OPC UA Server (1) | lloT | Source + | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| | MQTT Client (1) | 1101 | Binary (2) | Licelise | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |





STM32 – Industrial protocols (4/8)

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabili | ty | | | |
|----------|---|-----------------------------------|-----------------------------------|---------|----|----|----|----|-----|---------|----|----|----|--------|
| Provider | Solution name | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| | I-CUBE-NETX + PROFINET RT/IRT IO-Device (1) | | | | Υ | Υ | Y | Y | Υ | Υ | Y | Y | Υ | Y |
| | I-CUBE-NETX + EtherCAT Slave (1) | | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| | I-CUBE-NETX + Ethernet/IP Adapter (1) | Industrial real- time Ethernet | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y |
| Hilscher | I-CUBE-NETX + sercos Slave (1) | Factory | Source + Binary ⁽²⁾ | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | I-CUBE-NETX + POWERLINK Controlled Node (1) | automation | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y | Y | Y |
| | I-CUBE-NETX + Varan Client (1) | | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y | Υ | Υ |
| | I-CUBE-NETX + Open ModbusTCP (1) | | | | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Y | Υ | Y |



STM32 – Industrial protocols (5/8)

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabili | ty | | | |
|----------|--|------------------------|--------|---------|----|------------|----|----|-----|---------|----|----|----|----------------|
| Provider | Solution name | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| | IXXAT® CANopen | Automation, medical | Source | License | N | Υ | Υ | Y | Υ | Υ | Y | N | N | Υ1 |
| | IXXAT® SAE J1939 | Transportation | Source | License | N | Υ | Υ | Υ | Υ | Υ | Υ | N | N | Y1 |
| | EtherNet/IP Adapter | Factory automation | Source | License | N | Υ1 | Υ | N | Υ | Υ | Υ | N | N | Y1 |
| HMS | EtherNet/IP Scanner | Factory automation | Source | License | N | Y1 | Υ | N | Υ | Υ | Υ | N | N | Y1 |
| | IXXAT® IEEE1588 | Factory automation | Source | License | N | Υ1 | Υ | N | Υ | Υ | Υ | N | N | Y1 |
| | IXXAT® Safety over EtherCAT | Automation | Source | License | N | Y1 | Υ | N | Υ | Υ | Υ | N | N | Υ1 |
| | IXXAT® CIP Safety (EtherNet/IP, sercos) | Automation | Source | License | N | Y 1 | Υ | N | Υ | Υ | Υ | N | N | Y ¹ |





STM32 – Industrial protocols (6/8)

| Provider | Solution name | Application | Model | Cost | | | | | Avai | labilit | у | | | |
|--------------|-------------------|---------------------|-------------------|---------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|
| Provider | Solution name | Application | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4/L4+ |
| Matrikon | OPC UA Server | Factory automation | Source / Binaries | License | N | N | Υ | N | Υ | Υ | Υ | N | N | N |
| MESCO | <u>HART Slave</u> | Process automation | Source | Lic. + Royal. | Υ | Υ | Υ | Υ | Υ | N ² | N^2 | Υ | Υ | N ² |
| | <u>CANopen</u> | Automation, medical | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Y | N | N | Y |
| MigraControl | <u>J1939</u> | Commercial vehicles | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N | N | Y |
| MicroControl | EtherCAT1 | Factory automation | Source | License | Y ¹ |
| | <u>PROFINET</u> | Factory automation | Source | License | N | Υ | Y | N | Y | Y | Y | N | N | N |
| Micrium | μC/Modbus | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Y | Y | Y | Y | Y |

^{2/} Please contact supplier.



STM32 – Industrial protocols (7/8) 84

| Provider | Solution name | Application | Model | Cost | | | | | Ava | ilabili | ty | | | |
|----------|--------------------------|--------------------|--------|---------|----|----|----|----|-----|---------|----|----|----|--------|
| Provider | Solution hame | Application | Wodei | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| | <u>CANopen</u> | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | N | N | Υ |
| | EtherCAT ² | Factory automation | Source | License | Y1 | Υ | Υ | Υ | Υ | Υ1 | Y1 | N | N | Υ1 |
| Port | <u>PROFINET</u> | Factory automation | Source | License | N | N | N | N | Υ | Υ | Υ | N | N | N |
| | EtherNet/IP ² | Factory automation | Source | License | N | N | N | N | Υ | Υ | Y | N | N | N |
| | <u>POWERLINK</u> | Factory automation | Source | License | N | N | Y1 | N | N | N | N | N | N | N |
| SEGGER | <u>emModbus</u> | Factory automation | Source | License | Υ | Υ | Υ | Υ | Y | Υ | Υ | Y | Υ | Υ |

^{2/} Please contact supplier.



STM32 – Industrial protocols (8/8)

| Provider | Solution | Application | Model | Cost | | | | | Ava | ilabilit | y | | | |
|----------------|-----------------|--------------------|--------|---------------------|----|----|----|----|-----|----------|----|----|----|--------|
| Provider | name | Application | Wodel | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| TECanant | IO-Link Device | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| TEConcept | IO-Link Master | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ | Υ |
| | IO-Link Device | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Y | Υ | Υ | Υ | Υ |
| TMGTE TMGTE | IO-Link Master | Factory automation | Source | License | N | Υ | Υ | Υ | Υ | Υ | Υ | N | N | N |
| TMGTE | Profibus DP | Factory automation | Source | License | Υ | Υ | Υ | Υ | Υ | Y | Υ | Y | Υ | Y |
| TMGTE TMGTE | <u>Profinet</u> | Factory automation | Source | License + royalties | N | N | Υ | N | Υ | Υ | Υ | N | N | N |
| | Ethernet/IP | Factory automation | Source | License + royalties | N | N | Υ | N | Υ | Υ | Υ | N | N | N |
| Rowebots | UNISON Modbus | Factory automation | Source | License | N | Υ | Υ | Υ | Υ | Y | Υ | N | Υ | Y |





STM8 – Industrial protocols 86

| Dunyidan | Calutian nama | Application | Model | Cost | Availa | ability |
|-----------|-----------------|--------------------|-------------------|---------|------------------|------------------|
| Provider | Solution name | Application | Application Model | | STM8S/ STM8AF | STM8L/ STM8AL |
| SEGGER | <u>emModbus</u> | Factory automation | Source | License | Υ | Y |
| TEConcept | IO-Link Device | Factory automation | Source | License | Υ | Υ |
| TMGTE | IO-Link Device | Factory automation | Source | License | Y | Y |







Application field – Motor control 87

Control your 3-phase motor with top performance

• STM32 microcontrollers offer the performance of the industry-standard Arm® Cortex®-M cores running either Vector control or FOC modes, widely used in high-performance drives for air conditioning, home appliances, drones, building and industrial automation, medical and e-bike applications. STM32 MC SDK (motor control software development kit) firmware (X-CUBE-MCSDK and X-CUBE-MCSDK-FUL) includes the Permanent-magnet synchronous motor (PMSM) firmware library (FOC control) and the STM32 Motor Control Workbench (to configure the FOC firmware library parameters), with its graphical user interface (GUI). STM32 Motor Control Workbench is PC software that reduces the design effort and time needed for the STM32 PMSM FOC firmware configuration. The user generates a project file through the GUI, and initializes the library according to the application needs. Some of the variables of the algorithm being used can be monitored and changed in real time.

| | Often-seen acronyms |
|-----------|---|
| BLDC | Brushless DC: permanent magnet motor with trapezoidal shaped B-EMF, FOC applicable |
| PMSM | Permanent magnet synchronous motor: with sinusoidal shaped B-EMF, FOC applicable |
| ACIM | AC induction motor: type of motor, FOC applicable |
| FOC | Field-oriented control: Mathematical technique used to achieve decoupled control of the flux and torque in a 3-phase motor. |
| HFI, MTPA | High Frequency Injection: algorithm for very low speed and fast acceleration (air con, fridge) and increased efficiency for low-or zero-speed, full torque applications (washing machines, factory automation) • "Maximum Torque Per Ampere" (MTPA): optimizes the motor torque for each load and increases the efficiency, • "Feed Forward": improves the current control at high speed. |





STM32 - Motor control - PMSM 88

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | | |
|----------|--|--|------|--------------|------|------|----|----|------|------|----|----|---------|--|
| Flovidei | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/ L4+ | |
| ST | STM32 Motor Control Software Development Kit (MCSDK) v5.0.0 Software development kit including: • Motor control library (sensors, algorithms), Single or Dual control • Motor control application (implementation of library, high-level MC commands) • HFI (next release v5.1, 18Q2) • PFC (next release v5.1, 18Q2) • ST Motor Control Workbench software tool • Demo projects and utilities | X-CUBE-MCSDK Source code except CORDIC, MTPA, X-CUBE-MCSDK-FUL Full source code (Registration/approbation required) | Free | Y | 18Q2 | 18Q4 | Y | Y | 18Q3 | 18Q4 | N | N | 18Q3 | |





STM32 - Motor control - BLDC 89

| Provider | Solution name | Model | Cost | Availability | | | | | | | | | |
|----------|--|---------|------|--------------|----|----|----|----|-----------|----|----|----|----------------|
| FIOVICE | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| ST | Bipolar stepper motors driving | Sources | Free | N¹ | Υ | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | N¹ | N ¹ |
| ST | STSW-IHM040V1 STM32 6-step FW example (STM32F100) STSW-IHM043V1 STM32 6-step FW example (STM32F0) | Source | Free | Y | Y | N | N | N | N | N | N | N | N |
| ST | X-CUBE-SPN7 Three-phase brushless DC motor driver | Source | Free | Υ | Υ | N | Y | Y | N | N | N | N | N |





STM32 – Motor control - Stepper 90

| Provider | Solution name | Model Cost | Availability | | | | | | | | | | |
|-----------|--|------------|--------------|----|----|----|----|----|-----------|----|----|----|---------|
| FIOVILLEI | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4 /L4+ |
| ST | STSW-STM32018 Driving bipolar stepper motors using STM32F103 microcontroller | Source | Free | N | Υ | N | N | N | N | N | N | N | N |
| ST | X-CUBE-SPN1 Stepper bipolar motor driver software expansion forSTM32Cube | Source | Free | Υ | N | N | Υ | Υ | N | N | Υ | N | N |
| ST | X-CUBE-SPN2 Two axes stepper motor driver software expansion for STM32Cube | Source | Free | Υ | N | N | Υ | Υ | N | N | N | N | N |
| ST | X-CUBE-SPN3 High-power stepper motor driver software expansion for STM32Cube | Source | Free | Υ | N | N | Υ | Υ | N | N | Y | N | N |
| ST | X-CUBE-SPN4 Dual-brush DC motor driver software expansion for STM32Cube | Source | Free | N | N | N | N | Υ | N | N | Υ | N | N |
| ST | X-CUBE-SPN5 Stepper bipolar motor driver software expansion for STM32Cube | Source | Free | N | N | N | Υ | Y | N | N | Υ | N | N |





STM8 – Motor control 91

| | | | | Availability | | |
|----------|--|----------|---------------------------------------|--------------|---|--|
| Provider | Solution name | Model | el Cost STM8S / STM8L STM8AF STM8A | | | |
| ST | STSW-STM8020 STM8S and STM8A BLDC and ACIM motor control firmware library Scalar control of induction motor control Scalar control (6-step) of permanent magnet brushless motors (BLDC and PMSM) | Source | Free | Υ | N | |
| ST | STSW-STM8042 STM8S motor control firmware library builder GUI | Binaries | Free | Υ | N | |







Application field – Automotive – 92

More than hardware

• In addition to microcontrollers dedicated to automotive equipment, ST proposes a set of firmware solutions

| | Often-seen acronyms |
|-------|--|
| J1939 | Vehicle standard used for communication and diagnostics with vehicle components (e.g. agricultural machines). |
| J2602 | USA variant of LIN |
| LIN | Local interconnect network: The LIN bus is a small and slow network system that is used as a cheap sub-network of a CAN bus to integrate intelligent sensor devices or actuators in today's cars. The LIN specification is enforced by the LIN-consortium, with the first exploited version being 1.1, released in 1999. Since then, the specification has evolved to version 2.1 and 2.2 to meet current networking needs. Bit rates vary within the range of 1 to 20 Kbit/s. |
| CAN | Controller-area network (CAN or CAN-bus): This is a standard vehicle bus designed to allow microcontrollers and devices to communicate with each other within a vehicle without a host computer. Possible bit rates from 125 Kbit/s up to 1 Mbit/s. |
| ASIL | ASIL is a risk classification scheme defined by the ISO 26262 - Functional Safety for Road Vehicles standard. This is an adaptation of the Safety Integrity Level used in IEC 61508 for the automotive industry. |





STM8 – Automotive solutions

| Duovidos | Colution nome | Model | Cont | Avail | ability |
|----------------|-----------------------|--------|---------|----------------|-------------------|
| Provider | Solution name | Cost | STM8AF | STM8AL | |
| | J2602 Driver | Source | Free | Y | N ¹ |
| ST | LIN 2.1 Driver | Source | Free | Y | Y |
| | STM8A-SafeASIL | Free | Υ | N ¹ | |
| | ssCAN | Source | License | Y | N.A. ³ |
| | <u>ssJ1939</u> | Source | License | Y | N.A. ³ |
| Simma Software | <u>ssl15765</u> | Source | License | Y | N.A. ³ |
| | <u>ssl14229 (UDS)</u> | Source | License | Y | N.A. ³ |
| | ssNMEA2000 | Source | License | Y | N.A. ³ |
| | CANbedded | Source | License | Y | N.A. ³ |
| Vector | CANbedded LIN | Source | License | Y | N^2 |
| | CANbedded J1939 | Source | License | Y | N.A. ³ |

^{2/} Please contact supplier

^{3/} No CAN in STM8AL series



STM32 – CAN / LIN solutions

• Warning: STM32 devices are not qualified for automotive, and thus must not be used in automotive application.

| Provider | Solution name | Cost | Availability | | | | | | | | | | |
|----------------|-----------------|--------|--------------|----------------|----|----------------|----------------|----------------|------------|----------------|-------------------|-------------------|----------------|
| Provider | Solution name | Model | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | LO | L1 | L4/L4+ |
| IHR Gmbh | LIN 2.1 Driver | Source | License | N ¹ | Υ | N ¹ | N ¹ | Y ² | Y 3 | N ¹ | N ¹ | N ¹ | N ¹ |
| | <u>ssCAN</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | N.A. ⁴ | N.A.4 | Υ |
| | <u>ssJ1939</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | N.A. ⁴ | N.A. ⁴ | Υ |
| Simma Software | <u>ssl15765</u> | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | N.A. ⁴ | N.A. ⁴ | Υ |
| | ssl14229 (UDS) | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | N.A. ⁴ | N.A. ⁴ | Υ |
| | ssNMEA2000 | Source | License | Υ | Υ | Υ | Υ | Υ | Υ | N¹ | N.A. ⁴ | N.A. ⁴ | Υ |



^{2/} slave only

^{3/} multi-master (4 channels)

^{4/} No CAN in STM32L0 and STM32L1 series



Sensor Hub 95

Sensor hub solution for Android & Windows 8 mobile devices

• Sensor hub framework including 9-axis sensor fusion, gesture recognition, context awareness and pedestrian dead reckoning on STM32

Power-efficient implementations

• Optimized algorithms with smart scheduling allowing power-efficient sensor data acquisition, data batching and motion processing on STM32

| | Definitions |
|----------------------|---|
| Motion sensors | Components able to sense various motions including accelerations (accelerometers = A), rotations (gyroscopes = G) and Earth magnetic field (magnetometers = M). |
| Sensor hub | Microcontroller collecting and processing the motion sensors raw data (data fusion) for an upstream device (usually a mobile application processor). |
| 9-axis sensor fusion | Processing that "fuse" the raw data collected from the AGM sensors in order to compute the device orientation. Fusing each other sensor data allow to increase the orientation accuracy and to correct each sensor inherent noise, drift or sensitivity to external environment. Sensor data calibration is included as well in the fusion process. |
| Gestures recognition | Processing that allows the sensor hub to sense various gestures like shaking, taping, flipping, twisting, |
| Context awareness | Processing that allows the sensor hub to detect the system user's activity like device facing up, device facing down, user standing, user walking, user running, |
| PDR | Pedestrian dead reckoning allowing the sensor hub to determine the system user's trajectory based one the sensors data only (no GPS, no Wi-Fi,). The trajectory will be represented in 2D or 3D (if pressure sensor is used) environment. |





STM32 – Sensor Hub solutions

Solutions with Partners. Ask your local ST Sales office.





Virtual Machines & Model-driven development

High-level languages are available in STM32 ecosystem, as well as modeldriven development tools

Easier migration

• ST and 3rd parties help customers migrate to these new environments



| Environment | Meaning Meaning |
|-----------------|---|
| Javascript | High Level Interpreted Language, typically used in HTML, internet and web related design |
| Python | High Level Language, interpreted or using a VM (Virtual Machine), popular within scientific community |
| .NET | C# object-oriented language and Microsoft Visual Studio development environment. This is Microsoft .NET Micro Framework for microcontrollers. |
| MATLAB/Simulink | Brands from MathWorks company, for software enabling model-driven approach |





STM32 – Virtual Machines and Model-driven development

| Provider | Description | Model | Cost | | | | | Ava | ilabili | ty | | | |
|-------------|---|---------------------------|------|----|----|----|----|-----|-----------|----|----|----|---------|
| Provider | Description | Wodel | Cost | F0 | F1 | F2 | F3 | F4 | F7 | H7 | L0 | L1 | L4 /L4+ |
| ST | STM32 Peripheral blocks for Matlab/Simulink | License | Free | Y | Y | Y | Υ | Y | Y | Y | Y | Y | Y |
| Mountaineer | Microsoft .NET Micro Framework | Open source Apache 2.0 | Free | N | Υ | Υ | N | Y | N | N | N | N | N |
| Espruino | <u>Javascript</u> | Open source MPLv2 | Free | N | Υ | N | N | Y | N | N | N | N | Y |
| JerryScript | <u>Javascript</u> | Open source Apache 2.0 | Free | N | N | N | N | Υ | N | N | N | N | N |
| Duktape | <u>Javascript</u> | Open source MIT | Free | N | N | N | N | Υ | N | N | N | N | Y |
| Micropython | <u>Python</u> | Open source MIT | Free | N | N | N | N | Υ | Y | N | N | N | Y |





Releasing your creativity 16





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