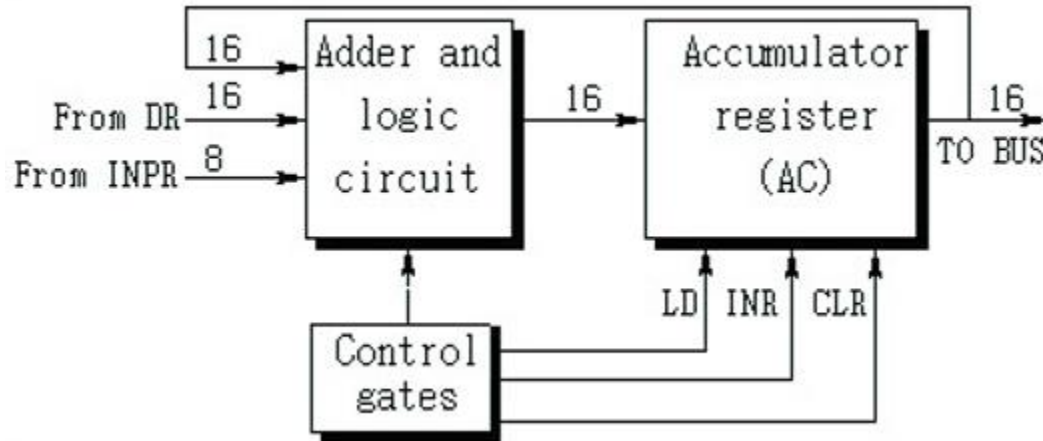


AC(Accumulator)제 어 보충

## 5.10 누산기(AC) 논리의 설계



마이크로 연산에서  
목적지 레지스터가 AC 인  
레지스터 전송문 : 이 문장들의  
구현은 곧 AC의 LD 제어입력을  
액티브 시키는 것이다.

$$D_0T_5: AC \leftarrow AC \wedge DR$$

$$D_1T_5: AC \leftarrow AC + DR$$

$$D_2T_5: AC \leftarrow DR$$

$$pB_{11}: AC(0-7) \leftarrow INPR$$

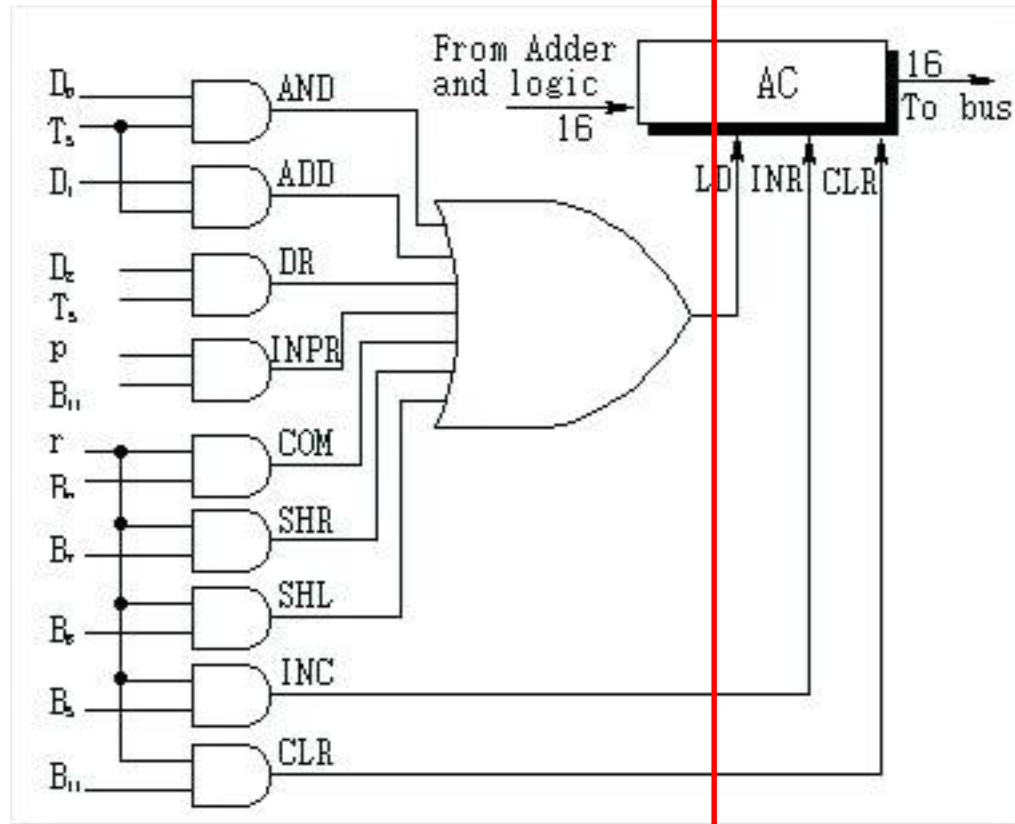
$$rB_9: AC \leftarrow \overline{AC}$$

$$rB_7: AC \leftarrow shrAC, AC(15) \leftarrow E$$

$$rB_6: AC \leftarrow shlAC, AC(0) \leftarrow E$$

$$rB_{11}: AC \leftarrow 0$$

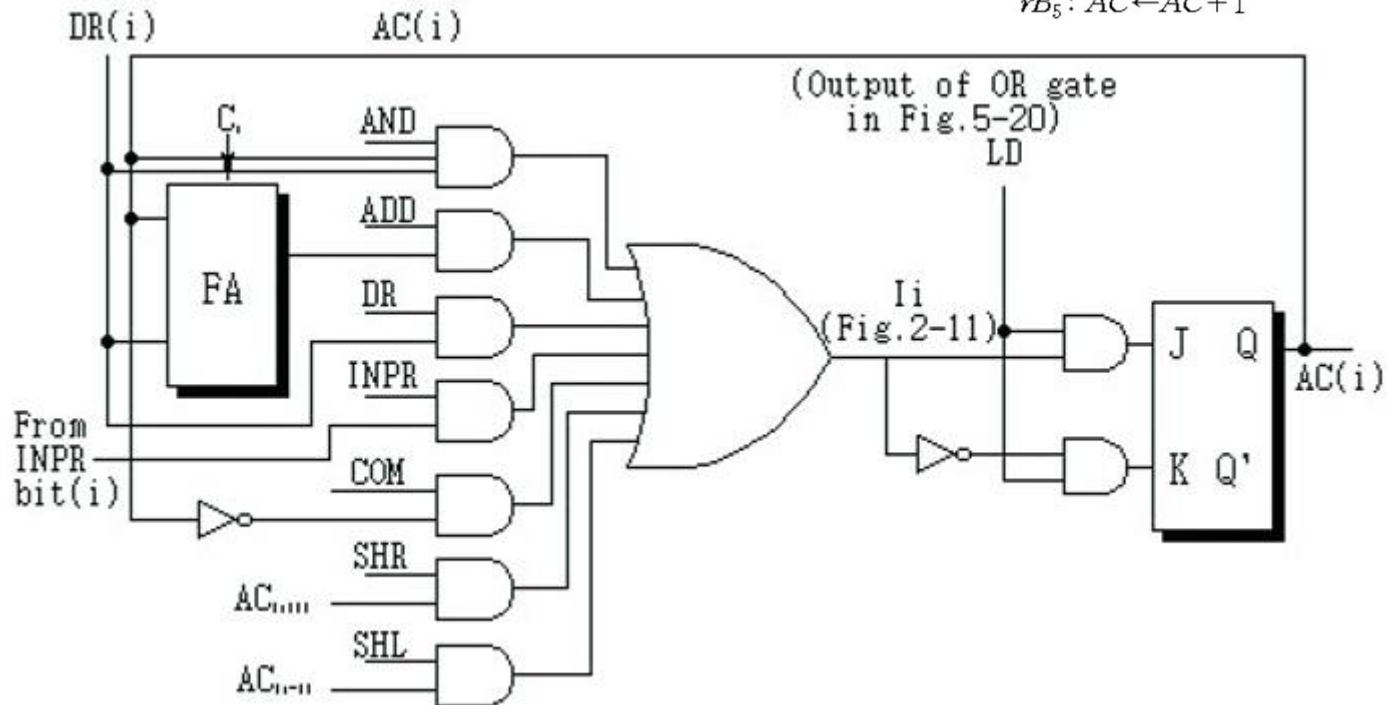
$$rB_5: AC \leftarrow AC + 1$$



앞 그림의 control gate

$D_0T_5: AC \leftarrow AC \wedge DR$   
 $D_1T_5: AC \leftarrow AC + DR$   
 $D_2T_5: AC \leftarrow DR$   
 $pB_{11}: AC(0-7) \leftarrow INPR$   
 $rB_9: AC \leftarrow \overline{AC}$   
 $rB_7: AC \leftarrow shrAC, AC(15) \leftarrow E$   
 $rB_6: AC \leftarrow shlAC, AC(0) \leftarrow E$   
 $rB_{11}: AC \leftarrow 0$   
 $rB_5: AC \leftarrow AC + 1$

# 가산 논리 회로



$D_0T_5: AC \leftarrow AC \wedge DR$   
 $D_1T_5: AC \leftarrow AC + DR$   
 $D_2T_5: AC \leftarrow DR$   
 $pB_{11}: AC(0-7) \leftarrow INPR$   
 $rB_9: AC \leftarrow \overline{AC}$   
 $rB_7: AC \leftarrow shrAC, AC(15) \leftarrow E$   
 $rB_6: AC \leftarrow shlAC, AC(0) \leftarrow E$   
 $rB_{11}: AC \leftarrow 0$   
 $rB_5: AC \leftarrow AC + 1$

Adder and Logic 회로의 1단을 표시