

Register	Value
----------	-------

R0	0x00003500
R1	0x00000000
R2	0x00000000
R3	0x00000000
R4	0x00000000
R5	0x00000000
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x00000000
R11	0x00000000
R12	0x00000000
R13 (SP)	0x20000714
R14 (LR)	0x00000000
R15 (PC)	0x00000000
xPSR	0x27000000

Logic Analyzer

Setup... Load... Save...

Min Time 0 s

Max Time 6.740943 s

Grid 10 ns

Zoom In Out All

Update Screen Stop Clear

Transition Prev Next

Jump to Code Trace

Signal Info Show Cycles

Amplitude Cursor

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

5.98854 s

Performance Analyzer

Logic Analyzer

SystemClock

startup.s

TableTrafficLight.c

55 void SysTick_Wait(uint32_t wait)

56 volatile uint32_t elapsedTime;

57

58

59

60

61

62

63

64

65

66

67

68

69

70

71

72

73

74

75

76

77

78

79

80

81

82

83

84

85

86

Port F Hardware

SW1

SW2

PF0

PF1

PF2

PF3

PF4

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

LED

Traffic Light Hardware

TM4C123

PE1

PE2

PE3

PE4

PE5

PE6

PE7

PE8

PE9

PE10

PE11

PE12

PE13

PE14

PE15

PE16

PE17

PE18

PE19

PE20

PE21

PE22

PE23

PE24

PE25

PE26

PE27

PE28

PE29

PE30

80 MHz

Red

Yellow

Green

East/west

North/south

Clock enabled

Grading Controls

Number from edX

Grade

Score: 0

Copy this to edX

Simulation

tl: 6.74095468 sec

L62 C2

CAP NUM SCRL OVR

10:55 AM

3/10/2016

Search the web and Windows

MISSION BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE DEFINE DIR