

Figure 1: Main Circuit

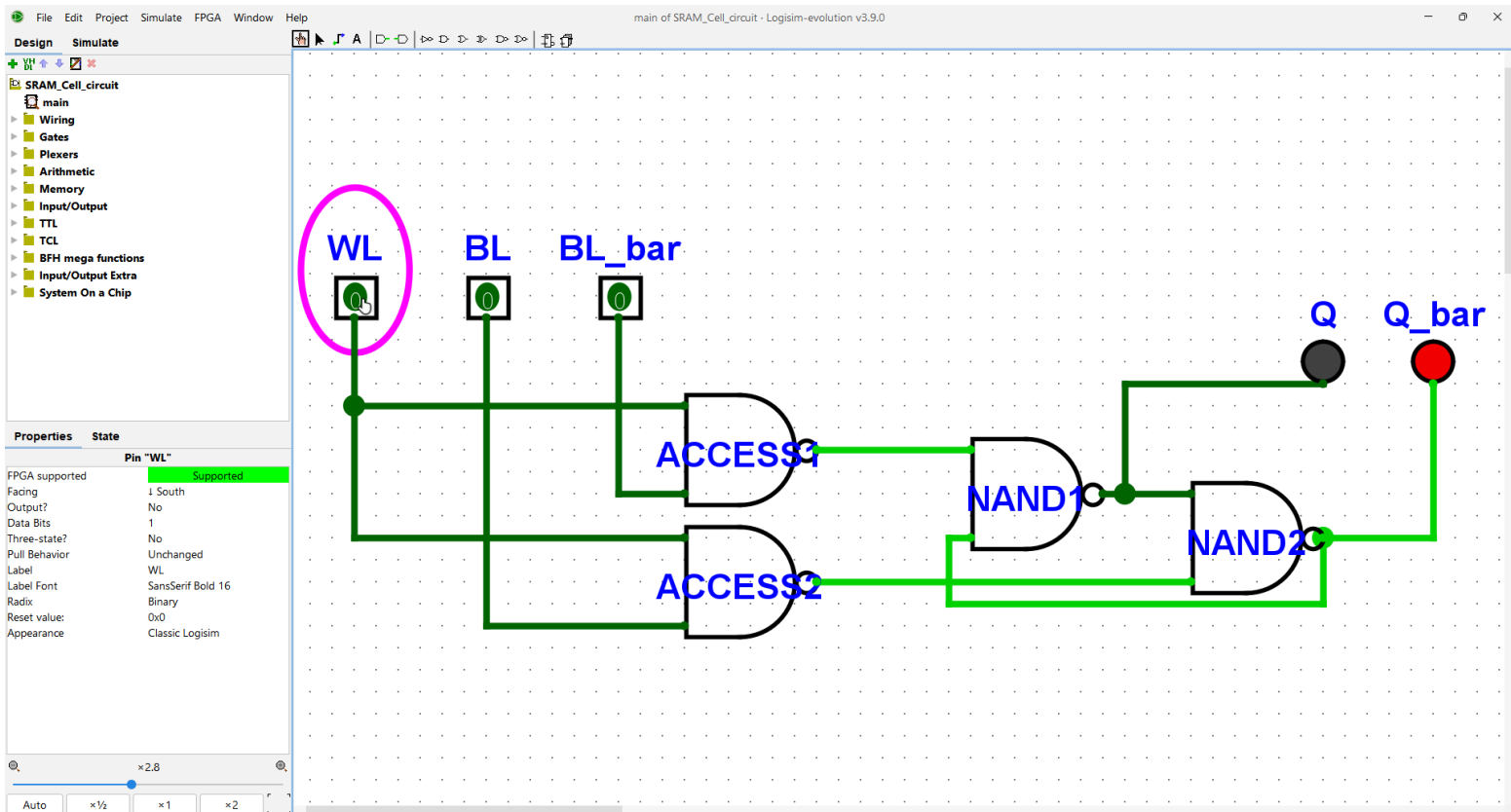


Figure 2: Read Operation (Shows Q_bar because it was performed after taking the outputs which are ahead)

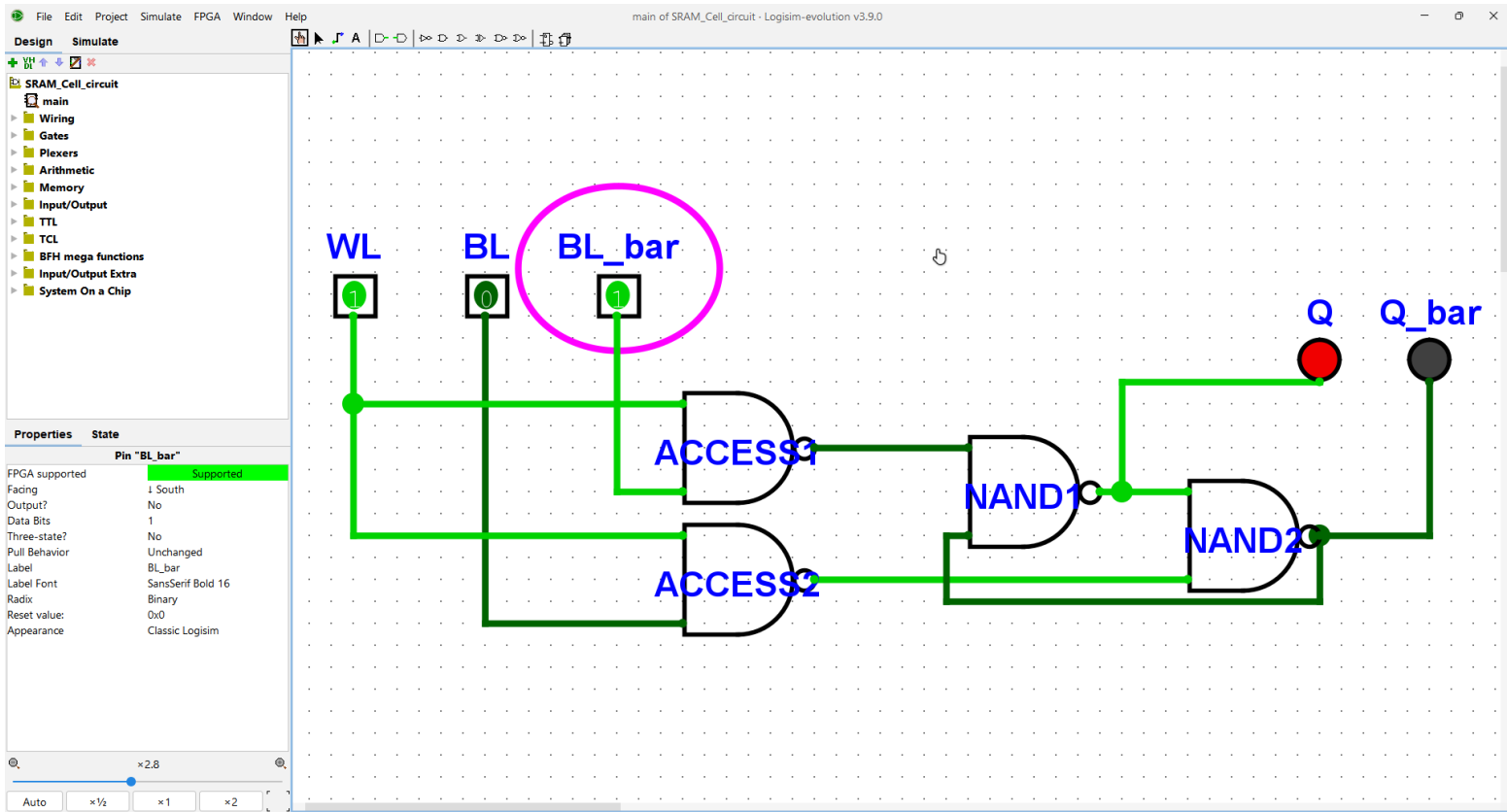


Figure 3: Write 1 Operation

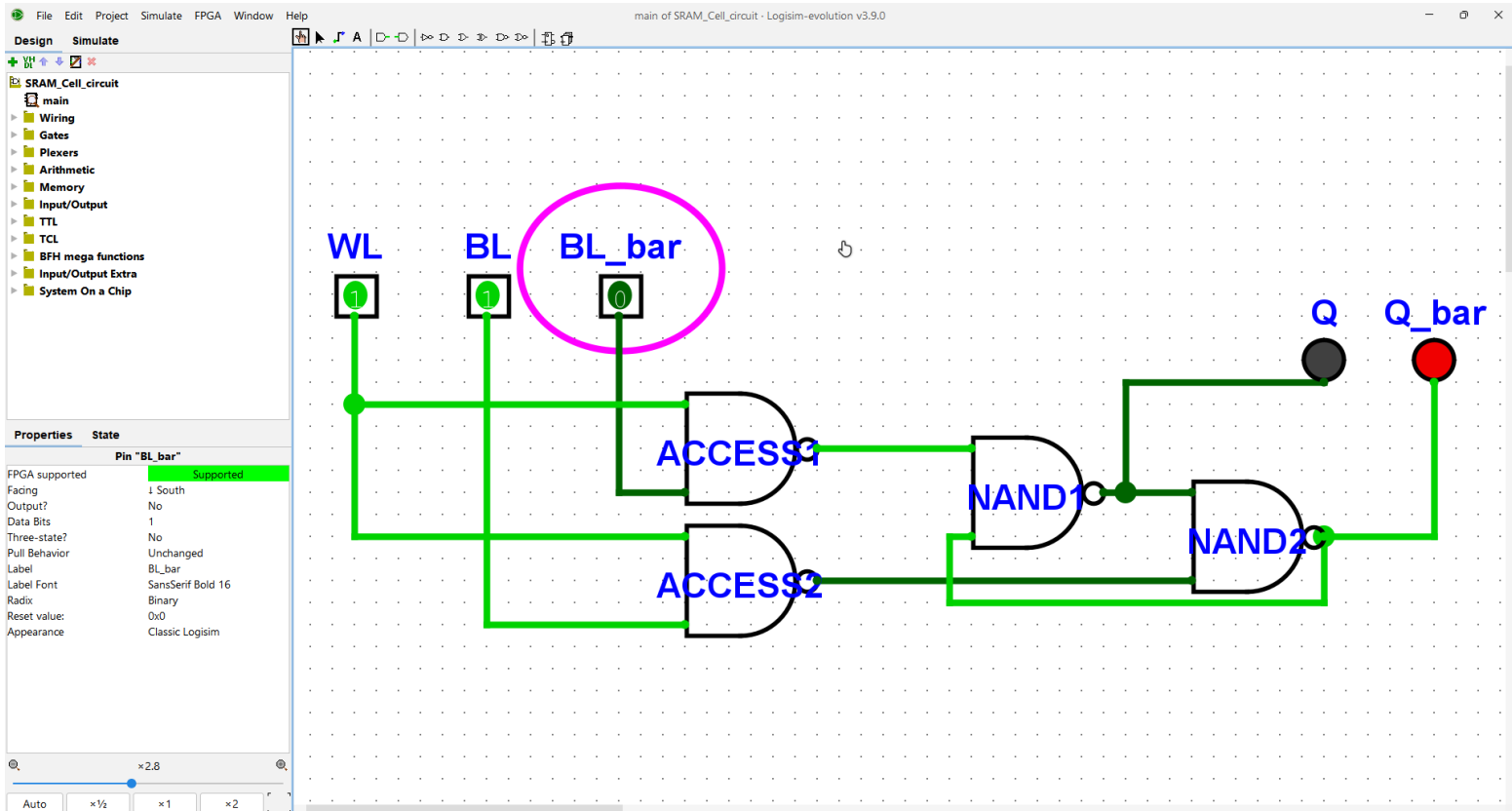


Figure 4: Write 0 Operation

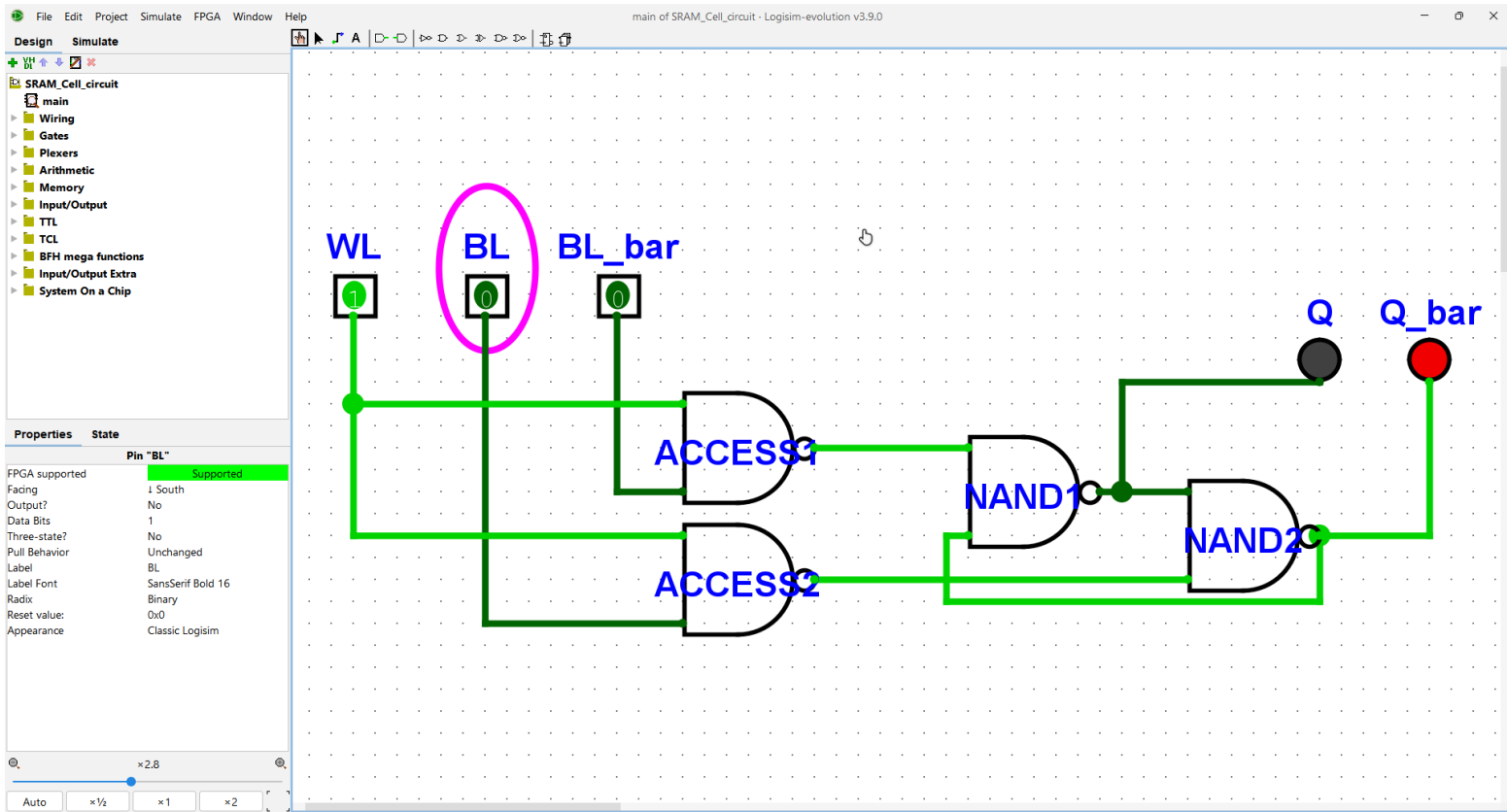


Figure 5: Hold(no effect) Operation

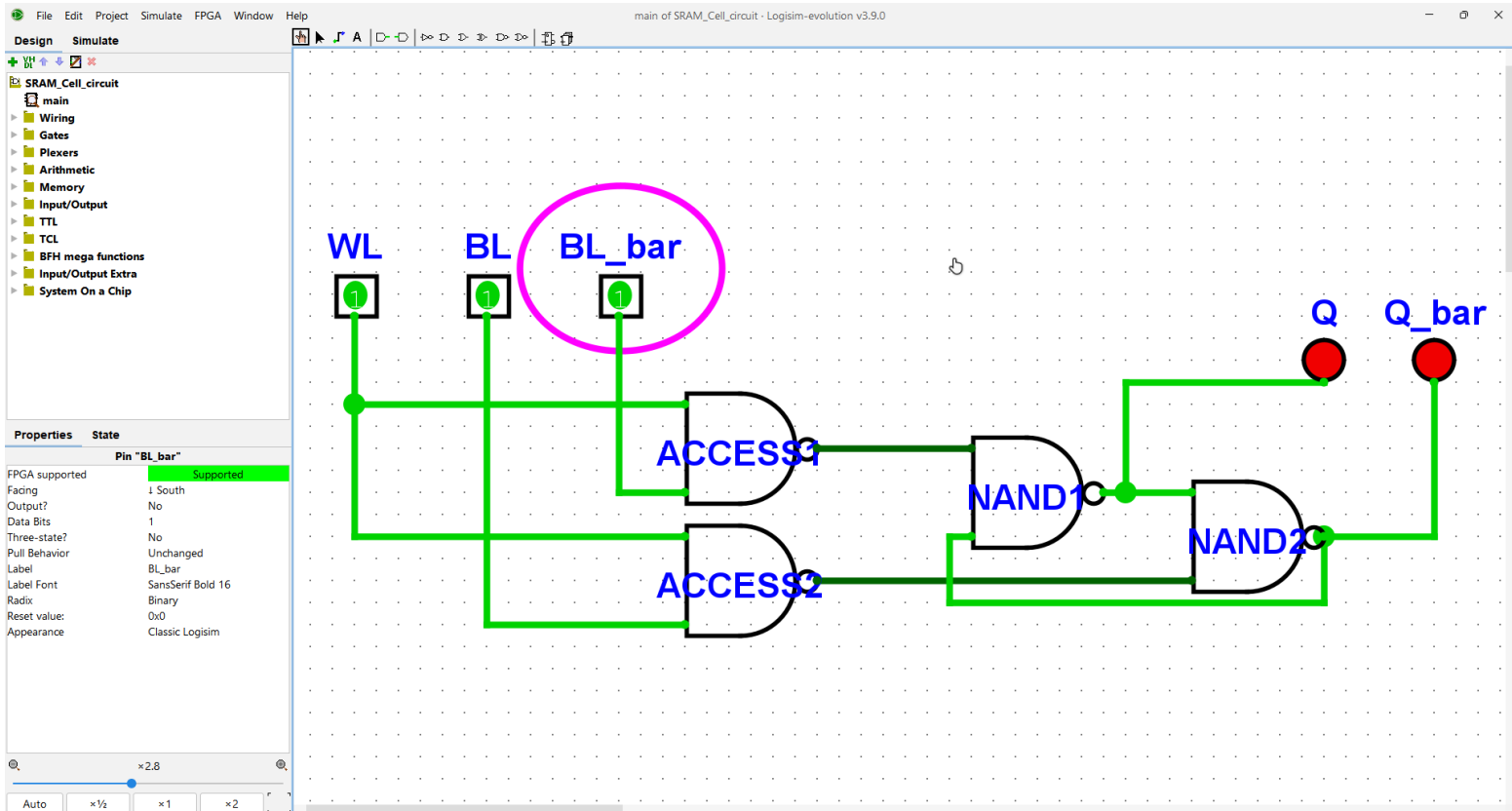


Figure 6: Invalid State, should be avoided