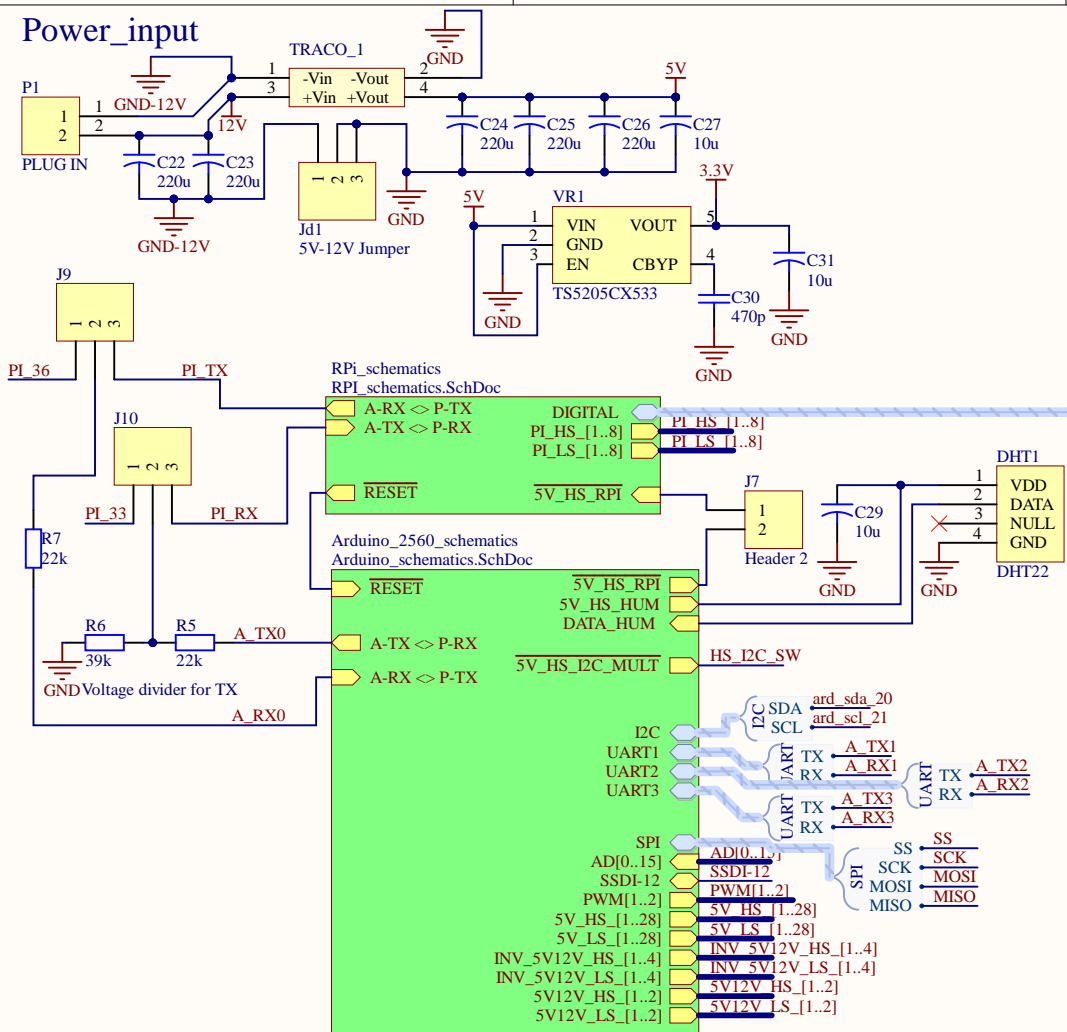
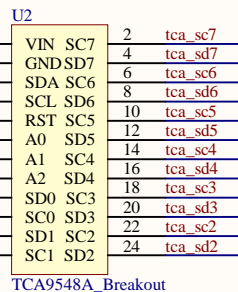
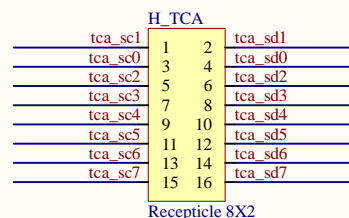
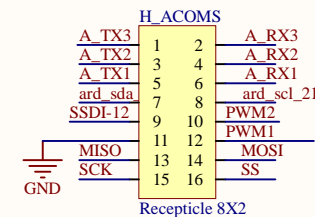
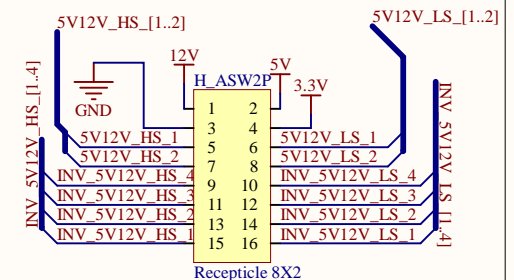
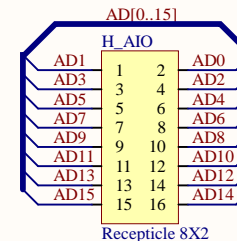
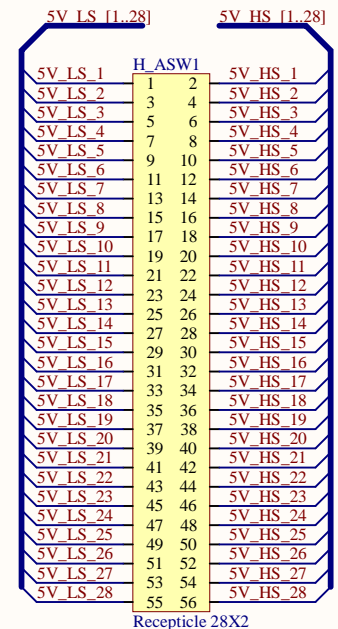
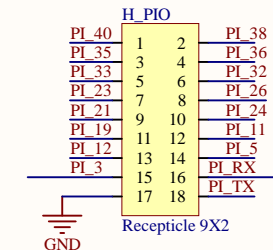
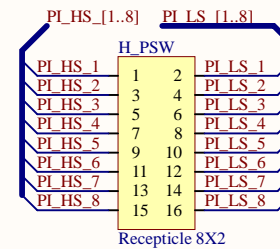



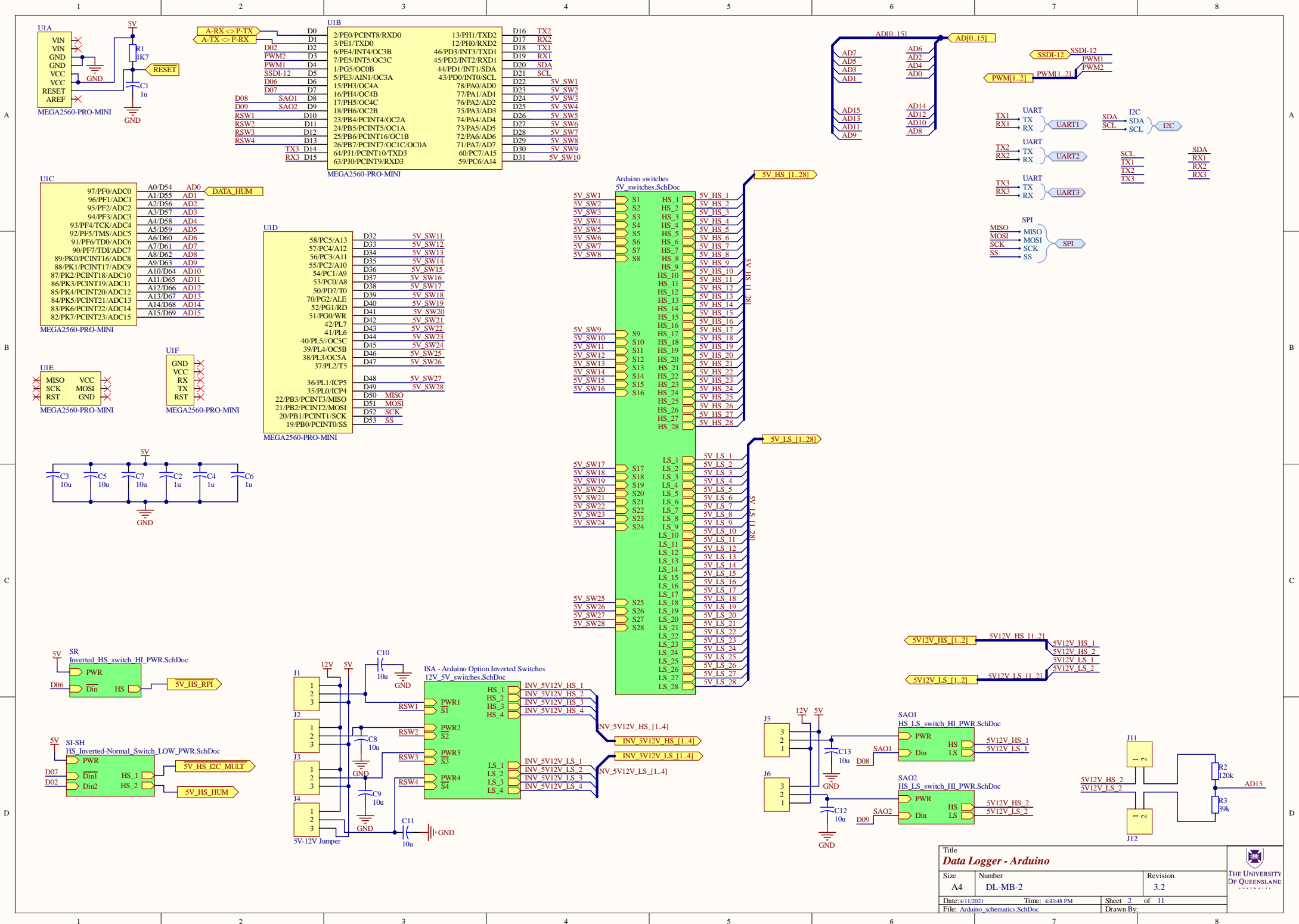
## Power\_input

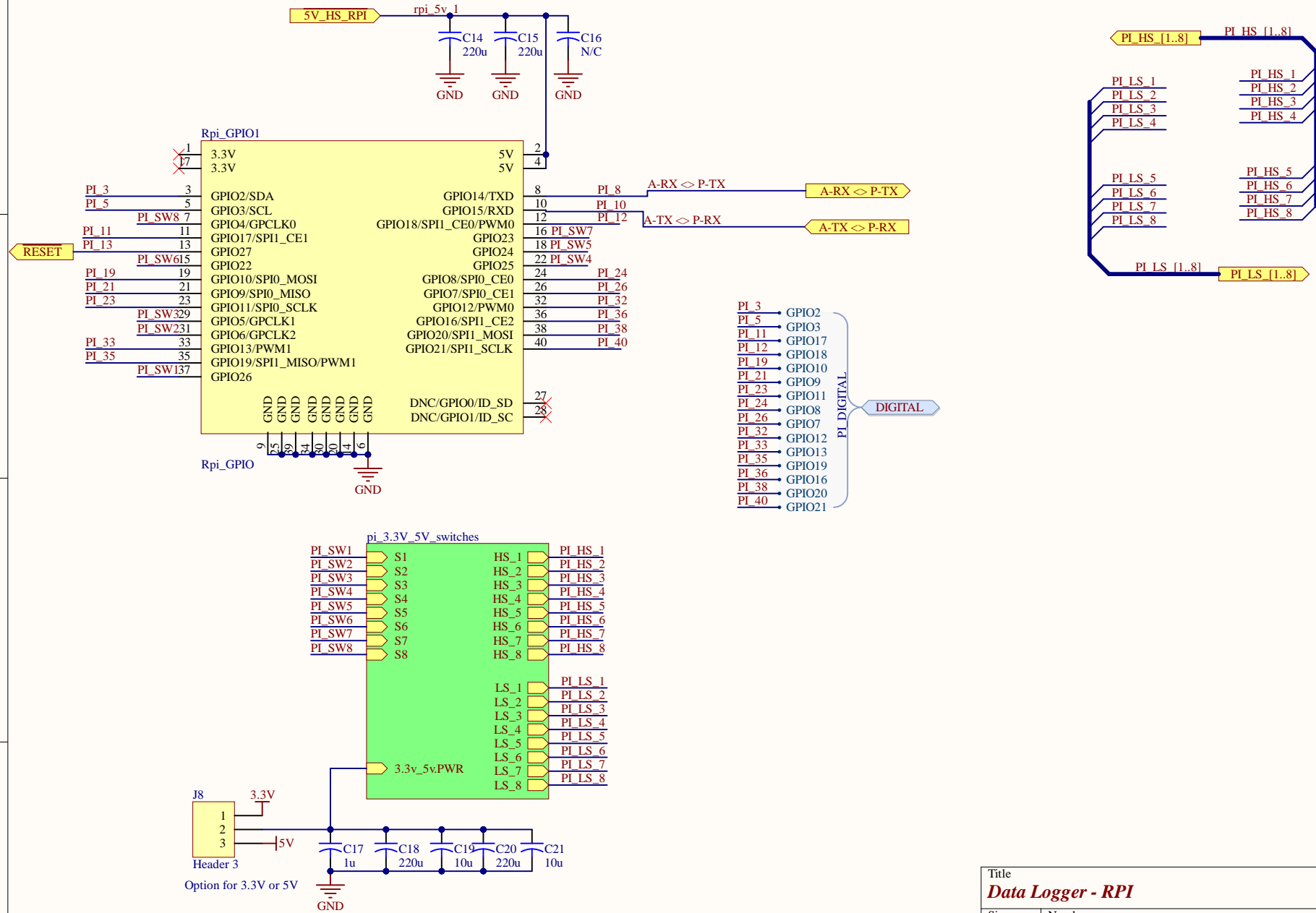



GPIO21 PL 40  
GPIO20 PL 38  
GPIO16 PL 36  
GPIO19 PL 35  
GPIO13 PL 33  
GPIO12 PL 32  
GPIO8 PL 26  
GPIO7 PL 24  
GPIO11 PL 23  
GPIO9 PL 21  
GPIO18 PL 12  
GPIO17 PL 11  
GPIO3 PL 5  
GPIO2 PL 3

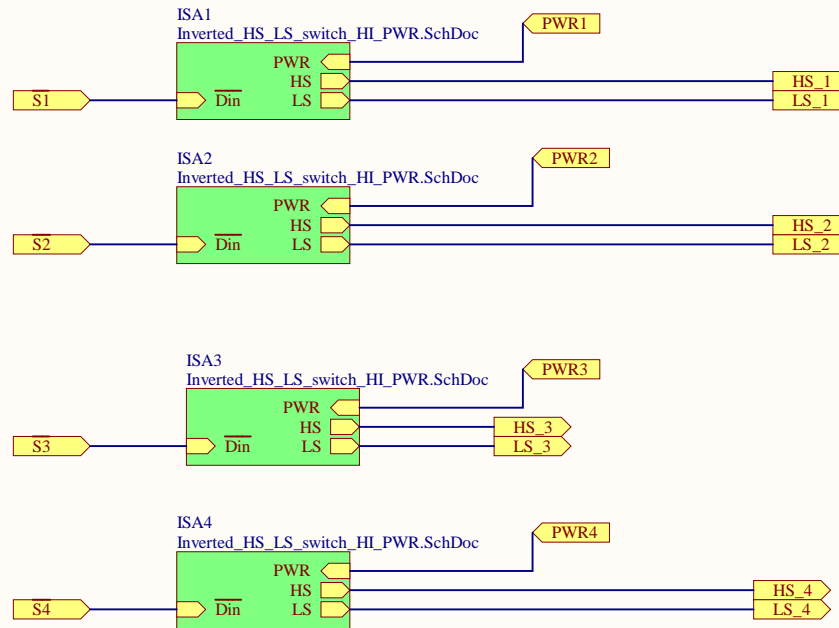



Title			 <b>THE UNIVERSITY OF QUEENSLAND</b> <small>A U S T R A L I A</small>
<i>Data Logger - Top Level</i>			
Size	Number	Revision	
A4	DL-MB-1	3.2	
Date: 4/11/2021		Time: 4:43:48 PM	
File: Top level.SchDoc		Sheet 1 of 11	
		Drawn By:	





Title				 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
<b>Data Logger - RPI</b>				
Size	Number		Revision	
A4	DL-MB-3		3.2	
Date: 4/11/2021		Time: 4:43:48 PM	Sheet 3 of 11	
File: RPI_schematics.SchDoc		Drawn By:		



Title <b>Data Logger - 12V,5V Switches</b>			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
Size A4	Number DL-MB-4	Revision 3.2	
Date: 4/11/2021		Time: 4:43:48 PM	
File: 12V_5V_switches.SchDoc		Sheet 4 of 11	
Drawn By:			

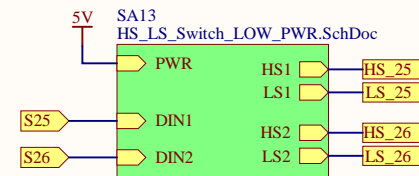
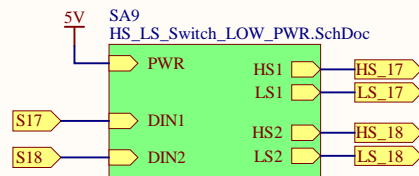
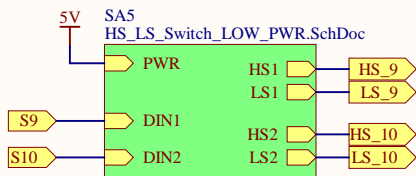
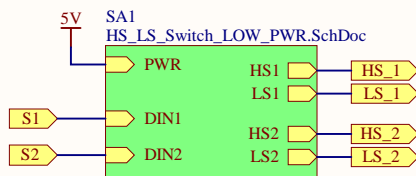
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2

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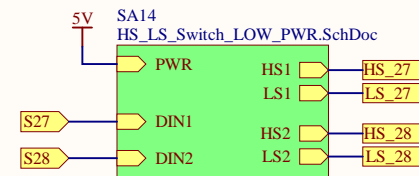
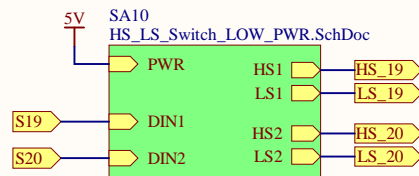
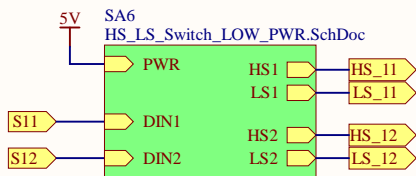
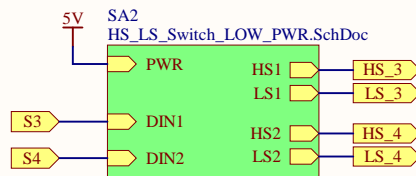
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A



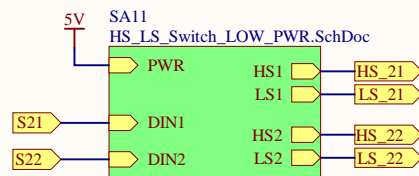
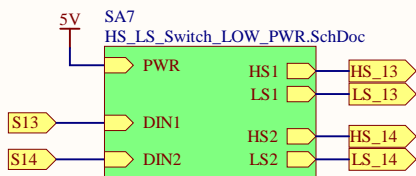
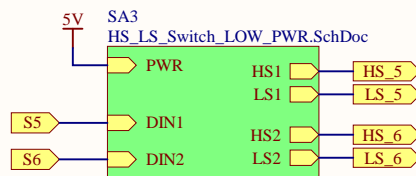
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B



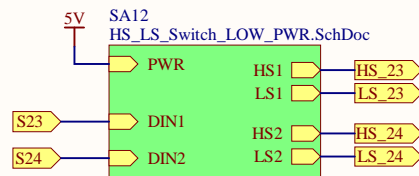
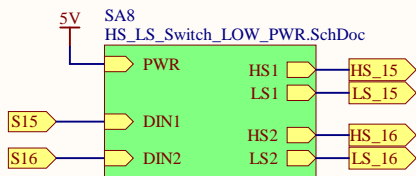
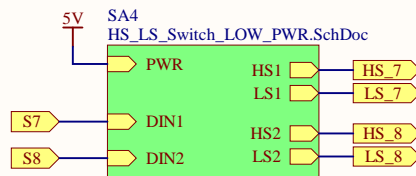
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C




C

D



D

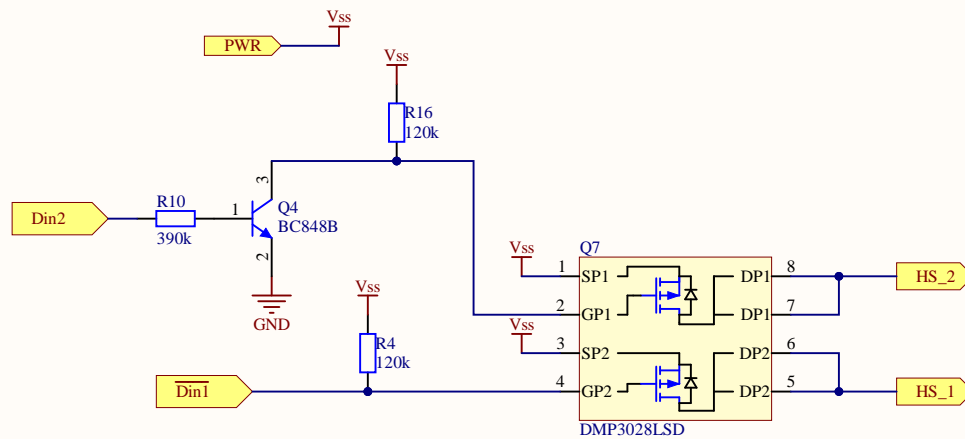
Title <b>Data Logger - 5V Switches</b>			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
Size A4	Number DL-MB-5	Revision 3.2	
Date: 4/11/2021		Time: 4:43:48 PM	
File: 5V_switches.SchDoc		Sheet 5 of 11	
Drawn By:			


1

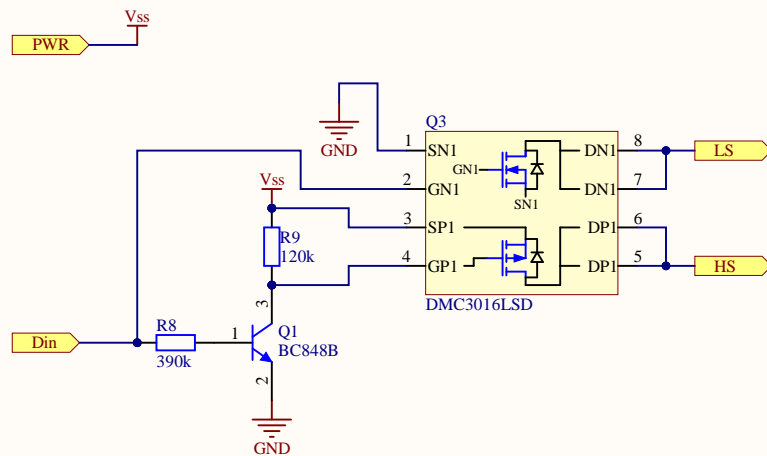
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3

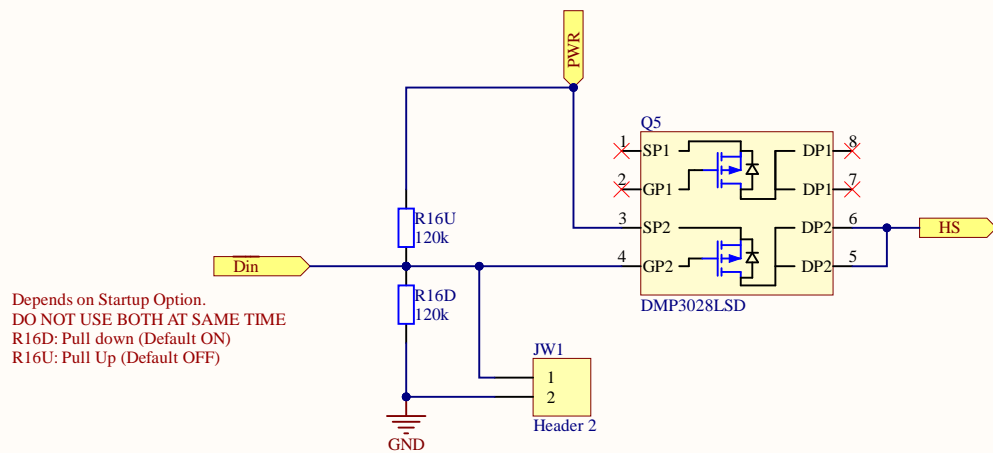
4




Title <b>Data Logger - Inverted HS, Normal Switches</b>			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
Size A4	Number DL-MB-6	Revision 3.2	
Date: 4/11/2021	Time: 4:43:48 PM	Sheet 6 of 11	
File: HS_Inverted-Normal_Switch_LOW_PWR.SchDoc		Drawn By:	

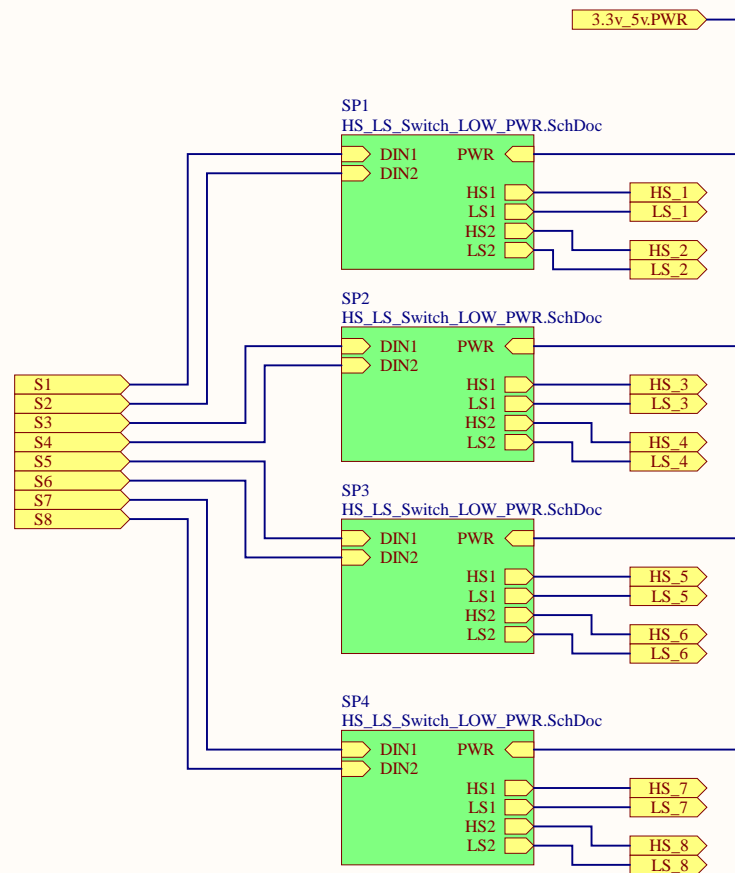



Title <b><i>Data Logger - HS LS HI Power Switches</i></b>			 <b>THE UNIVERSITY OF QUEENSLAND</b> <small>A U S T R A L I A</small>
Size <b>A4</b>	Number <b>DL-MB-7</b>	Revision <b>3.2</b>	
Date: 4/11/2021	Time: 4:43:48 PM	Sheet 7 of 11	
File: <a href="#">HS_LS_switch_HI_PWR.SchDoc</a>		Drawn By:	

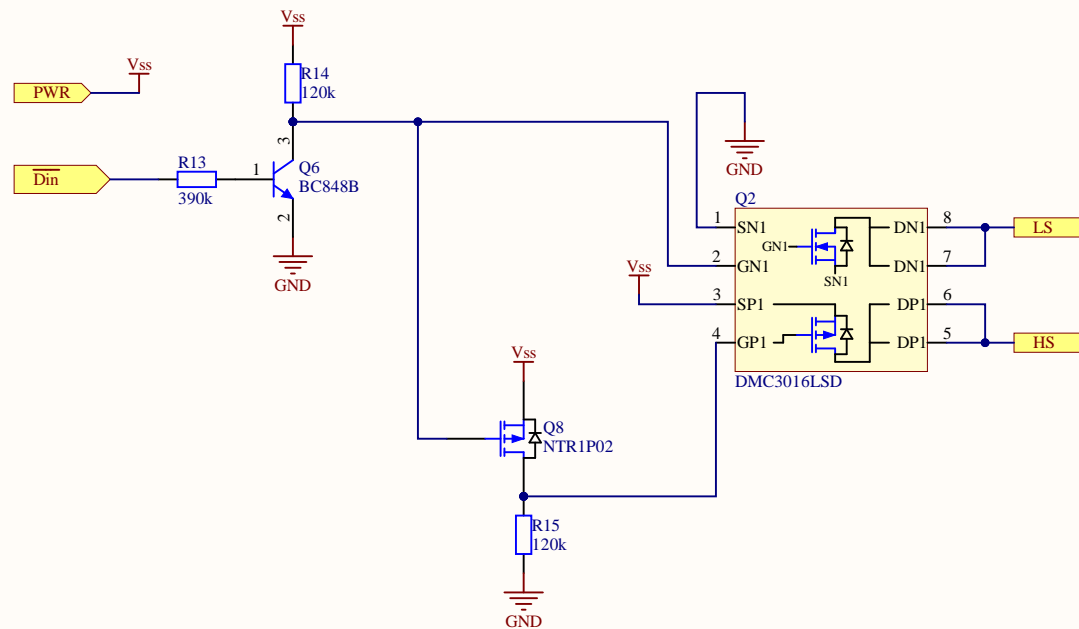



Title			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
<i>Data Logger - Inverted HS Hi Power Switch</i>			
Size	Number	Revision	
A4	DL-MB-8	3.2	
Date: 4/11/2021		Time: 4:43:48 PM	Sheet 8 of 11
File: Inverted_HS_switch_HI_PWR.SchDoc		Drawn By:	

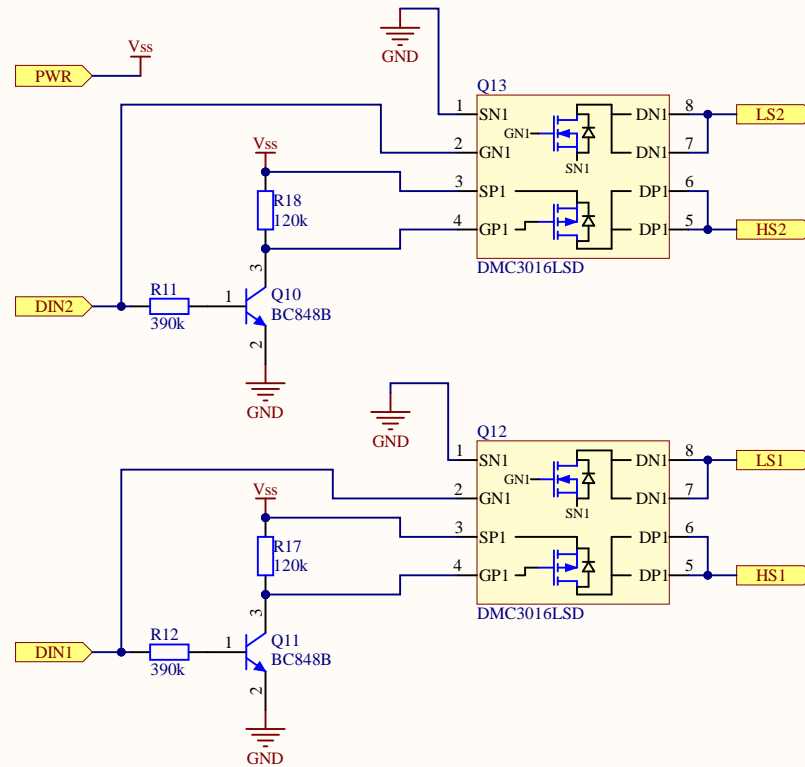





Title			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
<i>Data Logger - RPI 3.3V,5V Switches</i>			
Size	Number	Revision	
A4	DL-MB-9	3.2	
Date: 4/11/2021      Time: 4:43:48 PM		Sheet 9 of 11	
File: pi_3.3v_5v_switches.SchDoc		Drawn By:	



Title <b><i>Data Logger - Inverted HS LS Hi Power Switches</i></b>			 <b>THE UNIVERSITY OF QUEENSLAND</b> <small>AUSTRALIA</small>
Size <b>A4</b>	Number <b>DL-MB-10</b>	Revision <b>3.2</b>	
Date: 4/11/2021	Time: 4:43:48 PM	Sheet 10 of 11	
File: Inverted_HS_LS_switch_HI_PWR.SchDoc		Drawn By:	



Title <b>Data Logger - HS LS Low Power Switches</b>			 THE UNIVERSITY OF QUEENSLAND AUSTRALIA
Size A4	Number DL-MB-11	Revision 3.2	
Date: 4/11/2021	Time: 4:43:48 PM	Sheet 11 of 11	
File: HS_LS_Switch_LOW_PWR.SchDoc		Drawn By:	