ARM Cortex-A9 Pipeline

	Fetch	1
	Fetch	2
	Fetch	3
	Decode	4
	Decode	5
	Rename	6
	Dispatch	7
	Integer	8
Multiply	WriteBack	9
Multiply		10
WriteBack		11
		12
		13
		14
		15
		16
		17
		18
		19
		20
		21
		22
		23
		24

ARM Cortex-A15 Pipeline

						Up to 24 Stages NEON/FPU Pipeline	18 Stages Load/Store Pipeline	15 Stages Integer Pipeline
				Fetch	1			
				Fetch	2			
				Fetch	3			
				Fetch	4			
				Fetch	5			
				Decode	6			
				Rename	7			
				Decode Rename Dispatch	8			
				Decode Rename Dispatch	9			
				Decode Rename Dispatch	10			
				Decode Rename Dispatch	11			
				Decode Rename Dispatch	12			
Issue	Issue	Issue	Issue	Issue	13			
Load/Store	Multiply/Divide	NEON/FPU	Branch	Integer	14			
Load/Store	Multiply/Divide	NEON/FPU	WriteBack	WriteBack	15			
Load/Store	Multiply/Divide	NEON/FPU			16			
Load/Store	Multiply/Divide	NEON/FPU			17			
WriteBack	WriteBack	NEON/FPU			18			
		NEON/FPU			19			
		NEON/FPU			20			
		NEON/FPU			21			
		NEON/FPU			22			
		NEON/FPU			23			
		WriteBack			24			