

Microprocessors and Embedded Systems Project

In this project, you are going to follow a series of tutorials at the end of which you will have completed the implementation of a traffic lights simulation.

Follow the tutorials provided in the following link. There are a total of 23 tutorials therein presented by an expert in VHDL.

<https://vhdlwhiz.com/basic-vhdl-tutorials/>

Once you completed the project, you need to submit a report (max. 2 pages, .pdf format) describing the following:

1. The problem.
2. What you learned in brief.
3. Any ideas to improve.

Along with a report, you also need to submit a 3 minutes video (.mp4, .mpg or .mpeg format) of yourself describing the Project and making a short demonstration. **It is essential that you appear in the video by introducing yourself, i.e. show your face (you and your peer) and tell your name. Videos should not exceed 20MB.**

The project is to be completed by groups of 2 (two) students, not any more. Groups with a higher number of members will immediately get a grade of 0. Individual demonstrations are allowed.

During the demo, you need to demonstrate the general code structure, working simulation along with a signal analysis of different cases.

Project Deadline: 25 December, 2022, 23:00 through e-kampus.

(No extensions are permitted and no late submissions for any reason are accepted!)

Note on cheating: Any form of plagiarism or cheating will be penalized according to Ankara University regulations. It is understandable that the code will be very similar, since the source is single, but reports and videos must be original.